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1. Introduction

This document describes the compiler directives, library routines, and environment variables that collectively define the OpenACC™ Application Programming Interface (OpenACC API) for writing parallel programs in C, C++, and Fortran that run identified regions in parallel on multicore CPUs or attached accelerators. The method described provides a model for parallel programming that is portable across operating systems and various types of multicore CPUs and accelerators. The directives extend the ISO/ANSI standard C, C++, and Fortran base languages in a way that allows a programmer to migrate applications incrementally to parallel multicore and accelerator targets using standards-based C, C++, or Fortran.

The directives and programming model defined in this document allow programmers to create applications capable of using accelerators without the need to explicitly manage data or program transfers between a host and accelerator or to initiate accelerator startup and shutdown. Rather, these details are implicit in the programming model and are managed by the OpenACC API-enabled compilers and runtime environments. The programming model allows the programmer to augment information available to the compilers, including specification of data local to an accelerator, guidance on mapping of loops for parallel execution, and similar performance-related details.

1.1 Scope

This OpenACC API document covers only user-directed parallel and accelerator programming, where the user specifies the regions of a program to be targeted for parallel execution. The remainder of the program will be executed sequentially on the host. This document does not describe features or limitations of the host programming environment as a whole; it is limited to specification of loops and regions of code to be executed in parallel on a multicore CPU or an accelerator.

This document does not describe automatic detection of parallel regions or automatic offloading of regions of code to an accelerator by a compiler or other tool. This document does not describe splitting loops or code regions across multiple accelerators attached to a single host. While future compilers may allow for automatic parallelization or automatic offloading, or parallelizing across multiple accelerators of the same type, or across multiple accelerators of different types, these possibilities are not addressed in this document.

1.2 Execution Model

The execution model targeted by OpenACC API-enabled implementations is host-directed execution with an attached parallel accelerator, such as a GPU, or a multicore host with a host thread that initiates parallel execution on the multiple cores, thus treating the multicore CPU itself as a device. Much of a user application executes on a host thread. Compute intensive regions are offloaded to an accelerator or executed on the multiple host cores under control of a host thread. A device, either an attached accelerator or the multicore CPU, executes parallel regions, which typically contain work-sharing loops, kernels regions, which typically contain one or more loops that may be executed as kernels, or serial regions, which are blocks of sequential code. Even in accelerator-targeted regions, the host thread may orchestrate the execution by allocating memory on the accelerator device, initiating data transfer, sending the code to the accelerator, passing arguments to the compute region, queuing the accelerator code, waiting for completion, transferring results back to the host,
and deallocating memory. In most cases, the host can queue a sequence of operations to be executed on a device, one after the other.

Most current accelerators and many multicore CPUs support two or three levels of parallelism. Most accelerators and multicore CPUs support coarse-grain parallelism, which is fully parallel execution across execution units. There may be limited support for synchronization across coarse-grain parallel operations. Many accelerators and some CPUs also support fine-grain parallelism, often implemented as multiple threads of execution within a single execution unit, which are typically rapidly switched on the execution unit to tolerate long latency memory operations. Finally, most accelerators and CPUs also support SIMD or vector operations within each execution unit. The execution model exposes these multiple levels of parallelism on a device and the programmer is required to understand the difference between, for example, a fully parallel loop and a loop that is vectorizable but requires synchronization between statements. A fully parallel loop can be programmed for coarse-grain parallel execution. Loops with dependences must either be split to allow coarse-grain parallel execution, or be programmed to execute on a single execution unit using fine-grain parallelism, vector parallelism, or sequentially.

OpenACC exposes these three levels of parallelism via gang, worker, and vector parallelism. Gang parallelism is coarse-grain. A number of gangs will be launched on the accelerator. Worker parallelism is fine-grain. Each gang will have one or more workers. Vector parallelism is for SIMD or vector operations within a worker.

When executing a compute region on a device, one or more gangs are launched, each with one or more workers, where each worker may have vector execution capability with one or more vector lanes. The gangs start executing in gang-redundant mode (GR mode), meaning one vector lane of one worker in each gang executes the same code, redundantly. When the program reaches a loop or loop nest marked for gang-level work-sharing, the program starts to execute in gang-partitioned mode (GP mode), where the iterations of the loop or loops are partitioned across gangs for truly parallel execution, but still with only one worker per gang and one vector lane per worker active.

When only one worker is active, in either GR or GP mode, the program is in worker-single mode (WS mode). When only one vector lane is active, the program is in vector-single mode (VS mode).

If a gang reaches a loop or loop nest marked for worker-level work-sharing, the gang transitions to worker-partitioned mode (WP mode), which activates all the workers of the gang. The iterations of the loop or loops are partitioned across the workers of this gang. If the same loop is marked for both gang-partitioning and worker-partitioning, then the iterations of the loop are spread across all the workers of all the gangs. If a worker reaches a loop or loop nest marked for vector-level work-sharing, the worker will transition to vector-partitioned mode (VP mode). Similar to WP mode, the transition to VP mode activates all the vector lanes of the worker. The iterations of the loop or loops will be partitioned across the vector lanes using vector or SIMD operations. Again, a single loop may be marked for one, two, or all three of gang, worker, and vector parallelism, and the iterations of that loop will be spread across the gangs, workers, and vector lanes as appropriate.

The program starts executing with a single initial host thread, identified by a program counter and its stack. The initial host thread may spawn additional host threads, using OpenACC or another mechanism, such as with the OpenMP API. On a device, a single vector lane of a single worker of a single gang is called a device thread. When executing on an accelerator, a parallel execution context is created on the accelerator and may contain many such threads.

The user should not attempt to implement barrier synchronization, critical sections or locks across any of gang, worker, or vector parallelism. The execution model allows for an implementation that
executes some gangs to completion before starting to execute other gangs. This means that trying to implement synchronization between gangs is likely to fail. In particular, a barrier across gangs cannot be implemented in a portable fashion, since all gangs may not ever be active at the same time. Similarly, the execution model allows for an implementation that executes some workers within a gang or vector lanes within a worker to completion before starting other workers or vector lanes, or for some workers or vector lanes to be suspended until other workers or vector lanes complete. This means that trying to implement synchronization across workers or vector lanes is likely to fail.

In particular, implementing a barrier or critical section across workers or vector lanes using atomic operations and a busy-wait loop may never succeed, since the scheduler may suspend the worker or vector lane that owns the lock, and the worker or vector lane waiting on the lock can never complete.

Some devices, such as a multicore CPU, may also create and launch additional compute regions, allowing for nested parallelism. In that case, the OpenACC directives may be executed by a host thread or a device thread. This specification uses the term local thread or local memory to mean the thread that executes the directive, or the memory associated with that thread, whether that thread executes on the host or on the accelerator. The specification uses the term local device to mean the device on which the local thread is executing.

Most accelerators can operate asynchronously with respect to the host thread. Such devices have one or more activity queues. The host thread will enqueue operations onto the device activity queues, such as data transfers and procedure execution. After enqueuing the operation, the host thread can continue execution while the device operates independently and asynchronously. The host thread may query the device activity queue(s) and wait for all the operations in a queue to complete. Operations on a single device activity queue will complete before starting the next operation on the same queue; operations on different activity queues may be active simultaneously and may complete in any order.

1.3 Memory Model

The most significant difference between a host-only program and a host+accelerator program is that the memory on an accelerator may be discrete from host memory. This is the case with most current GPUs, for example. In this case, the host thread may not be able to read or write device memory directly because it is not mapped into the host thread’s virtual memory space. All data movement between host memory and accelerator memory must be performed by the host thread through system calls that explicitly move data between the separate memories, typically using direct memory access (DMA) transfers. Similarly, it is not valid to assume the accelerator can read or write host memory, though this is supported by some accelerators, often with significant performance penalty.

The concept of discrete host and accelerator memories is very apparent in low-level accelerator programming languages such as CUDA or OpenCL, in which data movement between the memories can dominate user code. In the OpenACC model, data movement between the memories can be implicit and managed by the compiler, based on directives from the programmer. However, the programmer must be aware of the potentially discrete memories for many reasons, including but not limited to:

- Memory bandwidth between host memory and accelerator memory determines the level of compute intensity required to effectively accelerate a given region of code.
- The user should be aware that a discrete device memory is usually significantly smaller than the host memory, prohibiting offloading regions of code that operate on very large amounts of data.
Host addresses stored to pointers on the host may only be valid on the host; addresses stored to pointers in accelerator memory may only be valid on that device. Explicitly transferring pointer values between host and accelerator memory is not advised. Dereferencing host pointers on an accelerator or dereferencing accelerator pointers on the host is likely to be invalid on such targets.

OpenACC exposes the discrete memories through the use of a device data environment. Device data has an explicit lifetime, from when it is allocated or created until it is deleted. If a device shares memory with the local thread, its device data environment will be shared with the local thread. In that case, the implementation need not create new copies of the data for the device and no data movement need be done. If a device has a discrete memory and shares no memory with the local thread, the implementation will allocate space in device memory and copy data between the local memory and device memory, as appropriate. The local thread may share some memory with a device and also have some memory that is not shared with that device. In that case, data in shared memory may be accessed by both the local thread and the device. Data not in shared memory will be copied to device memory as necessary.

Some accelerators implement a weak memory model. In particular, they do not support memory coherence between operations executed by different threads; even on the same execution unit, memory coherence is only guaranteed when the memory operations are separated by an explicit memory fence. Otherwise, if one thread updates a memory location and another reads the same location, or two threads store a value to the same location, the hardware may not guarantee the same result for each execution. While a compiler can detect some potential errors of this nature, it is nonetheless possible to write a compute region that produces inconsistent numerical results.

Similarly, some accelerators implement a weak memory model for memory shared between the host and the accelerator, or memory shared between multiple accelerators. Programmers need to be very careful that the program uses appropriate synchronization to ensure that an assignment or modification by a thread on any device to data in shared memory is complete and available before that data is used by another thread on the same or another device.

Some current accelerators have a software-managed cache, some have hardware managed caches, and most have hardware caches that can be used only in certain situations and are limited to read-only data. In low-level programming models such as CUDA or OpenCL languages, it is up to the programmer to manage these caches. In the OpenACC model, these caches are managed by the compiler with hints from the programmer in the form of directives.

## 1.4 Language Interoperability

The specification supports programs written using OpenACC in two or more of Fortran, C, and C++ languages. The parts of the program in any one base language will interoperate with the parts written in the other base languages as described here. In particular:

- Data made present in one base language on a device will be seen as present by any base language.

- A region that starts and ends in a procedure written in one base language may directly or indirectly call procedures written in any base language. The execution of those procedures are part of the region.
1.5 Conventions used in this document

Some terms are used in this specification that conflict with their usage as defined in the base languages. When there is potential confusion, the term will appear in the [Glossary]. Keywords and punctuation that are part of the actual specification will appear in typewriter font:

```
#pragma acc
```

Italic font is used where a keyword or other name must be used:

```
#pragma acc directive-name
```

For C and C++, `new-line` means the newline character at the end of a line:

```
#pragma acc directive-name new-line
```

Optional syntax is enclosed in square brackets; an option that may be repeated more than once is followed by ellipses:

```
#pragma acc directive-name [clause [...] clause]... new-line
```

In this spec, a `var` (in italics) is one of the following:

- a variable name (a scalar, array, or composite variable name);
- a subarray specification with subscript ranges;
- an array element;
- a member of a composite variable;
- a common block name between slashes.

Not all options are allowed in all clauses; the allowable options are clarified for each use of the term `var`. Unnamed common blocks (blank commons) are not permitted and common blocks of the same name must be of the same size in all scoping units as required by the Fortran standard.

To simplify the specification and convey appropriate constraint information, a `pqr-list` is a comma-separated list of `pqr` items. For example, an `int-expr-list` is a comma-separated list of one or more integer expressions, and a `var-list` is a comma-separated list of one or more `vars`. The one exception is `clause-list`, which is a list of one or more clauses optionally separated by commas.

```
#pragma acc directive-name [clause-list] new-line
```

In this spec, a `do loop` (in italics) is the `do` construct as defined by the Fortran standard. The `do-stmt` of the `do` construct must conform to one of the following forms:

```
do [label] do-var = lb, ub [, incr]
do concurrent [label] concurrent-header [concurrent-locality]
```

The `do-var` is a variable name and the `lb, ub, incr` are scalar integer expressions. A `do concurrent` is treated as if defining a loop for each index in the `concurrent-header`.

1.6 Organization of this document

The rest of this document is organized as follows:
Chapter 2 [Directives] describes the C, C++, and Fortran directives used to delineate accelerator regions and augment information available to the compiler for scheduling of loops and classification of data.

Chapter 3 [Runtime Library] defines user-callable functions and library routines to query the accelerator features and control behavior of accelerator-enabled programs at runtime.

Chapter 4 [Environment Variables] defines user-settable environment variables used to control behavior of accelerator-enabled programs at runtime.

Chapter 5 [Profiling Interface] describes the OpenACC interface for tools that can be used for profile and trace data collection.

Chapter 6 [Glossary] defines common terms used in this document.

Appendix A [Recommendations for Implementers] gives advice to implementers to support more portability across implementations and interoperability with other accelerator APIs.

1.7 References

Each language version inherits the limitations that remain in previous versions of the language in this list.


The use of the following C11 features may result in unspecified behavior.

- Threads
- Thread-local storage
- Parallel memory model
- Atomic


The use of the following C18 features may result in unspecified behavior.

- Thread related features

The use of the following C++11 features may result in unspecified behavior.

- Extern templates
- copy and rethrow exceptions
- memory model
- atomics
- move semantics
1.8 Changes from Version 1.0 to 2.0


The use of the following Fortran 2008 features may result in unspecified behavior.

- Coarrays
- Simply contiguous arrays rank remapping to rank>1 target
- Allocatable components of recursive type
- Polymorphic assignment

The use of the following Fortran 2018 features may result in unspecified behavior.

- Interoperability with C
  * C functions declared in ISO Fortran binding.h
  * Assumed rank
- All additional parallel/coarray features

- OpenMP Application Program Interface, version 5.0, November 2018
- NVIDIA CUDA® C Programming Guide, version 11.1.1, October 2020
- The OpenCL Specification, version 2.2, Khronos OpenCL Working Group, July 2019

1.8 Changes from Version 1.0 to 2.0

- _OPENACC value updated to 201306
- default (none) clause on parallel and kernels directives
- the implicit data attribute for scalars in parallel constructs has changed
- the implicit data attribute for scalars in loops with loop directives with the independent attribute has been clarified
- acc_async_sync and acc_async_noval values for the async clause
- Clarified the behavior of the reduction clause on a gang loop
- Clarified allowable loop nesting (gang may not appear inside worker, which may not appear within vector)
• wait clause on parallel, kernels and update directives
• async clause on the wait directive
• enter data and exit data directives
• Fortran common block names may now appear in many data clauses
• link clause for the declare directive
• the behavior of the declare directive for global data
• the behavior of a data clause with a C or C++ pointer variable has been clarified
• predefined data attributes
• support for multidimensional dynamic C/C++ arrays
• tile and auto loop clauses
• update self introduced as a preferred synonym for update host
• routine directive and support for separate compilation
• device_type clause and support for multiple device types
• nested parallelism using parallel or kernels region containing another parallel or kernels region
• atomic constructs
• new concepts: gang-redundant, gang-partitioned; worker-single, worker-partitioned; vector-single, vector-partitioned; thread
• new API routines:
  – acc_wait, acc_wait_all instead of acc_async_wait and acc_async_wait_all
  – acc_wait_async
  – acc_copyin, acc_present_or_copyin
  – acc_create, acc_present_or_create
  – acc_copyout, acc_delete
  – acc_map_data, acc_unmap_data
  – acc_deviceptr, acc_hostptr
  – acc_is_present
  – acc_memcpy_to_device, acc_memcpy_from_device
  – acc_update_device, acc_update_self
• defined behavior with multiple host threads, such as with OpenMP
• recommendations for specific implementations
• clarified that no arguments are allowed on the vector clause in a parallel region
1.9 Corrections in the August 2013 document

- corrected the atomic capture syntax for C/C++
- fixed the name of the acc_wait and acc_wait_all procedures
- fixed description of the acc_hostptr procedure

1.10 Changes from Version 2.0 to 2.5

- The _OPENACC value was updated to 201510; see Section 2.2 Conditional Compilation.
- The num_gangs, num_workers, and vector_length clauses are now allowed on the kernels construct; see Section 2.5.3 Kernels Construct.
- Reduction on C++ class members, array elements, and struct elements are explicitly disallowed; see Section 2.5.14 reduction clause.
- Reference counting is now used to manage the correspondence and lifetime of device data; see Section 2.6.7 Reference Counters.
- The behavior of the exit data directive has changed to decrement the dynamic reference counter. A new optional finalize clause was added to set the dynamic reference counter to zero. See Section 2.6.6 Enter Data and Exit Data Directives.
- The copy, copyin, copyout, and create data clauses were changed to behave like present_or_copy, etc. The present_or_copy, pcopy, present_or_copyin, pcopyin, present_or_copyout, pcopyout, present_or_create, and p create data clauses are no longer needed, though will be accepted for compatibility; see Section 2.7 Data Clauses.
- Reductions on orphaned gang loops are explicitly disallowed; see Section 2.9 Loop Construct.
- The description of the loop auto clause has changed; see Section 2.9.7 auto clause.
- Text was added to the private clause on a loop construct to clarify that a copy is made for each gang or worker or vector lane, not each thread; see Section 2.9.10 private clause.
- The description of the reduction clause on a loop construct was corrected; see Section 2.9.11 reduction clause.
- A restriction was added to the cache clause that all references to that variable must lie within the region being cached; see Section 2.10 Cache Directive.
- Text was added to the private and reduction clauses on a combined construct to clarify that they act like private and reduction on the loop, not private and reduction on the parallel or reduction on the kernels; see Section 2.11 Combined Constructs.
- The declare create directive with a Fortran allocatable has new behavior; see Section 2.13.2 create clause.
- A new if_present clause was added to the update directive, which changes the behavior when data is not present from a runtime error to a no-op; see Section 2.14.4 Update Directive.
1.11 Changes from Version 2.5 to 2.6

- The _OPENACC value was updated to 201711.
- A new serial compute construct was added. See Section 2.5.2 Serial Construct.
- A new runtime API query routine was added. acc_get_property may be called from the host and returns properties about any device. See Section 3.2.6.
- The text has clarified that if a variable is in a reduction which spans two or more nested loops, each loop directive on any of those loops must have a reduction clause that contains the variable; see Section 2.9.11 reduction clause.
- An optional if or if_present clause is now allowed on the host_data construct. See Section 2.8 Host Data Construct.
- A new no_create data clause is now allowed on compute and data constructs. See Section 2.7.10 no_create clause.
- The behavior of Fortran optional arguments in data clauses and in routine calls has been specified; see Section 2.17.1 Optional Arguments.
- The descriptions of some of the Fortran versions of the runtime library routines were simplified; see Section 3.2 Runtime Library Routines.
- To allow for manual deep copy of data structures with pointers, new attach and detach behavior was added to the data clauses, new attach and detach clauses were added, and matching acc_attach and acc_detach runtime API routines were added; see Sections 2.6.4, 2.7.12, 2.7.13 and 3.2.40, 3.2.41.
The Intel Coprocessor Offload Interface target and API routine sections were removed from the Section A Recommendations for Implementers since Intel no longer produces this product.

1.12 Changes from Version 2.6 to 2.7

- The _OPENACC value was updated to 201811.
- The specification allows for hosts that share some memory with the device but not all memory. The wording in the text now discusses whether local thread data is in shared memory (memory shared between the local thread and the device) or discrete memory (local thread memory that is not shared with the device), instead of shared-memory devices and non-shared memory devices. See Sections 1.3 Memory Model and 2.6 Data Environment.
- The text was clarified to allow an implementation that treats a multicore CPU as a device, either an additional device or the only device.
- The readonly modifier was added to the copyin data clause and cache directive. See Sections 2.7.7 and 2.10.
- The local device was defined; see Section 1.2 Execution Model and the Glossary.
- The term var is used more consistently throughout the specification to mean a variable name, array name, subarray specification, array element, composite variable member, or Fortran common block name between slashes. Some uses of var allow only a subset of these options, and those limitations are given in those cases.
- The self clause was added to the compute constructs; see Section 2.5.6 self clause.
- The appearance of a reduction clause on a compute construct implies a copy clause for each reduction variable; see Sections 2.5.14 reduction clause and 2.11 Combined Constructs.
- The default (none) and default (present) clauses were added to the data construct; see Section 2.6.5 Data Construct.
- Data is defined to be present based on the values of the structured and dynamic reference counters; see Section 2.6.7 Reference Counters and the Glossary.
- The interaction of the acc_map_data and acc_unmap_data runtime API calls on the present counters is defined; see Section 2.7.2 3.2.32 and 3.2.33.
- A restriction clarifying that a host_data construct must have at least one use_device clause was added.
- Arrays, subarrays and composite variables are now allowed in reduction clauses; see Sections 2.9.11 reduction clause and 2.5.14 reduction clause.
- Changed behavior of ICVs to support nested compute regions and host as a device semantics. See Section 2.3.

1.13 Changes from Version 2.7 to 3.0

- Updated _OPENACC value to 201911.
- Updated the normative references to the most recent standards for all base languages. See Section 1.7.
• Changed the text to clarify uses and limitations of the `device_type` clause and added examples; see Section 2.4.

• Clarified the conflict between the implicit `copy` clause for variables in a `reduction` clause and the implicit `firstprivate` for scalar variables not in a data clause but used in a `parallel` or `serial` construct; see Sections 2.5.1 and 2.5.2.

• Required at least one data clause on a `data` construct, an `enter data` directive, or an `exit data` directive; see Sections 2.6.5 and 2.6.6.

• Added text describing how a C++ `lambda` invoked in a compute region and the variables captured by the `lambda` are handled; see Section 2.6.2.

• Added a `zero` modifier to `create` and `copyout` data clauses that zeros the device memory after it is allocated; see Sections 2.7.8 and 2.7.9.

• Added a new restriction on the `loop` directive allowing only one of the `seq`, `independent`, and `auto` clauses to appear; see Section 2.9.

• Added a new restriction on the `loop` directive disallowing a `gang`, `worker`, or `vector` clause to appear if a `seq` clause appears; see Section 2.9.

• Allowed variables to be modified in an atomic region in a loop where the iterations must otherwise be data independent, such as loops with a `loop independent` clause or a `loop` directive in a `parallel` construct; see Sections 2.9.2, 2.9.3, 2.9.4, and 2.9.6.

• Clarified the behavior of the `auto` and `independent` clauses on the `loop` directive; see Sections 2.9.7 and 2.9.6.

• Clarified that an orphaned `loop` construct, or a `loop` construct in a `parallel` construct with no `auto` or `seq` clauses is treated as if an `independent` clause appears; see Section 2.9.6.

• For a variable in a `reduction` clause, clarified when the update to the original variable is complete, and added examples; see Section 2.9.11.

• Clarified that a variable in an orphaned `reduction` clause must be private; see Section 2.9.11.

• Required at least one clause on a `declare` directive; see Section 2.13.

• Added an `if` clause to `init`, `shutdown`, `set`, and `wait` directives; see Sections 2.14.1, 2.14.2, 2.14.3, and 2.16.3.

• Required at least one clause on a `set` directive; see Section 2.14.3.

• Added a `devnum` modifier to the `wait` directive and clause to specify a device to which the wait operation applies; see Section 2.16.3.

• Allowed a `routine` directive to include a C++ `lambda` name or to appear before a C++ `lambda` definition, and defined implicit `routine` directive behavior when a C++ `lambda` is called in a compute region or an `accelerator routine`; see Section 2.15.

• Added runtime API routine `acc_memcpy_d2d` for copying data directly between two device arrays on the same or different devices; see Section 3.2.42.

• Defined the values for the `acc_construct_t` and `acc_device_api` enumerations for cross-implementation compatibility; see Sections 5.2.2 and 5.2.3.
1.14 Changes from Version 3.0 to 3.1

- Updated _OPENACC value to 202011.
- Clarified that Fortran blank common blocks are not permitted and that same-named common blocks must have the same size. See Section 1.5.
- Clarified that a parallel construct’s block is considered to start in gang-redundant mode even if there’s just a single gang. See Section 2.5.1.
- Added support for the Fortran BLOCK construct. See Sections 2.5.1, 2.5.3, 2.6.1, 2.6.5, 2.8, 2.13, and 6.
- Defined the serial construct in terms of the parallel construct to improve readability. Instead of defining it in terms of clauses num_gangs(1) num_workers(1) vector_length(1), defined the serial construct as executing with a single gang of a single worker with a vector length of one. See Section 2.5.2.
- Consolidated compute construct restrictions into a new section to improve readability. See Section 2.5.4.
- Clarified that a default clause may appear at most once on a compute construct. See Section 2.5.15.
- Consolidated discussions of implicit data attributes on compute and combined constructs into a separate section. Clarified the conditions under which each data attribute is implied. See Section 2.6.2.
- Added a restriction that certain loop reduction variables must have explicit data clauses on their parent compute constructs. This change addresses portability across existing OpenACC implementations. See Sections 2.6.2 and A.3.2.
- Restored the OpenACC 2.5 behavior of the present, copy, copyin, copyout, create, no_create, delete data clauses at exit from a region, or on an exit data directive, as applicable, and create clause at exit from an implicit data region where a declare directive appears, and acc_copyout, acc_delete routines, such that no action is taken if the appropriate reference counter is zero, instead of a runtime error being issued if data is not present. See Sections 2.7.5, 2.7.6, 2.7.7, 2.7.8, 2.7.9, 2.7.10, 2.7.11, 2.7.12, 2.7.28, and 3.2.29.
- Clarified restrictions on loop forms that can be associated with loop constructs, including the case of C++ range-based for loops. See Section 2.9.
- Specified where gang clauses are implied on loop constructs. This change standardizes behavior of existing OpenACC implementations. See Section 2.9.2.
- Corrected C/C++ syntax for atomic capture with a structured block. See Section 2.12.
- Added the behavior of the Fortran do concurrent construct. See Section 2.17.2.
• Changed the Fortran run-time procedures: `acc_device_property` has been renamed to `acc_device_property_kind` and `acc_get_property` uses a different integer kind for the result. See Section 3.2.

• Added or changed argument names for the Runtime Library routines to be descriptive and consistent. This mostly impacts Fortran programs, which can pass arguments by name. See Section 3.2.

• Replaced composite variable by aggregate variable in `reduction`, `default`, and `private` clauses and in implicitly determined data attributes; the new wording also includes Fortran character and allocatable(pointer) variables. See glossary in Section 6.

1.15 Topics Deferred For a Future Revision

The following topics are under discussion for a future revision. Some of these are known to be important, while others will depend on feedback from users. Readers who have feedback or want to participate may send email to feedback@openacc.org. No promises are made or implied that all these items will be available in a future revision.

• Directives to define implicit `deep copy` behavior for pointer-based data structures.

• Defined behavior when data in data clauses on a directive are aliases of each other.

• Clarifying when data becomes present or not present on the device for `enter data` or `exit data` directives with an `async` clause.

• Clarifying the behavior of Fortran `pointer` variables in data clauses.

• Allowing Fortran `pointer` variables to appear in `deviceptr` clauses.

• Defining the behavior of data clauses and runtime API routines for pointers that are `NULL`, or Fortran `pointer` variables that are not associated, or Fortran `allocatable` variables that are not allocated.

• Support for attaching C/C++ pointers that point to an address past the end of a memory region.

• Fully defined interaction with multiple host threads.

• Optionally removing the synchronization or barrier at the end of vector and worker loops.

• Allowing an `if` clause after a `device_type` clause.

• A `shared` clause (or something similar) for the loop directive.

• Better support for multiple devices from a single thread, whether of the same type or of different types.

• An `auto` construct (by some name), to allow `kernels`-like auto-parallelization behavior inside `parallel` constructs or accelerator routines.

• A `begin declare ... end declare` construct that behaves like putting any global variables declared inside the construct in a `declare` clause.

• Defining the behavior of additional parallelism constructs in the base languages when used inside a `compute` construct or accelerator routine.

• Optimization directives or clauses, such as an `unroll` directive or clause.
• Define runtime error behavior and allowing a user-defined error handlers.

• Extended reductions.

• Fortran bindings for all the API routines.

• A **linear** clause for the **loop** directive.

• Allowing two or more of **gang**, **worker**, **vector**, or **seq** clause on an **acc routine** directive.

• Requiring the implementation to imply an **acc routine** directive for procedures called within a compute construct or accelerator routine.

• A single list of all devices of all types, including the host device.

• A memory allocation API for specific types of memory, including device memory, host pinned memory, and unified memory.

• Bindings to other languages.
2. Directives

This chapter describes the syntax and behavior of the OpenACC directives. In C and C++, OpenACC directives are specified using the \texttt{#pragma} mechanism provided by the language. In Fortran, OpenACC directives are specified using special comments that are identified by a unique sentinel. Compilers will typically ignore OpenACC directives if support is disabled or not provided.

2.1 Directive Format

In C and C++, OpenACC directives are specified with the \texttt{#pragma} mechanism. The syntax of an OpenACC directive is:

\begin{verbatim}
#pragma acc directive-name [clause-list] new-line
\end{verbatim}

Each directive starts with \texttt{#pragma acc}. The remainder of the directive follows the C and C++ conventions for pragmas. White space may be used before and after the \texttt{#}; white space may be required to separate words in a directive. Preprocessing tokens following the \texttt{#pragma acc} are subject to macro replacement. Directives are case-sensitive.

In Fortran, OpenACC directives are specified in free-form source files as

\begin{verbatim}
!$acc directive-name [clause-list]
\end{verbatim}

The comment prefix (\texttt{!}) may appear in any column, but may only be preceded by white space (spaces and tabs). The sentinel (\texttt{!$acc}) must appear as a single word, with no intervening white space. Line length, white space, and continuation rules apply to the directive line. Initial directive lines must have white space after the sentinel. Continued directive lines must have an ampersand (\texttt{&}) as the last nonblank character on the line, prior to any comment placed in the directive. Continuation directive lines must begin with the sentinel (possibly preceded by white space) and may have an ampersand as the first non-white space character after the sentinel. Comments may appear on the same line as a directive, starting with an exclamation point and extending to the end of the line. If the first nonblank character after the sentinel is an exclamation point, the line is ignored.

In Fortran fixed-form source files, OpenACC directives are specified as one of

\begin{verbatim}
!$acc directive-name [clause-list]
c$acc directive-name [clause-list]
*$acc directive-name [clause-list]
\end{verbatim}

The sentinel (\texttt{!$acc, c$acc, or *$acc}) must occupy columns 1-5. Fixed form line length, white space, continuation, and column rules apply to the directive line. Initial directive lines must have a space or zero in column 6, and continuation directive lines must have a character other than a space or zero in column 6. Comments may appear on the same line as a directive, starting with an exclamation point on or after column 7 and continuing to the end of the line.

In Fortran, directives are case-insensitive. Directives cannot be embedded within continued statements, and statements must not be embedded within continued directives. In this document, free form is used for all Fortran OpenACC directive examples.

Only one \texttt{directive-name} can appear per directive, except that a combined directive name is considered a single \texttt{directive-name}. The order in which clauses appear is not significant unless otherwise
specified. Clauses may be repeated unless otherwise specified. Some clauses have an argument that can contain a list.

2.2 Conditional Compilation

The _OPENACC macro name is defined to have a value yyyyymm where yyyy is the year and mm is the month designation of the version of the OpenACC directives supported by the implementation. This macro must be defined by a compiler only when OpenACC directives are enabled. The version described here is 202011.

2.3 Internal Control Variables

An OpenACC implementation acts as if there are internal control variables (ICVs) that control the behavior of the program. These ICVs are initialized by the implementation, and may be given values through environment variables and through calls to OpenACC API routines. The program can retrieve values through calls to OpenACC API routines.

The ICVs are:

- *acc-current-device-type-var* - controls which type of device is used.
- *acc-current-device-num-var* - controls which device of the selected type is used.
- *acc-default-async-var* - controls which asynchronous queue is used when none appears in an async clause.

2.3.1 Modifying and Retrieving ICV Values

The following table shows environment variables or procedures to modify the values of the internal control variables, and procedures to retrieve the values:

<table>
<thead>
<tr>
<th>ICV</th>
<th>Ways to modify values</th>
<th>Way to retrieve value</th>
</tr>
</thead>
<tbody>
<tr>
<td>acc-current-device-type-var</td>
<td>acc_set_device_type</td>
<td>acc_get_device_type</td>
</tr>
<tr>
<td></td>
<td>set_device_type</td>
<td></td>
</tr>
<tr>
<td></td>
<td>ACC_DEVICE_TYPE</td>
<td></td>
</tr>
<tr>
<td>acc-current-device-num-var</td>
<td>acc_set_device_num</td>
<td>acc_get_device_num</td>
</tr>
<tr>
<td></td>
<td>set_device_num</td>
<td></td>
</tr>
<tr>
<td></td>
<td>ACC_DEVICE_NUM</td>
<td></td>
</tr>
<tr>
<td>acc-default-async-var</td>
<td>acc_set_default_async</td>
<td>acc_get_default_async</td>
</tr>
<tr>
<td></td>
<td>set_default_async</td>
<td></td>
</tr>
</tbody>
</table>

The initial values are implementation-defined. After initial values are assigned, but before any OpenACC construct or API routine is executed, the values of any environment variables that were set by the user are read and the associated ICVs are modified accordingly. There is one copy of each ICV for each host thread that is not generated by a compute construct. For threads that are generated by a compute construct the initial value for each ICV is inherited from the local thread. The behavior for each ICV is as if there is a copy for each thread. If an ICV is modified, then a unique copy of that ICV must be created for the modifying thread.
2.4 Device-Specific Clauses

OpenACC directives can specify different clauses or clause arguments for different devices using the `device_type` clause. Clauses that precede any `device_type` clause are default clauses. Clauses that follow a `device_type` clause up to the end of the directive or up to the next `device_type` clause are device-specific clauses for the device types specified in the `device_type` argument. For each directive, only certain clauses may be device-specific clauses. If a directive has at least one device-specific clause, it is device-dependent, and otherwise it is device-independent.

The argument to the `device_type` clause is a comma-separated list of one or more device architecture name identifiers, or an asterisk. An asterisk indicates all device types that are not named in any other `device_type` clause on that directive. A single directive may have one or several `device_type` clauses. The `device_type` clauses may appear in any order.

Except where otherwise noted, the rest of this document describes device-independent directives, on which all clauses apply when compiling for any device type. When compiling a device-dependent directive for a particular device type, the directive is treated as if the only clauses that appear are (a) the clauses specific to that device type and (b) all default clauses for which there are no like-named clauses specific to that device type. If, for any device type, the resulting directive is non-conforming, then the original directive is non-conforming.

The supported device types are implementation-defined. Depending on the implementation and the compiling environment, an implementation may support only a single device type, or may support multiple device types but only one at a time, or may support multiple device types in a single compilation.

A device architecture name may be generic, such as a vendor, or more specific, such as a particular generation of device; see Appendix A [Recommendations for Implementers] for recommended names. When compiling for a particular device, the implementation will use the clauses associated with the `device_type` clause that specifies the most specific architecture name that applies for this device; clauses associated with any other `device_type` clause are ignored. In this context, the asterisk is the least specific architecture name.

Syntax

The syntax of the `device_type` clause is

```
device_type( * )
device_type( device-type-list )
```

The `device_type` clause may be abbreviated to `dtype`.

Examples

- On the following directive, `worker` appears as a device-specific clause for devices of type `foo`, but `gang` appears as a default clause and so applies to all device types, including `foo`.

```
#pragma acc loop gang device_type(foo) worker
```
• The first directive below is identical to the previous directive except that loop is replaced with routine. Unlike loop, routine does not permit gang to appear with worker, but both apply for device type foo, so the directive is non-conforming. The second directive below is conforming because gang there applies to all device types except foo.

// non-conforming: gang and worker are not permitted together
#pragma acc routine gang device_type(foo) worker

// conforming: gang and worker apply to different device types
#pragma acc routine device_type(foo) worker \
   device_type(*) gang

• On the directive below, the value of num_gangs is 4 for device type foo, but it is 2 for all other device types, including bar. That is, foo has a device-specific num_gangs clause, so the default num_gangs clause does not apply to foo.

!$acc parallel num_gangs(2) &
!$acc device_type(foo) num_gangs(4) &
!$acc device_type(bar) num_workers(8)

• The directive below is the same as the previous directive except that num_gangs(2) has moved after device_type(*) and so now does not apply to foo or bar.

!$acc parallel device_type(*) num_gangs(2) &
!$acc device_type(foo) num_gangs(4) &
!$acc device_type(bar) num_workers(8)

2.5 Compute Constructs

2.5.1 Parallel Construct

Summary
This fundamental construct starts parallel execution on the current device.

Syntax
In C and C++, the syntax of the OpenACC parallel construct is

#pragma acc parallel [clause-list] new-line
structured block

and in Fortran, the syntax is

!$acc parallel [ clause-list ]
structured block
!$acc end parallel

or

!$acc parallel [ clause-list ]
block construct
The OpenACC® API 2.5. Compute Constructs

$\text{end parallel}$

where \textit{clause} is one of the following:

- \texttt{async} [	exttt{( int-expr )}]
- \texttt{wait} [	exttt{( int-expr-list )}]
- \texttt{num\_gangs} (\texttt{ int-expr })
- \texttt{num\_workers} (\texttt{ int-expr })
- \texttt{vector\_length} (\texttt{ int-expr })
- \texttt{device\_type} (\texttt{ device-type-list })
- \texttt{if} (\texttt{ condition })
- \texttt{self} [ (\texttt{ condition )}]
- \texttt{reduction} (\texttt{ operator : var-list })
- \texttt{copy} (\texttt{ var-list })
- \texttt{copyin} (\texttt{ readonly: }\texttt{ var-list })
- \texttt{copyout} (\texttt{ zero: }\texttt{ var-list })
- \texttt{create} (\texttt{ zero: }\texttt{ var-list })
- \texttt{no\_create} (\texttt{ var-list })
- \texttt{present} (\texttt{ var-list })
- \texttt{deviceptr} (\texttt{ var-list })
- \texttt{attach} (\texttt{ var-list })
- \texttt{private} (\texttt{ var-list })
- \texttt{firstprivate} (\texttt{ var-list })
- \texttt{default} (\texttt{ none | present })

\textbf{Description}

When the program encounters an accelerator \texttt{parallel} construct, one or more gangs of workers are created to execute the accelerator parallel region. The number of gangs, and the number of workers in each gang and the number of vector lanes per worker remain constant for the duration of that parallel region. Each gang begins executing the code in the structured block in gang-redundant mode even if there is only a single gang. This means that code within the parallel region, but outside of a loop construct with gang-level worksharing, will be executed redundantly by all gangs.

One worker in each gang begins executing the code in the structured block of the construct. \textbf{Note:} Unless there is a \texttt{loop} construct within the parallel region, all gangs will execute all the code within the region redundantly.

If the \texttt{async} clause does not appear, there is an implicit barrier at the end of the accelerator parallel region, and the execution of the local thread will not proceed until all gangs have reached the end of the parallel region.

The \texttt{copy}, \texttt{copyin}, \texttt{copyout}, \texttt{create}, \texttt{no\_create}, \texttt{present}, \texttt{deviceptr}, and \texttt{attach} data clauses are described in Section 2.7 Data Clauses. The \texttt{private} and \texttt{firstprivate} clauses are described in Sections 2.5.12 and Sections 2.5.13. The \texttt{device\_type} clause is described in Section 2.4 Device-Specific Clauses. Implicitly determined data attributes are described in Section 2.6.3. Restrictions are described in Section 2.5.4.

\subsection{2.5.2 Serial Construct}
Summary

This construct defines a region of the program that is to be executed sequentially on the current device. The behavior of the `serial` construct is the same as that of the `parallel` construct except that it always executes with a single gang of a single worker with a vector length of one.

Note: The `serial` construct may be used to execute sequential code on the current device, which removes the need for data movement when the required data is already present on the device.

Syntax

In C and C++, the syntax of the OpenACC `serial` construct is

```c
#pragma acc serial [clause-list] new-line structured block
```

and in Fortran, the syntax is

```fortran
!$acc serial [ clause-list ]
  structured block
!$acc end serial
```

or

```fortran
!$acc serial [ clause-list ]
  block construct
!$acc end serial
```

where `clause` is as for the `parallel` construct except that the `num_gangs`, `num_workers`, and `vector_length` clauses are not permitted.

2.5.3 Kernels Construct

Summary

This construct defines a region of the program that is to be compiled into a sequence of kernels for execution on the current device.

Syntax

In C and C++, the syntax of the OpenACC `kernels` construct is

```c
#pragma acc kernels [ clause-list ] new-line structured block
```

and in Fortran, the syntax is

```fortran
!$acc kernels [ clause-list ]
  structured block
!$acc end kernels
```

or

```fortran
!$acc kernels [ clause-list ]
  block construct
!$acc end kernels
```


where clause is one of the following:

- async [(int-expr)]
- wait [(int-expr-list)]
- num_gangs (int-expr)
- num_workers (int-expr)
- vector_length (int-expr)
- device_type (device-type-list)
- if (condition)
- self [(condition)]
- copy (var-list)
- copyin ([readonly:] var-list)
- copyout ([zero:] var-list)
- create ([zero:] var-list)
- no_create (var-list)
- present (var-list)
- deviceptr (var-list)
- attach (var-list)
- default (none | present)

**Description**

The compiler will split the code in the kernels region into a sequence of accelerator kernels. Typically, each loop nest will be a distinct kernel. When the program encounters a kernels construct, it will launch the sequence of kernels in order on the device. The number and configuration of gangs of workers and vector length may be different for each kernel.

If the async clause does not appear, there is an implicit barrier at the end of the kernels region, and the local thread execution will not proceed until the entire sequence of kernels has completed execution.

The copy, copyin, copyout, create, no_create, present, deviceptr, and attach data clauses are described in Section 2.7 Data Clauses. The device_type clause is described in Section 2.4 Device-Specific Clauses. Implicitly determined data attributes are described in Section 2.6.2 Restrictions are described in Section 2.5.4

**2.5.4 Compute Construct Restrictions**

The following restrictions apply to all compute constructs:

- A program may not branch into or out of a compute construct.
- A program must not depend on the order of evaluation of the clauses or on any side effects of the evaluations.
- Only the async, wait, num_gangs, num_workers, and vector_length clauses may follow a device_type clause.
- At most one if clause may appear. In Fortran, the condition must evaluate to a scalar logical value; in C or C++, the condition must evaluate to a scalar integer value.
- At most one default clause may appear, and it must have a value of either none or present.
2.5.5 if clause

The if clause is optional.

When the condition in the if clause evaluates to nonzero in C or C++, or .true. in Fortran, the region will execute on the current device. When the condition in the if clause evaluates to zero in C or C++, or .false. in Fortran, the local thread will execute the region.

2.5.6 self clause

The self clause is optional.

The self clause may have a single condition-argument. If the condition-argument is not present it is assumed to be nonzero in C or C++, or .true. in Fortran. When both an if clause and a self clause appear and the condition in the if clause evaluates to 0 in C or C++ or .false. in Fortran, the self clause has no effect.

When the condition evaluates to nonzero in C or C++, or .true. in Fortran, the region will execute on the local device. When the condition in the self clause evaluates to zero in C or C++, or .false. in Fortran, the region will execute on the current device.

2.5.7 async clause

The async clause is optional; see Section 2.16 Asynchronous Behavior for more information.

2.5.8 wait clause

The wait clause is optional; see Section 2.16 Asynchronous Behavior for more information.

2.5.9 num_gangs clause

The num_gangs clause is allowed on the parallel and kernels constructs. The value of the integer expression defines the number of parallel gangs that will execute the parallel region, or that will execute each kernel created for the kernels region. If the clause does not appear, an implementation-defined default will be used; the default may depend on the code within the construct. The implementation may use a lower value than specified based on limitations imposed by the target architecture.

2.5.10 num_workers clause

The num_workers clause is allowed on the parallel and kernels constructs. The value of the integer expression defines the number of workers within each gang that will be active after a gang transitions from worker-single mode to worker-partitioned mode. If the clause does not appear, an implementation-defined default will be used; the default value may be 1, and may be different for each parallel construct or for each kernel created for a kernels construct. The implementation may use a different value than specified based on limitations imposed by the target architecture.

2.5.11 vector_length clause

The vector_length clause is allowed on the parallel and kernels constructs. The value of the integer expression defines the number of vector lanes that will be active after a worker transi-
sections from vector-single mode to vector-partitioned mode. This clause determines the vector length
to use for vector or SIMD operations. If the clause does not appear, an implementation-defined
default will be used. This vector length will be used for loop constructs annotated with the `vector`
clause, as well as loops automatically vectorized by the compiler. The implementation may use a
different value than specified based on limitations imposed by the target architecture.

### 2.5.12 `private` clause

The `private` clause is allowed on the `parallel` and `serial` constructs; it declares that a copy
of each item on the list will be created for each gang.

**Restrictions**
- See Section 2.17.1 `Optional Arguments` for discussion of Fortran optional arguments in `private`
  clauses.

### 2.5.13 `firstprivate` clause

The `firstprivate` clause is allowed on the `parallel` and `serial` constructs; it declares that
a copy of each item on the list will be created for each gang, and that the copy will be initialized with
the value of that item on the local thread when a `parallel` or `serial` construct is encountered.

**Restrictions**
- See Section 2.17.1 `Optional Arguments` for discussion of Fortran optional arguments in `firstprivate`
  clauses.

### 2.5.14 `reduction` clause

The `reduction` clause is allowed on the `parallel` and `serial` constructs. It specifies a
reduction operator and one or more `vars`. It implies `copy` clauses as described in Section 2.6.2. For
each reduction `var`, a private copy is created for each parallel gang and initialized for that operator.
At the end of the region, the values for each gang are combined using the reduction operator, and
the result combined with the value of the original `var` and stored in the original `var`. If the reduction
`var` is an array or subarray, the array reduction operation is logically equivalent to applying that
reduction operation to each element of the array or subarray individually. If the reduction `var`
is a composite variable, the reduction operation is logically equivalent to applying that reduction
operation to each member of the composite variable individually. The reduction result is available
after the region.

The following table lists the operators that are valid and the initialization values; in each case, the
initialization value will be cast into the data type of the `var`. For `max` and `min` reductions, the
initialization values are the least representable value and the largest representable value for that data
type, respectively. At a minimum, the supported data types include Fortran `logical` as well as
the numerical data types in C (e.g., `_Bool`, `char`, `int`, `float`, `double`, `float _Complex`,
`double _Complex`), C++ (e.g., `bool`, `char`, `wchar_t`, `int`, `float`, `double`), and Fortran
(e.g., `integer`, `real`, `double precision`, `complex`). However, for each reduction operator,
the supported data types include only the types permitted as operands to the corresponding operator
in the base language where (1) for max and min, the corresponding operator is less-than and (2) for
other operators, the operands and the result are the same type.
The OpenACC API 2.6. Data Environment

<table>
<thead>
<tr>
<th>C and C++</th>
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<td>&amp;&amp;</td>
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<td></td>
<td>.eqv.</td>
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<tr>
<td></td>
<td>.neqv.</td>
</tr>
</tbody>
</table>

Restrictions

- A var in a reduction clause must be a scalar variable name, an aggregate variable name, an array element, or a subarray (refer to Section 2.7.1).
- If the reduction var is an array element or a subarray, accessing the elements of the array outside the specified index range results in unspecified behavior.
- The reduction var may not be a member of a composite variable.
- If the reduction var is a composite variable, each member of the composite variable must be a supported datatype for the reduction operation.
- See Section 2.17.1 Optional Arguments for discussion of Fortran optional arguments in reduction clauses.

2.5.15 default clause

The default clause is optional. At most one default clause may appear. It adjusts what data attributes are implicitly determined for variables used in the compute construct as described in Section 2.6.2.

2.6 Data Environment

This section describes the data attributes for variables. The data attributes for a variable may be predetermined, implicitly determined, or explicitly determined. Variables with predetermined data attributes may not appear in a data clause that conflicts with that data attribute. Variables with implicitly determined data attributes may appear in a data clause that overrides the implicit attribute. Variables with explicitly determined data attributes are those which appear in a data clause on a data construct, a compute construct, or a declare directive.

OpenACC supports systems with accelerators that have discrete memory from the host, systems with accelerators that share memory with the host, as well as systems where an accelerator shares some memory with the host but also has some discrete memory that is not shared with the host. In the first case, no data is in shared memory. In the second case, all data is in shared memory. In the third case, some data may be in shared memory and some data may be in discrete memory, although a single array or aggregate data structure must be allocated completely in shared or discrete memory.
memory. When a nested OpenACC construct is executed on the device, the default target device for that construct is the same device on which the encountering accelerator thread is executing. In that case, the target device shares memory with the encountering thread.

### 2.6.1 Variables with Predetermined Data Attributes

The loop variable in a C `for` statement or Fortran `do` statement that is associated with a loop directive is predetermined to be private to each thread that will execute each iteration of the loop. Loop variables in Fortran `do` statements within a compute construct are predetermined to be private to the thread that executes the loop.

Variables declared in a C block or Fortran block construct that is executed in `vector-partitioned` mode are private to the thread associated with each vector lane. Variables declared in a C block or Fortran block construct that is executed in `worker-partitioned vector-single` mode are private to the worker and shared across the threads associated with the vector lanes of that worker. Variables declared in a C block or Fortran block construct that is executed in `worker-single` mode are private to the gang and shared across the threads associated with the workers and vector lanes of that gang.

A procedure called from a compute construct will be annotated as `seq`, `vector`, `worker`, or `gang`, as described Section 2.15 Procedure Calls in Compute Regions. Variables declared in `seq` routine are private to the thread that made the call. Variables declared in `vector` routine are private to the worker that made the call and shared across the threads associated with the vector lanes of that worker. Variables declared in `worker` or `gang` routine are private to the gang that made the call and shared across the threads associated with the workers and vector lanes of that gang.

### 2.6.2 Variables with Implicitly Determined Data Attributes

When implicitly determining data attributes on a compute construct, the following clauses are visible at the compute construct:

- The nearest `default` clause appearing on the compute construct or a lexically containing `data` construct.
- All data clauses on the compute construct, a lexically containing `data` construct, or a visible `declare` directive.

On a compute or combined construct, if a variable appears in a `reduction` clause but no other data clause, it is treated as if it also appears in a `copy` clause. Otherwise, for any variable, the compiler will implicitly determine its data attribute on a compute construct if all of the following conditions are met:

- There is no `default (none)` clause visible at the compute construct.
- The variable is referenced in the compute construct.
- The variable does not have a predetermined data attribute.
- The variable does not appear in a data clause visible at the compute construct.

An aggregate variable will be treated as if it appears either:

- In a `present` clause if there is a `default (present)` clause visible at the compute construct.
• In a `copy` clause otherwise.

A scalar variable will be treated as if it appears either:

• In a `copy` clause if the compute construct is a `kernels` construct.

• In a `firstprivate` clause otherwise.

Restrictions

• If there is a `default(none)` clause visible at a compute construct, the compiler requires any variable referenced in the compute construct either to have a predetermined data attribute or to appear in a visible data clause. Note: A `copy` clause implied by a `reduction` clause suffices as such a data clause.

• If a scalar variable appears in a `reduction` clause on a `loop` construct that has a parent `parallel` or `serial` construct, it must have a predetermined data attribute or appear in an explicit data or `reduction` clause visible at the compute construct. Note: Implementations are encouraged to issue a compile-time diagnostic when this restriction is violated to assist users in writing portable OpenACC applications.

If a C++ `lambda` is called in a compute region and does not appear in a data clause, then it is treated as if it appears in a `copyin` clause on the current construct. A variable captured by a `lambda` is processed according to its data types: a pointer type variable is treated as if it appears in a `no_create` clause; a reference type variable is treated as if it appears in a `present` clause; for a struct or a class type variable, any pointer member is treated as if it appears in a `no_create` clause on the current construct. If the variable is defined as global or file or function static, it must appear in a `declare` directive.

2.6.3 Data Regions and Data Lifetimes

Data in shared memory is accessible from the current device as well as to the local thread. Such data is available to the accelerator for the lifetime of the variable. Data not in shared memory must be copied to and from device memory using data constructs, clauses, and API routines. A `data lifetime` is the duration from when the data is first made available to the accelerator until it becomes unavailable. For data in shared memory, the data lifetime begins when the data is allocated and ends when it is deallocated; for statically allocated data, the data lifetime begins when the program begins and does not end. For data not in shared memory, the data lifetime begins when it is made present and ends when it is no longer present.

There are four types of data regions. When the program encounters a `data` construct, it creates a data region.

When the program encounters a compute construct with explicit data clauses or with implicit data allocation added by the compiler, it creates a data region that has a duration of the compute construct.

When the program enters a procedure, it creates an implicit data region that has a duration of the procedure. That is, the implicit data region is created when the procedure is called, and exited when the program returns from that procedure invocation. There is also an implicit data region associated with the execution of the program itself. The implicit program data region has a duration of the execution of the program.

In addition to data regions, a program may create and delete data on the accelerator using `enter data` and `exit data` directives or using runtime API routines. When the program executes
an **enter data** directive, or executes a call to a runtime API **acc_copyin** or **acc_create** routine, each **var** on the directive or the variable on the runtime API argument list will be made live on accelerator.

### 2.6.4 Data Structures with Pointers

This section describes the behavior of data structures that contain pointers. A pointer may be a C or C++ pointer (e.g., float*), a Fortran pointer or array pointer (e.g., real, pointer, dimension(:)), or a Fortran allocatable (e.g., real, allocatable, dimension(:)).

When a data object is copied to device memory, the values are copied exactly. If the data is a data structure that includes a pointer, or is just a pointer, the pointer value copied to device memory will be the host pointer value. If the pointer target object is also allocated in or copied to device memory, the pointer itself needs to be updated with the device address of the target object before dereferencing the pointer in device memory.

An **attach** action updates the pointer in device memory to point to the device copy of the data that the host pointer targets; see Section 2.7.2. For Fortran array pointers and allocatable arrays, this includes copying any associated descriptor (dope vector) to the device copy of the pointer. When the device pointer target is deallocated, the pointer in device memory should be restored to the host value, so it can be safely copied back to host memory. A **detach** action updates the pointer in device memory to have the same value as the corresponding pointer in local memory; see Section 2.7.2. The **attach** and **detach** actions are performed by the **copy**, **copyin**, **copyout**, **create**, **attach**, and **detach** data clauses (Sections 2.7.4, 2.7.13), and the **acc_attach** and **acc_detach** runtime API routines (Sections 3.2.40 and 3.2.41). The **attach** and **detach** actions use attachment counters to determine when the pointer in device memory needs to be updated; see Section 2.6.8.

### 2.6.5 Data Construct

#### Summary

The **data** construct defines **vars** to be allocated in the current device memory for the duration of the region, whether data should be copied from local memory to the current device memory upon region entry, and copied from device memory to local memory upon region exit.

#### Syntax

In C and C++, the syntax of the OpenACC **data** construct is

```plaintext
#pragma acc data [clause-list] new-line
structured block
```

and in Fortran, the syntax is

```fortran
!$acc data [clause-list]
structured block
!$acc end data
```

or

```fortran
!$acc data [clause-list]
block construct
[$!$acc end data]
```
where clause is one of the following:

```
if( condition )
copy( var-list )
copyin( [readonly:]var-list )
copyout( [zero:]var-list )
create( [zero:]var-list )
no_create( var-list )
present( var-list )
deviceptr( var-list )
attach( var-list )
default( none | present )
```

Description

Data will be allocated in the memory of the current device and copied from local memory to device memory, or copied back, as required. The data clauses are described in Section 2.7 Data Clauses. Structured reference counters are incremented for data when entering a data region, and decremented when leaving the region, as described in Section 2.6.7 Reference Counters.

Restrictions

- At least one copy, copyin, copyout, create, no_create, present, deviceptr, attach, or default clause must appear on a data construct.

if clause

The if clause is optional; when there is no if clause, the compiler will generate code to allocate space in the current device memory and move data from and to the local memory as required. When an if clause appears, the program will conditionally allocate memory in and move data to and/or from device memory. When the condition in the if clause evaluates to zero in C or C++, or .false. in Fortran, no device memory will be allocated, and no data will be moved. When the condition evaluates to nonzero in C or C++, or .true. in Fortran, the data will be allocated and moved as specified. At most one if clause may appear.

default clause

The default clause is optional. At most one default clause may appear. It adjusts what data attributes are implicitly determined for variables used in lexically contained compute constructs as described in Section 2.6.2.

2.6.6 Enter Data and Exit Data Directives

Summary

An enter data directive may be used to define vars to be allocated in the current device memory for the remaining duration of the program, or until an exit data directive that deallocates the data. They also tell whether data should be copied from local memory to device memory at the enter data directive, and copied from device memory to local memory at the exit data directive. The dynamic range of the program between the enter data directive and the matching exit data directive is the data lifetime for that data.
Syntax

In C and C++, the syntax of the OpenACC `enter data` directive is

```
#pragma acc enter data clause-list new-line
```

and in Fortran, the syntax is

```
!$acc enter data clause-list
```

where `clause` is one of the following:

- `if(condition)`
- `async[(int-expr)]`
- `wait[(wait-argument)]`
- `copyin(var-list)`
- `create([zero:]var-list)`
- `attach(var-list)`

In C and C++, the syntax of the OpenACC `exit data` directive is

```
#pragma acc exit data clause-list new-line
```

and in Fortran, the syntax is

```
!$acc exit data clause-list
```

where `clause` is one of the following:

- `if(condition)`
- `async[(int-expr)]`
- `wait[(wait-argument)]`
- `copyout(var-list)`
- `delete(var-list)`
- `detach(var-list)`
- `finalize`

Description

At an `enter data` directive, data may be allocated in the current device memory and copied from local memory to device memory. This action enters a data lifetime for those `vars`, and will make the data available for `present` clauses on constructs within the data lifetime. Dynamic reference counters are incremented for this data, as described in Section [2.6.7 Reference Counters](#). Pointers in device memory may be `attached` to point to the corresponding device copy of the host pointer target.

At an `exit data` directive, data may be copied from device memory to local memory and deallocated from device memory. If no `finalize` clause appears, dynamic reference counters are decremented for this data. If a `finalize` clause appears, the dynamic reference counters are set to zero for this data. Pointers in device memory may be `detached` so as to have the same value as the original host pointer.

The data clauses are described in Section [2.7 Data Clauses](#) Reference counting behavior is described in Section [2.6.7 Reference Counters](#).
Restrictions

- At least one `copyin`, `create`, or `attach` clause must appear on an `enter data` directive.
- At least one `copyout`, `delete`, or `detach` clause must appear on an `exit data` directive.

if clause

The if clause is optional; when there is no if clause, the compiler will generate code to allocate or deallocate space in the current device memory and move data from and to local memory. When an if clause appears, the program will conditionally allocate or deallocate device memory and move data to and/or from device memory. When the condition in the if clause evaluates to zero in C or C++, or `.false.` in Fortran, no device memory will be allocated or deallocated, and no data will be moved. When the condition evaluates to nonzero in C or C++, or `.true.` in Fortran, the data will be allocated or deallocated and moved as specified.

async clause

The async clause is optional; see Section 2.16 Asynchronous Behavior for more information.

wait clause

The wait clause is optional; see Section 2.16 Asynchronous Behavior for more information.

finalize clause

The finalize clause is allowed on the `exit data` directive and is optional. When no finalize clause appears, the exit data directive will decrement the dynamic reference counters for vars appearing in `copyout` and `delete` clauses, and will decrement the attachment counters for pointers appearing in `detach` clauses. If a finalize clause appears, the exit data directive will set the dynamic reference counters to zero for vars appearing in `copyout` and `delete` clauses, and will set the attachment counters to zero for pointers appearing in `detach` clauses.

2.6.7 Reference Counters

When device memory is allocated for data not in shared memory due to data clauses or OpenACC API routine calls, the OpenACC implementation keeps track of that device memory and its relationship to the corresponding data in host memory.

Each section of device memory will be associated with two `reference counters` per device, a structured reference counter and a dynamic reference counter. The structured and dynamic reference counters are used to determine when to allocate or deallocate data in device memory. The structured reference counter for a block of data keeps track of how many nested data regions have been entered for that data. The initial value of the structured reference counter for static data in device memory (in a global `declare` directive) is one; for all other data, the initial value is zero. The dynamic reference counter for a block of data keeps track of how many dynamic data lifetimes are currently active in device memory for that block. The initial value of the dynamic reference counter is zero. Data is considered present if the sum of the structured and dynamic reference counters is greater than zero.
A structured reference counter is incremented when entering each data or compute region that contains an explicit data clause or implicitly-determined data attributes for that block of memory, and is decremented when exiting that region. A dynamic reference counter is incremented for each `enter data copyin` or `create` clause, or each `acc_copyin` or `acc_create` API routine call for that block of memory. The dynamic reference counter is decremented for each `exit data copyout` or `delete` clause when no `finalize` clause appears, or each `acc_copyout` or `acc_delete` API routine call for that block of memory. The dynamic reference counter will be set to zero with an `exit data copyout` or `delete` clause when a `finalize` clause appears, or each `acc_copyout_finalize` or `acc_delete_finalize` API routine call for the block of memory. The reference counters are modified synchronously with the local thread, even if the data directives include an `async` clause. When both structured and dynamic reference counters reach zero, the data lifetime in device memory for that data ends.

### 2.6.8 Attachment Counter

Since multiple pointers can target the same address, each pointer in device memory is associated with an *attachment counter* per device. The *attachment counter* for a pointer is initialized to zero when the pointer is allocated in device memory. The *attachment counter* for a pointer is set to one whenever the pointer is *attached* to a new target address, and incremented whenever an *attach* action for that pointer is performed for the same target address. The *attachment counter* is decremented whenever a *detach* action occurs for the pointer, and the pointer is *detached* when the *attachment counter* reaches zero. This is described in more detail in Section 2.7.2 *Data Clause Actions*.

A pointer in device memory can be assigned a device address in two ways. The pointer can be attached to a device address due to data clauses or API routines, as described in Section 2.7.2 *Data Clause Actions*, or the pointer can be assigned in a compute region executed on that device. Unspecified behavior may result if both ways are used for the same pointer.

Pointer members of structs, classes, or derived types in device or host memory can be overwritten due to update directives or API routines. It is the user’s responsibility to ensure that the pointers have the appropriate values before or after the data movement in either direction. The behavior of the program is undefined if any of the pointer members are attached when an update of a composite variable is performed.

### 2.7 Data Clauses

These data clauses may appear on the `parallel` construct, `kernels` construct, `serial` construct, `data` construct, the `enter data` and `exit data` directives, and `declare` directives. In the descriptions, the *region* is a compute region with a clause appearing on a `parallel`, `kernels`, or `serial` construct, a data region with a clause on a `data` construct, or an implicit data region with a clause on a `declare` directive. If the `declare` directive appears in a global context, the corresponding implicit data region has a duration of the program. The list argument to each data clause is a comma-separated collection of `vars`. For all clauses except `deviceptr` and `present`, the list argument may include a Fortran `common block` name enclosed within slashes, if that `common block` name also appears in a `declare` directive `link` clause. In all cases, the compiler will allocate and manage a copy of the `var` in the memory of the current device, creating a visible device copy of that `var`, for data not in shared memory.

OpenACC supports accelerators with discrete memories from the local thread. However, if the accelerator can access the local memory directly, the implementation may avoid the memory allo-
cation and data movement and simply share the data in local memory. Therefore, a program that uses and assigns data on the host and uses and assigns the same data on the accelerator within a data region without update directives to manage the coherence of the two copies may get different answers on different accelerators or implementations.

Restrictions

- Data clauses may not follow a `device_type` clause.
- See Section 2.17.1 [Optional Arguments] for discussion of Fortran optional arguments in data clauses.

2.7.1 Data Specification in Data Clauses

In C and C++, a subarray is an array name followed by an extended array range specification in brackets, with start and length, such as

```
AA[2:n]
```

If the lower bound is missing, zero is used. If the length is missing and the array has known size, the size of the array is used; otherwise the length is required. The subarray `AA[2:n]` means element `AA[2]`, `AA[3]`, ..., `AA[2+n-1]`.

In C and C++, a two dimensional array may be declared in at least four ways:

- Statically-sized array: `float AA[100][200];`
- Pointer to statically sized rows: `typedef float row[200]; row* BB;`
- Statically-sized array of pointers: `float* CC[200];`
- Pointer to pointers: `float** DD;`

Each dimension may be statically sized, or a pointer to dynamically allocated memory. Each of these may be included in a data clause using subarray notation to specify a rectangular array:

```
• AA[2:n] [0:200]
• BB[2:n] [0:m]
• CC[2:n] [0:m]
• DD[2:n] [0:m]
```

Multidimensional rectangular subarrays in C and C++ may be specified for any array with any combination of statically-sized or dynamically-allocated dimensions. For statically sized dimensions, all dimensions except the first must specify the whole extent, to preserve the contiguous data restriction, discussed below. For dynamically allocated dimensions, the implementation will allocate pointers in device memory corresponding to the pointers in local memory, and will fill in those pointers as appropriate.

In Fortran, a subarray is an array name followed by a comma-separated list of range specifications in parentheses, with lower and upper bound subscripts, such as

```
arr(1:high, low:100)
```
If either the lower or upper bounds are missing, the declared or allocated bounds of the array, if known, are used. All dimensions except the last must specify the whole extent, to preserve the contiguous data restriction, discussed below.

**Restrictions**

- In Fortran, the upper bound for the last dimension of an assumed-size dummy array must be specified.
- In C and C++, the length for dynamically allocated dimensions of an array must be explicitly specified.
- In C and C++, modifying pointers in pointer arrays during the data lifetime, either on the host or on the device, may result in undefined behavior.
- If a subarray appears in a data clause, the implementation may choose to allocate memory for only that subarray on the accelerator.
- In Fortran, array pointers may appear, but pointer association is not preserved in device memory.
- Any array or subarray in a data clause, including Fortran array pointers, must be a contiguous block of memory, except for dynamic multidimensional C arrays.
- In C and C++, if a variable or array of composite type appears, all the data members of the struct or class are allocated and copied, as appropriate. If a composite member is a pointer type, the data addressed by that pointer are not implicitly copied.
- In Fortran, if a variable or array of composite type appears, all the members of that derived type are allocated and copied, as appropriate. If any member has the `allocatable` or `pointer` attribute, the data accessed through that member are not copied.
- If an expression is used in a subscript or subarray expression in a clause on a `data` construct, the same value is used when copying data at the end of the data region, even if the values of variables in the expression change during the data region.

### 2.7.2 Data Clause Actions

Most of the data clauses perform one or more of the following actions. The actions test or modify one or both of the structured and dynamic reference counters, depending on the directive on which the data clause appears.

**Present Increment Action**

A *present increment* action is one of the actions that may be performed for a `present` (Section 2.7.5), `copy` (Section 2.7.6), `copyin` (Section 2.7.7), `copyout` (Section 2.7.8), `create` (Section 2.7.9), or `no_create` (Section 2.7.10) clause, or for a call to an `acc_copyin` (Section 3.2.26) or `acc_create` (Section 3.2.27) API routine. See those sections for details.

A *present increment* action for a `var` occurs only when `var` is already present in device memory.

A *present increment* action for a `var` increments the structured or dynamic reference counter for `var`. 
Present Decrement Action

A *present decrement* action is one of the actions that may be performed for a `present` (Section 2.7.5), `copy` (Section 2.7.6), `copyin` (Section 2.7.7), `copyout` (Section 2.7.8), `create` (Section 2.7.9), `no_create` (Section 2.7.10), or `delete` (Section 2.7.11) clause, or for a call to an `acc_copyout` (Section 3.2.28) or `acc_delete` (Section 3.2.29) API routine. See those sections for details.

A *present decrement* action for a `var` occurs only when `var` is already present in device memory.

A *present decrement* action for a `var` decrements the structured or dynamic reference counter for `var`, if its value is greater than zero. If the device memory associated with `var` was mapped to the device using `acc_map_data`, the dynamic reference count may not be decremented to zero, except by a call to `acc_unmap_data`. If the reference counter is already zero, its value is left unchanged.

Create Action

A `create` action is one of the actions that may be performed for a `copyout` (Section 2.7.8) or `create` (Section 2.7.9) clause, or for a call to an `acc_create` API routine (Section 3.2.27). See those sections for details.

A `create` action for a `var` occurs only when `var` is not already present in device memory.

A `create` action for a `var`:

• allocates device memory for `var`; and

• sets the structured or dynamic reference counter to one.

Copyin Action

A `copyin` action is one of the actions that may be performed for a `copy` (Section 2.7.6) or `copyin` (Section 2.7.7) clause, or for a call to an `acc_copyin` API routine (Section 3.2.26). See those sections for details.

A `copyin` action for a `var` occurs only when `var` is not already present in device memory.

A `copyin` action for a `var`:

• allocates device memory for `var`;

• initiates a copy of the data for `var` from the local thread memory to the corresponding device memory; and

• sets the structured or dynamic reference counter to one.

The data copy may complete asynchronously, depending on other clauses on the directive.

Copyout Action

A `copyout` action is one of the actions that may be performed for a `copy` (Section 2.7.6) or `copyout` (Section 2.7.8) clause, or for a call to an `acc_copyout` API routine (Section 3.2.28). See those sections for details.

A `copyout` action for a `var` occurs only when `var` is present in device memory.
A *copyout* action for a `var`:

- performs an *immediate detach* action for any pointer in `var`;
- initiates a copy of the data for `var` from device memory to the corresponding local thread memory; and
- deallocates device memory for `var`.

The data copy may complete asynchronously, depending on other clauses on the directive, in which case the memory is deallocated when the data copy is complete.

**Delete Action**

A *delete* action is one of the actions that may be performed for a `present` (Section 2.7.5), `copyin` (Section 2.7.7), `create` (Section 2.7.9), `no_create` (Section 2.7.10), or `delete` (Section 2.7.11) clause, or for a call to an `acc_delete` API routine (Section 3.2.29). See those sections for details.

A *delete* action for a `var` occurs only when `var` is present in device memory.

A *delete* action for `var`:

- performs an *immediate detach* action for any pointer in `var`; and
- deallocates device memory for `var`.

**Attach Action**

An *attach* action is one of the actions that may be performed for a `present` (Section 2.7.5), `copy` (Section 2.7.6), `copyin` (Section 2.7.7), `copyout` (Section 2.7.8), `create` (Section 2.7.9), `no_create` (Section 2.7.10), or `attach` (Section 2.7.11) clause, or for a call to an `acc_attach` API routine (Section 3.2.40). See those sections for details.

An *attach* action for a `var` occurs only when `var` is a pointer reference.

If the pointer `var` is in shared memory or is not present in the current device memory, or if the address to which `var` points is not present in the current device memory, no action is taken. If the attachment counter for `var` is nonzero and the pointer in device memory already points to the device copy of the data in `var`, the attachment counter for the pointer `var` is incremented. Otherwise, the pointer in device memory is *attached* to the device copy of the data by initiating an update for the pointer in device memory to point to the device copy of the data and setting the attachment counter for the pointer `var` to one. The update may complete asynchronously, depending on other clauses on the directive. The pointer update must follow any data copies due to `copyin` actions that are performed for the same directive.

**Detach Action**

A *detach* action is one of the actions that may be performed for a `present` (Section 2.7.5), `copy` (Section 2.7.6), `copyin` (Section 2.7.7), `copyout` (Section 2.7.8), `create` (Section 2.7.9), `no_create` (Section 2.7.10), `delete` (Section 2.7.11), or `detach` (Section 2.7.11) clause, or for a call to an `acc_detach` API routine (Section 3.2.41). See those sections for details.

A *detach* action for a `var` occurs only when `var` is a pointer reference.
If the pointer var is in shared memory or is not present in the current device memory, or if the attachment counter for var for the pointer is zero, no action is taken. Otherwise, the attachment counter for the pointer var is decremented. If the attachment counter is decreased to zero, the pointer is detached by initiating an update for the pointer var in device memory to have the same value as the corresponding pointer in local memory. The update may complete asynchronously, depending on other clauses on the directive. The pointer update must precede any data copies due to copyout actions that are performed for the same directive.

Immediate Detach Action

An immediate detach action is one of the actions that may be performed for a detach clause, or for a call to an acc_detach_finalize API routine. See those sections for details.

An immediate detach action for a var occurs only when var is a pointer reference and is present in device memory.

If the attachment counter for the pointer is zero, the immediate detach action has no effect. Otherwise, the attachment counter for the pointer set to zero and the pointer is detached by initiating an update for the pointer in device memory to have the same value as the corresponding pointer in local memory. The update may complete asynchronously, depending on other clauses on the directive. The pointer update must precede any data copies due to copyout actions that are performed for the same directive.

2.7.3 Data Clause Restrictions

The following restriction applies to data that appear in a present, copy, copyin, copyout, create, and delete clause:

- If only a subarray of an array is present in the current device memory, it is a runtime error if var includes array elements that are not part of the existing data lifetime.

2.7.4 deviceptr clause

The deviceptr clause may appear on structured data and compute constructs and declare directives.

The deviceptr clause is used to declare that the pointers in var-list are device pointers, so the data need not be allocated or moved between the host and device for this pointer.

In C and C++, the vars in var-list must be pointer variables.

In Fortran, the vars in var-list must be dummy arguments (arrays or scalars), and may not have the Fortran pointer, allocatable, or value attributes.

For data in shared memory, host pointers are the same as device pointers, so this clause has no effect.

2.7.5 present clause

The present clause may appear on structured data and compute constructs and declare directives. The present clause specifies that vars in var-list are in shared memory or are already
present in the current device memory due to data regions or data lifetimes that contain the construct on which the `present` clause appears.

For each `var` in `var-list`, if `var` is in shared memory, no action is taken; if `var` is not in shared memory, the `present` clause behaves as follows:

- At entry to the region:
  - If `var` is not present in the current device memory, a runtime error is issued.
  - Otherwise, a [present increment] action with the structured reference counter is performed. If `var` is a pointer reference, an [attach] action is performed.

- At exit from the region:
  - If the structured reference counter for `var` is zero, no action is taken.
  - Otherwise, a [present decrement] action with the structured reference counter is performed. If `var` is a pointer reference, a [detach] action is performed. If both structured and dynamic reference counters are zero, a [delete] action is performed.

The restrictions in Section 2.7.3 Data Clause Restrictions apply to this clause.

### 2.7.6 copy clause

The `copy` clause may appear on structured `data` and compute constructs and on `declare` directives.

For each `var` in `var-list`, if `var` is in shared memory, no action is taken; if `var` is not in shared memory, the `copy` clause behaves as follows:

- At entry to the region:
  - If `var` is present, a [present increment] action with the structured reference counter is performed. If `var` is a pointer reference, an [attach] action is performed.
  - Otherwise, a [copyin] action with the structured reference counter is performed. If `var` is a pointer reference, an [attach] action is performed.

- At exit from the region:
  - If the structured reference counter for `var` is zero, no action is taken.
  - Otherwise, a [present decrement] action with the structured reference counter is performed. If `var` is a pointer reference, a [detach] action is performed. If both structured and dynamic reference counters are zero, a [copyout] action is performed.

The restrictions in Section 2.7.3 Data Clause Restrictions apply to this clause.

For compatibility with OpenACC 2.0, `present_or_copy` and `pcopy` are alternate names for `copy`.

### 2.7.7 copyin clause

The `copyin` clause may appear on structured `data` and compute constructs, on `declare` directives, and on `enter data` directives.
For each \textit{var} in \textit{var-list}, if \textit{var} is in shared memory, no action is taken; if \textit{var} is not in shared memory, the \texttt{copyin} clause behaves as follows:

- At entry to a region, the structured reference counter is used. On an \texttt{enter data} directive, the dynamic reference counter is used.
  
  - If \textit{var} is present, a \texttt{present increment} action with the appropriate reference counter is performed. If \textit{var} is a pointer reference, an \texttt{attach} action is performed.
  
  - Otherwise, a \texttt{copyin} action with the appropriate reference counter is performed. If \textit{var} is a pointer reference, an \texttt{attach} action is performed.

- At exit from the region:
  
  - If the structured reference counter for \textit{var} is zero, no action is taken.
  
  - Otherwise, a \texttt{present decrement} action with the structured reference counter is performed. If \textit{var} is a pointer reference, a \texttt{detach} action is performed. If both structured and dynamic reference counters are zero, a \texttt{delete} action is performed.

If the optional \texttt{readonly} modifier appears, then the implementation may assume that the data referenced by \textit{var-list} is never written to within the applicable region.

The restrictions in Section \ref{Data Clause Restrictions} apply to this clause.

For compatibility with OpenACC 2.0, \texttt{present_or_copyin} and \texttt{pcopyin} are alternate names for \texttt{copyin}.

An \texttt{enter data} directive with a \texttt{copyin} clause is functionally equivalent to a call to the \texttt{acc_copyin} API routine, as described in Section \ref{acc_copyin}.

### 2.7.8 copyout clause

The \texttt{copyout} clause may appear on structured \texttt{data} and compute constructs, on \texttt{declare} directives, and on \texttt{exit data} directives. The clause may optionally have a \texttt{zero} modifier if the \texttt{copyout} clause appears on a structured \texttt{data} or compute construct.

For each \textit{var} in \textit{var-list}, if \textit{var} is in shared memory, no action is taken; if \textit{var} is not in shared memory, the \texttt{copyout} clause behaves as follows:

- At entry to a region:
  
  - If \textit{var} is present, a \texttt{present increment} action with the structured reference counter is performed. If \textit{var} is a pointer reference, an \texttt{attach} action is performed.
  
  - Otherwise, a \texttt{create action} with the structured reference is performed. If \textit{var} is a pointer reference, an \texttt{attach} action is performed. If a \texttt{zero} modifier appears, the memory is zeroed after the \texttt{create action}.

- At exit from a region, the structured reference counter is used. On an \texttt{exit data} directive, the dynamic reference counter is used.
  
  - If the appropriate reference counter for \textit{var} is zero, no action is taken.
  
  - Otherwise, the reference counter is updated:
On an `exit data` directive with a `finalize` clause, the dynamic reference counter is set to zero.

Otherwise, a `present decrement` action with the appropriate reference counter is performed.

If `var` is a pointer reference, a `detach` action is performed. If both structured and dynamic reference counters are zero, a `copyout` action is performed.

The restrictions in Section 2.7.3 Data Clause Restrictions apply to this clause.

For compatibility with OpenACC 2.0, `present_or_copyout` and `pcopyout` are alternate names for `copyout`.

An `exit data` directive with a `copyout` clause and with or without a `finalize` clause is functionally equivalent to a call to the `acc_copyout_finalize` or `acc_copyout` API routine, respectively, as described in Section 3.2.28.

### 2.7.9 create clause

The `create` clause may appear on structured `data` and compute constructs, on `declare` directives, and on `enter data` directives. The clause may optionally have a `zero` modifier.

For each `var` in `var-list`, if `var` is in shared memory, no action is taken; if `var` is not in shared memory, the `create` clause behaves as follows:

- At entry to a region, the structured reference counter is used. On an `enter data` directive, the dynamic reference counter is used.
  - If `var` is present, a `present increment` action with the appropriate reference counter is performed. If `var` is a pointer reference, an `attach` action is performed.
  - Otherwise, a `create` action with the appropriate reference counter is performed. If `var` is a pointer reference, an `attach` action is performed. If a `zero` modifier appears, the memory is zeroed after the `create` action.

- At exit from the region:
  - If the structured reference counter for `var` is zero, no action is taken.
  - Otherwise, a `present decrement` action with the structured reference counter is performed. If `var` is a pointer reference, a `detach` action is performed. If both structured and dynamic reference counters are zero, a `delete` action is performed.

The restrictions in Section 2.7.3 Data Clause Restrictions apply to this clause.

For compatibility with OpenACC 2.0, `present_or_create` and `pcreate` are alternate names for `create`.

An `enter data` directive with a `create` clause is functionally equivalent to a call to the `acc_create` API routine, as described in Section 3.2.27.

### 2.7.10 no_create clause

The `no_create` clause may appear on structured `data` and compute constructs.
For each var in var-list, if var is in shared memory, no action is taken; if var is not in shared memory, the no_create clause behaves as follows:

- At entry to the region:
  - If var is present, a present increment action with the structured reference counter is performed. If var is a pointer reference, an attach action is performed.
  - Otherwise, no action is performed, and any device code in this construct will use the local memory address for var.

- At exit from the region:
  - If the structured reference counter for var is zero, no action is taken.
  - Otherwise, a present decrement action with the structured reference counter is performed. If var is a pointer reference, a detach action is performed. If both structured and dynamic reference counters are zero, a delete action is performed.

The restrictions in Section 2.7.3 Data Clause Restrictions do not apply to this clause.

The delete clause may appear on exit data directives.

For each var in var-list, if var is in shared memory, no action is taken; if var is not in shared memory, the delete clause behaves as follows:

- If the dynamic reference counter for var is zero, no action is taken.
- Otherwise, the dynamic reference counter is updated:
  - On an exit data directive with a finalize clause, the dynamic reference counter is set to zero.
  - Otherwise, a present decrement action with the dynamic reference counter is performed.

If var is a pointer reference, a detach action is performed. If both structured and dynamic reference counters are zero, a delete action is performed.

An exit data directive with a delete clause and with or without a finalize clause is functionally equivalent to a call to the acc_delete_finalize or acc_delete API routine, respectively, as described in Section 3.2.29.

The restrictions in Section 2.7.3 Data Clause Restrictions apply to this clause.

The attach clause may appear on structured data and compute constructs and on enter data directives. Each var argument to an attach clause must be a C or C++ pointer or a Fortran variable or array with the pointer or allocatable attribute.

For each var in var-list, if var is in shared memory, no action is taken; if var is not in shared memory, the attach clause behaves as follows:

- At entry to a region or at an enter data directive, an attach action is performed.
At exit from the region, a detach action is performed.

2.7.13 detach clause

The detach clause may appear on exit data directives. Each var argument to a detach clause must be a C or C++ pointer or a Fortran variable or array with the pointer or allocatable attribute.

For each var in var-list, if var is in shared memory, no action is taken; if var is not in shared memory, the detach clause behaves as follows:

- If there is a finalize clause on the exit data directive, an immediate detach action is performed.
- Otherwise, a detach action is performed.

2.8 Host_Data Construct

Summary

The host_data construct makes the address of data in device memory available on the host.

Syntax

In C and C++, the syntax of the OpenACC host_data construct is

```c
#pragma acc host_data clause-list new-line
    structured block
```

and in Fortran, the syntax is

```fortran
!$acc host_data clause-list
    structured block
!$acc end host_data
```

or

```fortran
!$acc host_data clause-list
    block construct
[!$acc end host_data]
```

where clause is one of the following:

use_device(var-list)
if(condition)
if_present

Description

This construct is used to make the address of data in device memory available in host code.

Restrictions

- A var in a use_device clause must be the name of a variable or array.
- At least one use_device clause must appear.
- At most one if clause may appear. In Fortran, the condition must evaluate to a scalar logical value; in C or C++, the condition must evaluate to a scalar integer value.
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- See Section 2.17.1 Optional Arguments for discussion of Fortran optional arguments in use_device clauses.

2.8.1 use_device clause

The use_device clause tells the compiler to use the current device address of any var in var-list in code within the construct. In particular, this may be used to pass the device address of var to optimized procedures written in a lower-level API. When there is no if_present clause, and either there is no if clause or the condition in the if clause evaluates to nonzero (in C or C++) or .true. (in Fortran), the var in var-list must be present in the accelerator memory due to data regions or data lifetimes that contain this construct. For data in shared memory, the device address is the same as the host address.

2.8.2 if clause

The if clause is optional. When an if clause appears and the condition evaluates to zero in C or C++, or .false. in Fortran, the compiler will not replace the addresses of any var in code within the construct. When there is no if clause, or when an if clause appears and the condition evaluates to nonzero in C or C++, or .true. in Fortran, the compiler will replace the addresses as described in the previous subsection.

2.8.3 if_present clause

When an if_present clause appears on the directive, the compiler will only replace the address of any var which appears in var-list that is present in the current device memory.

2.9 Loop Construct

Summary

The OpenACC loop construct applies to a loop which must immediately follow this directive. The loop construct can describe what type of parallelism to use to execute the loop and declare private vars and reduction operations.

Syntax

In C and C++, the syntax of the loop construct is

```
#pragma acc loop [clause-list] new-line
   for loop
```

In Fortran, the syntax of the loop construct is

```
!$acc loop [clause-list]
   do loop
```

where clause is one of the following:

- collapse(n)
- gang((gang-arg-list))
- worker([([num:]int-expr)])
- vector([([length:]int-expr)])
- seq
- independent
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```
  auto
tile( size-expr-list )
device_type( device-type-list )
private( var-list )
reduction( operator : var-list )
```

where gang-arg is one of:

```
[num:]int-expr
static:size-expr
```

and gang-arg-list may have at most one `num` and one `static` argument,

and where size-expr is one of:

```
* int-expr
```

Some clauses are only valid in the context of a kernels construct; see the descriptions below.

An orphaned loop construct is a loop construct that is not lexically enclosed within a compute construct. The parent compute construct of a loop construct is the nearest compute construct that lexically contains the loop construct.

A loop construct is data-independent if it has an independent clause that is determined explicitly, implicitly, or from an auto clause. A loop construct is sequential if it has a seq clause that is determined explicitly or from an auto clause.

When do-loop is a do concurrent, the OpenACC loop construct applies to the loop for each index in the concurrent-header. The loop construct can describe what type of parallelism to use to execute all the loops, and declares all indices appearing in the concurrent-header to be implicitly private. If the loop construct that is associated with do concurrent is combined with a compute construct then concurrent-locality is processed as follows: variables appearing in a local are treated as appearing in a private clause; variables appearing in a local_init are treated as appearing in a firstprivate clause; variables appearing in a shared are treated as appearing in a copy clause; and a default(none) locality spec implies a default(none) clause on the compute construct. If the loop construct is not combined with a compute construct, the behavior is implementation-defined.

Restrictions

- Only the collapse, gang, worker, vector, seq, independent, auto, and tile clauses may follow a device_type clause.

- The int-expr argument to the worker and vector clauses must be invariant in the kernels region.

- A loop associated with a loop construct that does not have a seq clause must be written to meet all of the following conditions:
  - The loop variable must be of integer, C/C++ pointer, or C++ random-access iterator type.
  - The loop variable must monotonically increase or decrease in the direction of its termination condition.
– The loop iteration count must be computable in constant time when entering the loop construct.

For a C++ range-based for loop, the loop variable identified by the above conditions is the internal iterator, such as a pointer, that the compiler generates to iterate the range. It is not the variable declared by the for loop.

- Only one of the seq, independent, and auto clauses may appear.
- A gang, worker, or vector clause may not appear if a seq clause appears.
- A tile and collapse clause may not appear on loop that is associated with do concurrent.

2.9.1 collapse clause

The collapse clause is used to specify how many tightly nested loops are associated with the loop construct. The argument to the collapse clause must be a constant positive integer expression. If no collapse clause appears, only the immediately following loop is associated with the loop construct.

If more than one loop is associated with the loop construct, the iterations of all the associated loops are all scheduled according to the rest of the clauses. The trip count for all loops associated with the collapse clause must be computable and invariant in all the loops.

It is implementation-defined whether a gang, worker or vector clause on the construct is applied to each loop, or to the linearized iteration space.

2.9.2 gang clause

When the parent compute construct is a parallel construct, or on an orphaned loop construct, the gang clause specifies that the iterations of the associated loop or loops are to be executed in parallel by distributing the iterations among the gangs created by the parallel construct. A loop construct with the gang clause transitions a compute region from gang-redundant mode to gang-partitioned mode. The number of gangs is controlled by the parallel construct; only the static argument is allowed. The loop iterations must be data independent, except for vars which appear in a reduction clause or which are modified in an atomic region. The region of a loop with the gang clause may not contain another loop with the gang clause unless within a nested compute region.

When the parent compute construct is a kernels construct, the gang clause specifies that the iterations of the associated loop or loops are to be executed in parallel across the gangs. An argument with no keyword or with the num keyword is allowed only when the num_gangs does not appear on the kernels construct. If an argument with no keyword or an argument after the num keyword appears, it specifies how many gangs to use to execute the iterations of this loop. The region of a loop with the gang clause may not contain another loop with a gang clause unless within a nested compute region.

The scheduling of loop iterations to gangs is not specified unless the static modifier appears as an argument. If the static modifier appears with an integer expression, that expression is used as a chunk size. If the static modifier appears with an asterisk, the implementation will select a chunk size. The iterations are divided into chunks of the selected chunk size, and the chunks are assigned to gangs starting with gang zero and continuing in round-robin fashion. Two gang loops
in the same parallel region with the same number of iterations, and with static clauses with the
same argument, will assign the iterations to gangs in the same manner. Two gang loops in the
same kernels region with the same number of iterations, the same number of gangs to use, and with
static clauses with the same argument, will assign the iterations to gangs in the same manner.

A gang clause without arguments is implied on a data-independent loop construct without an
explicit gang clause if the following conditions hold while ignoring gang, worker, and vector
clauses on any sequential loop constructs:

- This loop construct’s parent compute construct, if any, is not a kernels construct.
- An explicit gang clause would be permitted on this loop construct.
- For every lexically enclosing data-independent loop construct, either an explicit gang clause
  would not be permitted on the enclosing loop construct, or the enclosing loop construct
  lexically encloses a compute construct that lexically encloses this loop construct.

Note: As a performance optimization, the implementation might select different levels of paral-
lelism for a loop construct than specified by explicitly or implicitly determined clauses as long
as it can prove program semantics are preserved. In particular, the implementation must consider
semantic differences between gang-redundant and gang-partitioned mode. For example, in a series
of tightly nested, data-independent loop constructs, implementations often move gang-partitioning
from one loop construct to another without affecting semantics.

Note: If the auto or device_type clause appears on a loop construct, it is the programmer’s
responsibility to ensure that program semantics are the same regardless of whether the auto clause
is treated as independent or seq and regardless of the device type for which the program is
compiled. In particular, the programmer must consider the effect on both explicitly and implicitly
determined gang clauses and thus on gang-redundant and gang-partitioned mode. Examples in
Section 2.9.11 demonstrate this issue for the auto clause.

2.9.3 worker clause

When the parent compute construct is a parallel construct, or on an orphaned loop construct,
the worker clause specifies that the iterations of the associated loop or loops are to be executed
in parallel by distributing the iterations among the multiple workers within a single gang. A loop
construct with a worker clause causes a gang to transition from worker-single mode to worker-
partitioned mode. In contrast to the gang clause, the worker clause first activates additional
worker-level parallelism and then distributes the loop iterations across those workers. No argu-
ment is allowed. The loop iterations must be data independent, except for vars which appear in
a reduction clause or which are modified in an atomic region. The region of a loop with the
worker clause may not contain a loop with the gang or worker clause unless within a nested
compute region.

When the parent compute construct is a kernels construct, the worker clause specifies that the
iterations of the associated loop or loops are to be executed in parallel across the workers within
a single gang. An argument is allowed only when the num_workers does not appear on the
kernels construct. The optional argument specifies how many workers per gang to use to execute
the iterations of this loop. The region of a loop with the worker clause may not contain a loop
with a gang or worker clause unless within a nested compute region.
All workers will complete execution of their assigned iterations before any worker proceeds beyond the end of the loop.

### 2.9.4 vector clause

When the parent compute construct is a `parallel` construct, or on an orphaned `loop` construct, the `vector` clause specifies that the iterations of the associated loop or loops are to be executed in vector or SIMD mode. A `loop` construct with a `vector` clause causes a worker to transition from vector-single mode to vector-partitioned mode. Similar to the `worker` clause, the `vector` clause first activates additional vector-level parallelism and then distributes the loop iterations across those vector lanes. The operations will execute using vectors of the length specified or chosen for the parallel region. The loop iterations must be data independent, except for `vars` which appear in a `reduction` clause or which are modified in an `atomic` region. The region of a loop with the `vector` clause may not contain a loop with the `gang`, `worker`, or `vector` clause unless within a nested compute region.

When the parent compute construct is a `kernels` construct, the `vector` clause specifies that the iterations of the associated loop or loops are to be executed with vector or SIMD processing. An argument is allowed only when the `vector_length` does not appear on the `kernels` construct. If an argument appears, the iterations will be processed in vector strips of that length; if no argument appears, the implementation will choose an appropriate vector length. The region of a loop with the `vector` clause may not contain a loop with a `gang`, `worker`, or `vector` clause unless within a nested compute region.

All vector lanes will complete execution of their assigned iterations before any vector lane proceeds beyond the end of the loop.

### 2.9.5 seq clause

The `seq` clause specifies that the associated loop or loops are to be executed sequentially by the accelerator. This clause will override any automatic parallelization or vectorization.

### 2.9.6 independent clause

The `independent` clause tells the implementation that the loop iterations must be data independent, except for `vars` which appear in a `reduction` clause or which are modified in an `atomic` region. This allows the implementation to generate code to execute the iterations in parallel with no synchronization.

A `loop` construct with no `auto` or `seq` clause is treated as if it has the `independent` clause when it is an orphaned `loop` construct or its parent compute construct is a `parallel` construct.

**Note**

- It is likely a programming error to use the `independent` clause on a loop if any iteration writes to a variable or array element that any other iteration also writes or reads, except for `vars` which appear in a `reduction` clause or which are modified in an `atomic` region.
- The implementation may be restricted in the levels of parallelism it can apply by the presence of `loop` constructs with `gang`, `worker`, or `vector` clauses for outer or inner loops.
2.9.7 auto clause

The auto clause specifies that the implementation must analyze the loop and determine whether the loop iterations are data-independent. If it determines that the loop iterations are data-independent, the implementation must treat the auto clause as if it is an independent clause. If not, or if it is unable to make a determination, it must treat the auto clause as if it is a seq clause, and it must ignore any gang, worker, or vector clauses on the loop construct.

When the parent compute construct is a kernels construct, a loop construct with no independent or seq clause is treated as if it has the auto clause.

2.9.8 tile clause

The tile clause specifies that the implementation should split each loop in the loop nest into two loops, with an outer set of tile loops and an inner set of element loops. The argument to the tile clause is a list of one or more tile sizes, where each tile size is a constant positive integer expression or an asterisk. If there are n tile sizes in the list, the loop construct must be immediately followed by n tightly-nested loops. The first argument in the size-expr-list corresponds to the innermost loop of the n associated loops, and the last element corresponds to the outermost associated loop. If the tile size is an asterisk, the implementation will choose an appropriate value. Each loop in the nest will be split or strip-mined into two loops, an outer tile loop and an inner element loop. The trip count of the element loop will be limited to the corresponding tile size from the size-expr-list. The tile loops will be reordered to be outside all the element loops, and the element loops will all be inside the tile loops.

If the vector clause appears on the loop construct, the vector clause is applied to the element loops. If the gang clause appears on the loop construct, the gang clause is applied to the tile loops. If the worker clause appears on the loop construct, the worker clause is applied to the element loops if no vector clause appears, and to the tile loops otherwise.

2.9.9 device_type clause

The device_type clause is described in Section 2.4 Device-Specific Clauses.

2.9.10 private clause

The private clause on a loop construct specifies that a copy of each item in var-list will be created. If the body of the loop is executed in vector-partitioned mode, a copy of the item is created for each thread associated with each vector lane. If the body of the loop is executed in worker-partitioned vector-single mode, a copy of the item is created for and shared across the set of threads associated with all the vector lanes of each worker. Otherwise, a copy of the item is created for and shared across the set of threads associated with all the vector lanes of all the workers of each gang.

Restrictions

- See Section 2.17.1 Optional Arguments for discussion of Fortran optional arguments in private clauses.

2.9.11 reduction clause

The reduction clause specifies a reduction operator and one or more vars. For each reduction var, a private copy is created in the same manner as for a private clause on the loop construct,
and initialized for that operator; see the table in Section 2.5.14: reduction clause. After the loop, the values for each thread are combined using the specified reduction operator, and the result combined with the value of the original var and stored in the original var. If the original var is not private, this update occurs by the end of the compute region, and any access to the original var is undefined within the compute region. Otherwise, the update occurs at the end of the loop. If the reduction var is an array or subarray, the reduction operation is logically equivalent to applying that reduction operation to each array element of the array or subarray individually. If the reduction var is a composite variable, the reduction operation is logically equivalent to applying that reduction operation to each member of the composite variable individually.

If a variable is involved in a reduction that spans multiple nested loops where two or more of those loops have associated loop directives, a reduction clause containing that variable must appear on each of those loop directives.

Restrictions

- A var in a reduction clause must be a scalar variable name, an aggregate variable name, an array element, or a subarray (refer to Section 2.7.1).
- Reduction clauses on nested constructs for the same reduction var must have the same reduction operator.
- Every var in a reduction clause appearing on an orphaned loop construct must be private.
- The restrictions for a reduction clause on a compute construct listed in in Section 2.5.14: reduction clause also apply to a reduction clause on a loop construct.
- See Section 2.17.1: Optional Arguments for discussion of Fortran optional arguments in reduction clauses.
- See Section 2.6.2: Variables with Implicitly Determined Data Attributes for a restriction requiring certain loop reduction variables to have explicit data clauses on their parent compute constructs.

Examples

- x is not private at the loop directive below, so its reduction normally updates x at the end of the parallel region, where gangs synchronize. When possible, the implementation might choose to partially update x at the loop exit instead, or fully if num_gangs(1) were added to the parallel directive. However, portable applications cannot rely on such early updates, so accesses to x are undefined within the parallel region outside the loop.

```c
int x = 0;
#pragma acc parallel copy(x)
{
    // gang-shared x undefined
    #pragma acc loop gang worker vector reduction(+:x)
    for (int i = 0; i < I; ++i)
        x += 1; // vector-private x modified
    // gang-shared x undefined
```
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```c
int x = 0;
#pragma acc parallel copy(x)
{
    // gang-shared x undefined
    #pragma acc loop gang reduction(+:x)
    for (int i = 0; i < I; ++i) {
        #pragma acc loop worker reduction(+:x)
        for (int j = 0; j < J; ++j) {
            #pragma acc loop vector reduction(+:x)
            for (int k = 0; k < K; ++k) {
                x += 1; // vector-private x modified
            } // worker-private x updated for vector reduction
        } // gang-private x updated for worker reduction
    } // gang-shared x undefined
} // gang-shared x updated for gang reduction
// x = I * J * K
```

- At each loop directive below, x is private and y is not private due to the data clauses on the parallel directive. Thus, each reduction updates x at the loop exit, but each reduction updates y by the end of the parallel region instead.

```c
int x = 0, y = 0;
#pragma acc parallel firstprivate(x) copy(y)
{
    // gang-private x = 0; gang-shared y undefined
    #pragma acc loop seq reduction(+:x,y)
    for (int i = 0; i < I; ++i) {
        x += 1; y += 2; // loop-private x and y modified
    } // gang-private x updated for seq reduction (trivial reduction)
    // gang-private x = I; gang-shared y undefined
    #pragma acc loop worker reduction(+:x,y)
    for (int i = 0; i < I; ++i) {
        x += 1; y += 2; // worker-private x and y modified
    } // gang-private x updated for worker reduction
    // gang-private x = 2 * I; gang-shared y undefined
    #pragma acc loop vector reduction(+:x,y)
    for (int i = 0; i < I; ++i) {
        x += 1; y += 2; // vector-private x and y modified
    } // gang-private x updated for vector reduction
    // gang-private x = 3 * I; gang-shared y undefined
    #pragma acc loop reduction(+:x,y)
    for (int i = 0; i < I; ++i) {
        x += 1; y += 2; // gang-private x and y modified
    } // gang-shared y updated for gang/seq/worker/vector reductions
    // x = 0; y = 3 * I * 2
```
The examples below are equivalent. That is, the reduction clause on the combined construct applies to the loop construct but implies a copy clause on the parallel construct. Thus, $x$ is not private at the loop directive, so the reduction updates $x$ by the end of the parallel region.

```cpp
int x = 0;
#pragma acc parallel loop worker reduction(+:x)
for (int i = 0; i < I; ++i) {
  x += 1; // worker-private x modified
} // gang-shared x updated for gang/worker reduction
// x = I
```

```
int x = 0;
#pragma acc parallel copy(x)
{
  // gang-shared x undefined
  #pragma acc loop worker reduction(+:x)
  for (int i = 0; i < I; ++i) {
    x += 1; // worker-private x modified
  }
  // gang-shared x undefined
} // gang-shared x updated for gang/worker reduction
// x = I
```

- If the implementation treats the auto clause below as independent, the loop executes in gang-partitioned mode and thus examines every element of `arr` once to compute `arr`’s maximum. However, if the implementation treats auto as seq, the gangs redundantly compute `arr`’s maximum, but the combined result is still `arr`’s maximum. Either way, because $x$ is not private at the loop directive, the reduction updates $x$ by the end of the parallel region.

```cpp
int x = 0;
const int *arr = /*array of I values*/;
#pragma acc parallel copy(x)
{
  // gang-shared x undefined
  #pragma acc loop auto gang reduction(max:x)
  for (int i = 0; i < I; ++i) {
    // complex loop body
    x = x < arr[i] ? arr[i] : x; // gang or loop-private x modified
  }
  // gang-shared x undefined
} // gang-shared x updated for gang or gang/seq reduction
// x = arr maximum
```

- The following example is the same as the previous one except that the reduction operator is now +. While gang-partitioned mode sums the elements of `arr` once, gang-redundant mode sums them once per gang, producing a result many times `arr`’s sum. This example shows that, for some reduction operators, combining auto, gang, and reduction is typically non-portable.
int x = 0;
const int *arr = /*array of I values*/;
#pragma acc parallel copy(x)
{
    // gang-shared x undefined
    #pragma acc loop auto gang reduction(+:x)
    for (int i = 0; i < I; ++i) {
        // complex loop body
        x += arr[i]; // gang or loop-private x modified
    }
    // gang-shared x updated for gang or gang/seq reduction
    // x = arr sum possibly times number of gangs
}

• At the following loop directive, x and z are private, so the loop reductions are not across gangs even though the loop is gang-partitioned. Nevertheless, the reduction clause on the loop directive is important as the loop is also vector-partitioned. These reductions are only partial reductions relative to the full set of values computed by the loop, so the reduction clause is needed on the parallel directive to reduce across gangs.

int x = 0, y = 0;
#pragma acc parallel copy(x) reduction(+:x,y)
{
    int z = 0;
    #pragma acc loop gang vector reduction(+:x,z)
    for (int i = 0; i < I; ++i) {
        x += 1; z += 2; // vector-private x and z modified
    } // gang-private x and z updated for vector reduction (trivial 1-gang reduction)
    y += z; // gang-private y modified
    } // gang-shared x and y updated for gang reduction
    // x = I; y = I * 2

2.10 Cache Directive

Summary
The cache directive may appear at the top of (inside of) a loop. It specifies array elements or subarrays that should be fetched into the highest level of the cache for the body of the loop.

Syntax
In C and C++, the syntax of the cache directive is

```c
#pragma acc cache([readonly:]var-list ) new-line
```

In Fortran, the syntax of the cache directive is

```fortran
!$acc cache([readonly:]var-list )
```

A var in a cache directive must be a single array element or a simple subarray. In C and C++, a simple subarray is an array name followed by an extended array range specification in brackets,
with start and length, such as

\[
\text{arr}[\text{lower} : \text{length}]
\]

where the lower bound is a constant, loop invariant, or the \texttt{for} loop variable plus or minus a constant or loop invariant, and the length is a constant.

In Fortran, a simple subarray is an array name followed by a comma-separated list of range specifications in parentheses, with lower and upper bound subscripts, such as

\[
\text{arr}(\text{lower} : \text{upper}, \text{lower2} : \text{upper2})
\]

The lower bounds must be constant, loop invariant, or the \texttt{do} loop variable plus or minus a constant or loop invariant; moreover the difference between the corresponding upper and lower bounds must be a constant.

If the optional \texttt{readonly} modifier appears, then the implementation may assume that the data referenced by any \texttt{var} in that directive is never written to within the applicable region.

**Restrictions**

- If an array element or subarray is listed in a \texttt{cache} directive, all references to that array during execution of that loop iteration must not refer to elements of the array outside the index range specified in the \texttt{cache} directive.

- See Section 2.17.1 [Optional Arguments] for discussion of Fortran optional arguments in \texttt{cache} directives.

### 2.11 Combined Constructs

**Summary**

The combined OpenACC \texttt{parallel loop, kernels loop}, and \texttt{serial loop} constructs are shortcuts for specifying a \texttt{loop} construct nested immediately inside a \texttt{parallel, kernels}, or \texttt{serial} construct. The meaning is identical to explicitly specifying a \texttt{parallel, kernels}, or \texttt{serial} construct containing a \texttt{loop} construct. Any clause that is allowed on a \texttt{parallel} or \texttt{loop} construct is allowed on the \texttt{parallel loop} construct; any clause allowed on a \texttt{kernels} or \texttt{loop} construct is allowed on a \texttt{kernels loop} construct; and any clause allowed on a \texttt{serial} or \texttt{loop} construct is allowed on a \texttt{serial loop} construct.

**Syntax**

In C and C++, the syntax of the \texttt{parallel loop} construct is

\[
\texttt{#pragma acc parallel loop[clause-list] new-line for loop}
\]

In Fortran, the syntax of the \texttt{parallel loop} construct is

\[
\texttt{!!$_\text{acc parallel loop [clause-list] do loop [!!$_\text{acc end parallel loop}$]}
\]

The associated structured block is the loop which must immediately follow the directive. Any of the \texttt{parallel} or \texttt{loop} clauses valid in a parallel region may appear.

In C and C++, the syntax of the \texttt{kernels loop} construct is
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#pragma acc kernels loop [clause-list] new-line
   for loop

In Fortran, the syntax of the kernels loop construct is

!$acc kernels loop [clause-list]
   do loop

[!$acc end kernels loop]

The associated structured block is the loop which must immediately follow the directive. Any of the kernels or loop clauses valid in a kernels region may appear.

In C and C++, the syntax of the serial loop construct is

#pragma acc serial loop [clause-list] new-line
   for loop

In Fortran, the syntax of the serial loop construct is

!$acc serial loop [clause-list]
   do loop

[!$acc end serial loop]

The associated structured block is the loop which must immediately follow the directive. Any of the serial or loop clauses valid in a serial region may appear.

A private or reduction clause on a combined construct is treated as if it appeared on the loop construct. In addition, a reduction clause on a combined construct implies copy clauses as described in Section 2.6.2.

Restrictions

- The restrictions for the parallel, kernels, serial, and loop constructs apply.

2.12 Atomic Construct

Summary

An atomic construct ensures that a specific storage location is accessed and/or updated atomically, preventing simultaneous reading and writing by gangs, workers, and vector threads that could result in indeterminate values.

Syntax

In C and C++, the syntax of the atomic constructs is:

#pragma acc atomic [atomic-clause] new-line
   expression-stmt

or:

#pragma acc atomic capture new-line
   structured block

Where atomic-clause is one of read, write, update, or capture. The expression-stmt is an expression statement with one of the following forms:

If the atomic-clause is read:
If the atomic-clause is **write**:

```c
v = x;
```

If the atomic-clause is **update** or no clause appears:

```c
x = expr;
```

If the atomic-clause is **capture**:

```c
v = x++;
```

The **structured-block** is a structured block with one of the following forms:

```c
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
{v = x; x = expr; v = x;}
```

In the preceding expressions:

- \( x \) and \( v \) (as applicable) are both l-value expressions with scalar type.
- During the execution of an atomic region, multiple syntactic occurrences of \( x \) must designate the same storage location.
- Neither of \( v \) and \( expr \) (as applicable) may access the storage location designated by \( x \).
- Neither of \( x \) and \( expr \) (as applicable) may access the storage location designated by \( v \).
• **expr** is an expression with scalar type.

• **binop** is one of ‐, *, /, ^, |, <<, or >>.

• **binop**, **binop=**, ++, and -- are not overloaded operators.

• The expression **x binop expr** must be mathematically equivalent to **x binop (expr)**. This requirement is satisfied if the operators in expr have precedence greater than binop, or by using parentheses around expr or subexpressions of expr.

• The expression **expr binop x** must be mathematically equivalent to **(expr) binop x**. This requirement is satisfied if the operators in expr have precedence equal to or greater than binop, or by using parentheses around expr or subexpressions of expr.

• For forms that allow multiple occurrences of x, the number of times that x is evaluated is unspecified.

In Fortran the syntax of the **atomic** constructs is:

```
!$acc atomic read
  capture-statement
![$acc end atomic]
```

or

```
!$acc atomic write
  write-statement
![$acc end atomic]
```

or

```
!$acc atomic[update]
  update-statement
![$acc end atomic]
```

or

```
!$acc atomic capture
  capture-statement
  update-statement
!$acc end atomic
```

or

```
!$acc atomic capture
  capture-statement
  write-statement
!$acc end atomic
```

where **write-statement** has the following form (if **atomic-clause** is **write** or **capture**):
where \( x \) is an expression.

\[
\text{v} = x
\]

and where \( \text{update-statement} \) has one of the following forms (if \( \text{atomic-clause} \) is \text{update}, \text{capture}, or no clause appears):

\[
x = x \text{ operator } expr
\]
\[
x = expr \text{ operator } x
\]
\[
x = \text{intrinsic\_procedure\_name}( x, \text{expr-list} )
\]
\[
x = \text{intrinsic\_procedure\_name}( \text{expr-list}, x )
\]

In the preceding statements:

- \( x \) and \( v \) (as applicable) are both scalar variables of intrinsic type.
- \( x \) must not be an allocatable variable.
- During the execution of an atomic region, multiple syntactic occurrences of \( x \) must designate the same storage location.
- None of \( v \), \( expr \), and \( expr\_list \) (as applicable) may access the same storage location as \( x \).
- None of \( x \), \( expr \), and \( expr\_list \) (as applicable) may access the same storage location as \( v \).
- \( expr \) is a scalar expression.
- \( expr\_list \) is a comma-separated, non-empty list of scalar expressions. If \( \text{intrinsic\_procedure\_name} \) refers to \text{iand}, \text{ior}, or \text{ieor}, exactly one expression must appear in \( expr\_list \).
- \( \text{intrinsic\_procedure\_name} \) is one of \text{max}, \text{min}, \text{iand}, \text{ior}, or \text{ieor}. \( \text{operator} \) is one of \(+\), \(-\), \\
  \( \cdot\), \(/\), \( .\text{and} \), \( .\text{or} \), \( .\text{eqv} \), or \( .\text{neqv} \).
- The expression \( x \text{ operator } expr \) must be mathematically equivalent to \( x \text{ operator } (expr) \). This requirement is satisfied if the operators in \( expr \) have precedence greater than \( \text{operator} \), or by using parentheses around \( expr \) or subexpressions of \( expr \).
- The expression \( expr \text{ operator } x \) must be mathematically equivalent to \( (expr) \text{ operator } x \). This requirement is satisfied if the operators in \( expr \) have precedence equal to or greater than \( \text{operator} \), or by using parentheses around \( expr \) or subexpressions of \( expr \).
- \( \text{intrinsic\_procedure\_name} \) must refer to the intrinsic procedure name and not to other program entities.
- \( \text{operator} \) must refer to the intrinsic operator and not to a user-defined operator. All assignments must be intrinsic assignments.
- For forms that allow multiple occurrences of \( x \), the number of times that \( x \) is evaluated is unspecified.

An atomic construct with the \text{read} clause forces an atomic read of the location designated by \( x \).
An atomic construct with the \text{write} clause forces an atomic write of the location designated by \( x \).
An atomic construct with the \text{update} clause forces an atomic update of the location designated by \( x \) using the designated operator or intrinsic. Note that when no clause appears, the semantics
are equivalent to **atomic update**. Only the read and write of the location designated by $x$ are performed mutually atomically. The evaluation of $\text{expr}$ or $\text{expr-list}$ need not be atomic with respect to the read or write of the location designated by $x$.

An **atomic** construct with the **capture** clause forces an atomic update of the location designated by $x$ using the designated operator or intrinsic while also capturing the original or final value of the location designated by $x$ with respect to the atomic update. The original or final value of the location designated by $x$ is written into the location designated by $v$ depending on the form of the **atomic** construct structured block or statements following the usual language semantics. Only the read and write of the location designated by $x$ are performed mutually atomically. Neither the evaluation of $\text{expr}$ or $\text{expr-list}$, nor the write to the location designated by $v$, need to be atomic with respect to the read or write of the location designated by $x$.

For all forms of the **atomic** construct, any combination of two or more of these **atomic** constructs enforces mutually exclusive access to the locations designated by $x$. To avoid race conditions, all accesses of the locations designated by $x$ that could potentially occur in parallel must be protected with an **atomic** construct.

Atomic regions do not guarantee exclusive access with respect to any accesses outside of atomic regions to the same storage location $x$ even if those accesses occur during the execution of a reduction clause.

If the storage location designated by $x$ is not size-aligned (that is, if the byte alignment of $x$ is not a multiple of the size of $x$), then the behavior of the atomic region is implementation-defined.

**Restrictions**

- All atomic accesses to the storage locations designated by $x$ throughout the program are required to have the same type and type parameters.
- Storage locations designated by $x$ must be less than or equal in size to the largest available native atomic operator width.

### 2.13 Declare Directive

**Summary**

A **declare** directive is used in the declaration section of a Fortran subroutine, function, block construct, or module, or following a variable declaration in C or C++. It can specify that a var is to be allocated in device memory for the duration of the implicit data region of a function, subroutine or program, and specify whether the data values are to be transferred from local memory to device memory upon entry to the implicit data region, and from device memory to local memory upon exit from the implicit data region. These directives create a visible device copy of the var.

**Syntax**

In C and C++, the syntax of the **declare** directive is:

```c
#pragma acc declare clause-list new-line
```

In Fortran the syntax of the **declare** directive is:

```fortran
!$acc declare clause-list
```

where clause is one of the following:

---

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2.13. Declare Directive

The associated region is the implicit region associated with the function, subroutine, or program in which the directive appears. If the directive appears in the declaration section of a Fortran module subprogram, for a Fortran common block, or in a C or C++ global or namespace scope, the associated region is the implicit region for the whole program. The `copy`, `copyin`, `copyout`, `present`, and `deviceptr` data clauses are described in Section 2.7 Data Clauses.

Restrictions

- A `declare` directive must be in the same scope as the declaration of any `var` that appears in the clauses of the directive or any scope within a C or C++ function or Fortran function, subroutine, or program.
- At least one clause must appear on a `declare` directive.
- A `var` in a `declare` declare must be a variable or array name, or a Fortran common block name between slashes.
- A `var` may appear at most once in all the clauses of `declare` directives for a function, subroutine, program, or module.
- In Fortran, assumed-size dummy arrays may not appear in a `declare` directive.
- In Fortran, pointer arrays may appear, but pointer association is not preserved in device memory.
- In a Fortran module declaration section, only `create`, `copyin`, `device_resident`, and `link` clauses are allowed.
- In C or C++ global or namespace scope, only `create`, `copyin`, `deviceptr`, `device_resident` and `link` clauses are allowed.
- C and C++ `extern` variables may only appear in `create`, `copyin`, `deviceptr`, `device_resident` and `link` clauses on a `declare` directive.
- In C or C++, the `link` clause must appear at global or namespace scope or the arguments must be `extern` variables. In Fortran, the `link` clause must appear in a `module` declaration section, or the arguments must be `common block` names enclosed in slashes.
- In C or C++, a `longjmp` call in the region must return to a `setjmp` call within the region.
- In C++, an exception thrown in the region must be handled within the region.
- See Section 2.17.1 Optional Arguments for discussion of Fortran optional dummy arguments in data clauses, including `device_resident` clauses.
2.13.1 device_resident clause

Summary
The device_resident clause specifies that the memory for the named variables should be allocated in the current device memory and not in local memory. The host may not be able to access variables in a device_resident clause. The accelerator data lifetime of global variables or common blocks that appear in a device_resident clause is the entire execution of the program.

In Fortran, if the variable has the Fortran allocatable attribute, the memory for the variable will be allocated in and deallocated from the current device memory when the host thread executes an allocate or deallocate statement for that variable, if the current device is a non-shared memory device. If the variable has the Fortran pointer attribute, it may be allocated or deallocated by the host in the current device memory, or may appear on the left hand side of a pointer assignment statement, if the right hand side variable itself appears in a device_resident clause.

In Fortran, the argument to a device_resident clause may be a common block name enclosed in slashes; in this case, all declarations of the common block must have a matching device_resident clause. In this case, the common block will be statically allocated in device memory, and not in local memory. The common block will be available to accelerator routines; see Section 2.15 Procedure Calls in Compute Regions.

In a Fortran module declaration section, a var in a device_resident clause will be available to accelerator subprograms.

In C or C++ global scope, a var in a device_resident clause will be available to accelerator routines. A C or C++ extern variable may appear in a device_resident clause only if the actual declaration and all extern declarations are also followed by device_resident clauses.

2.13.2 create clause

For data in shared memory, no action is taken.

For data not in shared memory, the create clause on a declare directive behaves as follows, for each var in var-list:

- At entry to an implicit data region where the declare directive appears:
  - If var is present, a present_increment action with the structured reference counter is performed. If var is a pointer reference, an attach action is performed.
  - Otherwise, a create action with the structured reference counter is performed. If var is a pointer reference, an attach action is performed.

- At exit from an implicit data region where the declare directive appears:
  - If the structured reference counter for var is zero, no action is taken.
  - Otherwise, a present_decrement action with the structured reference counter is performed. If var is a pointer reference, a detach action is performed. If both structured and dynamic reference counters are zero, a delete action is performed.

If the declare directive appears in a global context, then the data in var-list is statically allocated in device memory and the structured reference counter is set to one.

In Fortran, if a variable var in var-list has the Fortran allocatable or pointer attribute, then:
• An `allocate` statement for `var` will allocate memory in both local memory as well as in the current device memory, for a non-shared memory device, and the dynamic reference counter will be set to one.

• A `deallocate` statement for `var` will deallocate memory from both local memory as well as the current device memory, for a non-shared memory device, and the dynamic reference counter will be set to zero. If the structured reference counter is not zero, a runtime error is issued.

In Fortran, if a variable `var` in `var-list` has the Fortran `pointer` attribute, then it may appear on the left hand side of a pointer assignment statement, if the right hand side variable itself appears in a `create` clause.

### 2.13.3 link clause

The `link` clause is used for large global host static data that is referenced within an accelerator routine and that should have a dynamic data lifetime on the device. The `link` clause specifies that only a global link for the named variables should be statically created in accelerator memory. The host data structure remains statically allocated and globally available. The device data memory will be allocated only when the global variable appears on a data clause for a `data` construct, compute construct, or `enter data` directive. The arguments to the `link` clause must be global data. A `declare link` clause must be visible everywhere the global variables or common block variables are explicitly or implicitly used in a data clause, compute construct, or accelerator routine. The global variable or `common block` variables may be used in accelerator routines. The accelerator data lifetime of variables or common blocks that appear in a `link` clause is the data region that allocates the variable or common block with a data clause, or from the execution of the `enter data` directive that allocates the data until an `exit data` directive deallocates it or until the end of the program.

### 2.14 Executable Directives

#### 2.14.1 Init Directive

**Summary**

The `init` directive tells the runtime to initialize the runtime for that device type. This can be used to isolate any initialization cost from the computational cost, when collecting performance statistics. If no device type appears all devices will be initialized. An `init` directive may be used in place of a call to the `acc_init` runtime API routine, as described in Section 3.2.7.

**Syntax**

In C and C++, the syntax of the `init` directive is:

```
#pragma acc init [clause-list] new-line
```

In Fortran the syntax of the `init` directive is:

```
!$acc init [clause-list]
```

where `clause` is one of the following:

- `device_type (device-type-list )`
- `device_num ( int-expr )`
device_type clause

The device_type clause specifies the type of device that is to be initialized in the runtime. If the device_type clause appears, then the acc-current-device-type-var for the current thread is set to the argument value. If no device_num clause appears then all devices of this type are initialized.

device_num clause

The device_num clause specifies the device id to be initialized. If the device_num clause appears, then the acc-current-device-num-var for the current thread is set to the argument value. If no device_type clause appears, then the specified device id will be initialized for all available device types.

if clause

The if clause is optional; when there is no if clause, the implementation will generate code to perform the initialization unconditionally. When an if clause appears, the implementation will generate code to conditionally perform the initialization only when the condition evaluates to nonzero in C or C++, or .true. in Fortran.

Restrictions

• This directive may not be called within a compute region.

• If the device type specified is not available, the behavior is implementation-defined; in particular, the program may abort.

• If the directive is called more than once without an intervening acc_shutdown call or shutdown directive, with a different value for the device type argument, the behavior is implementation-defined.

• If some accelerator regions are compiled to only use one device type, using this directive with a different device type may produce undefined behavior.

2.14.2 Shutdown Directive

Summary

The shutdown directive tells the runtime to shut down the connection to the given accelerator, and free any runtime resources. This ends all data lifetimes in device memory, which effectively sets structured and dynamic reference counters to zero. A shutdown directive may be used in place of a call to the acc_shutdown runtime API routine, as described in Section 3.2.8.

Syntax

In C and C++, the syntax of the shutdown directive is:

```c
#pragma acc shutdown [clause-list] new-line
```

In Fortran the syntax of the shutdown directive is:

```fortran
!$acc shutdown [clause-list]
```
where clause is one of the following:

```plaintext
device_type (device-type-list)
device_num (int-expr)
if (condition)
```

**device_type clause**

The `device_type` clause specifies the type of device that is to be disconnected from the runtime. If no `device_num` clause appears then all devices of this type are disconnected.

**device_num clause**

The `device_num` clause specifies the device id to be disconnected. If no clauses appear then all available devices will be disconnected.

**if clause**

The `if` clause is optional; when there is no `if` clause, the implementation will generate code to perform the shutdown unconditionally. When an `if` clause appears, the implementation will generate code to conditionally perform the shutdown only when the `condition` evaluates to nonzero in C or C++, or `.true.` in Fortran.

**Restrictions**

- This directive may not be used during the execution of a compute region.

### 2.14.3 Set Directive

**Summary**

The `set` directive provides a means to modify internal control variables using directives. Each form of the `set` directive is functionally equivalent to a matching runtime API routine.

**Syntax**

In C and C++, the syntax of the `set` directive is:

```plaintext
#pragma acc set [clause-list] new-line
```

In Fortran the syntax of the `set` directive is:

```fortran
!$acc set [clause-list]
```

where clause is one of the following:

```plaintext
default_async (int-expr)
device_num (int-expr)
device_type (device-type-list)
if (condition)
```
The OpenACC API 2.14. Executable Directives

**default_async clause**

The `default_async` clause specifies the asynchronous queue that should be used if no queue appears and changes the value of `acc-default-async-var` for the current thread to the argument value. If the value is `acc_async_default`, the value of `acc-default-async-var` will revert to the initial value, which is implementation-defined. A `set default_async` directive is functionally equivalent to a call to the `acc_set_default_async` runtime API routine, as described in Section 3.2.2.

**device_num clause**

The `device_num` clause specifies the device number to set as the default device for accelerator regions and changes the value of `acc-current-device-num-var` for the current thread to the argument value. If the value of `device_num` argument is negative, the runtime will revert to the default behavior, which is implementation-defined. A `set device_num` directive is functionally equivalent to the `acc_set_device_num` runtime API routine, as described in Section 3.2.4.

**device_type clause**

The `device_type` clause specifies the device type to set as the default device type for accelerator regions and sets the value of `acc-current-device-type-var` for the current thread to the argument value. If the value of the `device_type` argument is zero or the clause does not appear, the selected device number will be used for all attached accelerator types. A `set device_type` directive is functionally equivalent to a call to the `acc_set_device_type` runtime API routine, as described in Section 3.2.2.

**if clause**

The `if` clause is optional; when there is no `if` clause, the implementation will generate code to perform the set operation unconditionally. When an `if` clause appears, the implementation will generate code to conditionally perform the set operation only when the `condition` evaluates to nonzero in C or C++, or `.true.` in Fortran.

**Restrictions**

- This directive may not be used within a compute region.
- Passing `default_async` the value of `acc_async_noval` has no effect.
- Passing `default_async` the value of `acc_async_sync` will cause all asynchronous directives in the default asynchronous queue to become synchronous.
- Passing `default_async` the value of `acc_async_default` will restore the default asynchronous queue to the initial value, which is implementation-defined.
- If the value of `device_num` is larger than the maximum supported value for the given type, the behavior is implementation-defined.
- At least one `default_async`, `device_num`, or `device_type` clause must appear.
- Two instances of the same clause may not appear on the same directive.
2.14.4 Update Directive

Summary

The `update` directive is used during the lifetime of accelerator data to update `vars` in local memory with values from the corresponding data in device memory, or to update `vars` in device memory with values from the corresponding data in local memory.

Syntax

In C and C++, the syntax of the `update` directive is:

```c
#pragma acc update clause-list new-line
```

In Fortran the syntax of the `update` data directive is:

```fortran
!$acc update clause-list
```

where `clause` is one of the following:

- `async` [(int-expr)]
- `wait` [(wait-argument)]
- `device_type` (device-type-list)
- `if` (condition)
- `if_present`
- `self` (var-list)
- `host` (var-list)
- `device` (var-list)

Multiple subarrays of the same array may appear in a `var-list` of the same or different clauses on the same directive. The effect of an `update` clause is to copy data from device memory to local memory for `update self`, and from local memory to device memory for `update device`. The updates are done in the order in which they appear on the directive.

Restrictions

- At least one `self`, `host`, or `device` clause must appear on an `update` directive.

**self clause**

The `self` clause specifies that the `vars` in `var-list` are to be copied from the current device memory to local memory for data not in shared memory. For data in shared memory, no action is taken. An `update` directive with the `self` clause is equivalent to a call to the `acc_update_self` routine, described in Section 3.2.31.

**host clause**

The `host` clause is a synonym for the `self` clause.

**device clause**

The `device` clause specifies that the `vars` in `var-list` are to be copied from local memory to the current device memory, for data not in shared memory. For data in shared memory, no action is taken. An `update` directive with the `device` clause is equivalent to a call to the `acc_update_device` routine, described in Section 3.2.30.
The if clause is optional; when there is no if clause, the implementation will generate code to perform the updates unconditionally. When an if clause appears, the implementation will generate code to conditionally perform the updates only when the condition evaluates to nonzero in C or C++, or .true. in Fortran.

The async clause is optional; see Section 2.16 Asynchronous Behavior for more information.

The wait clause is optional; see Section 2.16 Asynchronous Behavior for more information.

When an if_present clause appears on the directive, no action is taken for a var which appears in var-list that is not present in the current device memory. When no if_present clause appears, all vars in a device or self clause must be present in the current device memory, and an implementation may halt the program with an error message if some data is not present.

Restrictions

- The update directive is executable. It must not appear in place of the statement following an if, while, do, switch, or label in C or C++, or in place of the statement following a logical if in Fortran.
- If no if_present clause appears on the directive, each var in var-list must be present in the current device memory.
- Only the async and wait clauses may follow a device_type clause.
- At most one if clause may appear. In Fortran, the condition must evaluate to a scalar logical value; in C or C++, the condition must evaluate to a scalar integer value.
- Noncontiguous subarrays may appear. It is implementation-specific whether noncontiguous regions are updated by using one transfer for each contiguous subregion, or whether the non-contiguous data is packed, transferred once, and unpacked, or whether one or more larger subarrays (no larger than the smallest contiguous region that contains the specified subarray) are updated.
- In C and C++, a member of a struct or class may appear, including a subarray of a member. Members of a subarray of struct or class type may not appear.
- In C and C++, if a subarray notation is used for a struct member, subarray notation may not be used for any parent of that struct member.
- In Fortran, members of variables of derived type may appear, including a subarray of a member. Members of subarrays of derived type may not appear.
- In Fortran, if array or subarray notation is used for a derived type member, array or subarray notation may not be used for a parent of that derived type member.
- See Section 2.17.1 Optional Arguments for discussion of Fortran optional arguments in self, host, and device clauses.
2.14.5 Wait Directive

See Section 2.16 Asynchronous Behavior for more information.

2.14.6 Enter Data Directive

See Section 2.6.6 Enter Data and Exit Data Directives for more information.

2.14.7 Exit Data Directive

See Section 2.6.6 Enter Data and Exit Data Directives for more information.

2.15 Procedure Calls in Compute Regions

This section describes how routines are compiled for an accelerator and how procedure calls are compiled in compute regions. See Section 2.17.1 Optional Arguments for discussion of Fortran optional arguments in procedure calls inside compute regions.

2.15.1 Routine Directive

Summary

The routine directive is used to tell the compiler to compile a given procedure or a C++ lambda for an accelerator as well as for the host. In a file or routine with a procedure call, the routine directive tells the implementation the attributes of the procedure when called on the accelerator.

Syntax

In C and C++, the syntax of the routine directive is:

```c#
#pragma acc routine clause-list new-line
#pragma acc routine(name) clause-list new-line
```

In C and C++, the routine directive without a name may appear immediately before a function definition, a C++ lambda, or just before a function prototype and applies to that immediately following function or prototype. The routine directive with a name may appear anywhere that a function prototype is allowed and applies to the function or the C++ lambda in that scope with that name, but must appear before any definition or use of that function.

In Fortran the syntax of the routine directive is:

```fortran
!$acc routine clause-list
!$acc routine(name) clause-list
```

In Fortran, the routine directive without a name may appear within the specification part of a subroutine or function definition, or within an interface body for a subroutine or function in an interface block, and applies to the containing subroutine or function. The routine directive with a name may appear in the specification part of a subroutine, function or module, and applies to the named subroutine or function.

A C or C++ function or Fortran subprogram compiled with the routine directive for an accelerator is called an accelerator routine.

If an accelerator routine is a C++ lambda, the associated function will be compiled for both the accelerator and the host.
If a \textit{lambda} is called in a compute region and it is not an \textit{accelerator routine}, then the \textit{lambda} is treated as if its name appears in the name list of a \texttt{routine} directive with \texttt{seq} clause. If \textit{lambda} is defined in an \textit{accelerator routine} that has a \texttt{nohost} clause then the \textit{lambda} is treated as if its name appears in the name list of a \texttt{routine} directive with a \texttt{nohost} clause.

The \texttt{clause} is one of the following:

\begin{verbatim}
  gang
  worker
  vector
  seq
  bind( name )
  bind( string )
  device_type( device-type-list )
  nohost
\end{verbatim}

A \texttt{gang}, \texttt{worker}, \texttt{vector}, or \texttt{seq} clause specifies the \textit{level of parallelism} in the routine.

\textbf{gang clause}

The \texttt{gang} clause specifies that the procedure contains, may contain, or may call another procedure that contains a loop with a \texttt{gang} clause. A call to this procedure must appear in code that is executed in \texttt{gang-redundant} mode, and all gangs must execute the call. For instance, a procedure with a \texttt{routine gang} directive may not be called from within a loop that has a \texttt{gang} clause. Only one of the \texttt{gang}, \texttt{worker}, \texttt{vector} and \texttt{seq} clauses may appear for each device type.

\textbf{worker clause}

The \texttt{worker} clause specifies that the procedure contains, may contain, or may call another procedure that contains a loop with a \texttt{worker} clause, but does not contain nor does it call another procedure that contains a loop with the \texttt{gang} clause. A loop in this procedure with an \texttt{auto} clause may be selected by the compiler to execute in \texttt{worker} or \texttt{vector} mode. A call to this procedure must appear in code that is executed in \texttt{worker-single} mode, though it may be in \texttt{gang-redundant} or \texttt{gang-partitioned} mode. For instance, a procedure with a \texttt{routine worker} directive may be called from within a loop that has the \texttt{gang} clause, but not from within a loop that has the \texttt{worker} clause. Only one of the \texttt{gang}, \texttt{worker}, \texttt{vector}, and \texttt{seq} clauses may appear for each device type.

\textbf{vector clause}

The \texttt{vector} clause specifies that the procedure contains, may contain, or may call another procedure that contains a loop with the \texttt{vector} clause, but does not contain nor does it call another procedure that contains a loop with either a \texttt{gang} or \texttt{worker} clause. A loop in this procedure with an \texttt{auto} clause may be selected by the compiler to execute in \texttt{vector} mode, but not \texttt{worker} mode. A call to this procedure must appear in code that is executed in \texttt{vector-single} mode, though it may be in \texttt{gang-redundant} or \texttt{gang-partitioned} mode, and in \texttt{worker-single} or \texttt{worker-partitioned} mode. For instance, a procedure with a \texttt{routine vector} directive may be called from within a loop that has the \texttt{gang} clause or the \texttt{worker} clause, but not from within a loop that has the \texttt{vector} clause. Only one of the \texttt{gang}, \texttt{worker}, \texttt{vector}, and \texttt{seq} clauses may appear for each device type.
The seq clause specifies that the procedure does not contain nor does it call another procedure that contains a loop with a gang, worker, or vector clause. A loop in this procedure with an auto clause will be executed in seq mode. A call to this procedure may appear in any mode. Only one of the gang, worker, vector and seq clauses may appear for each device type.

The bind clause specifies the name to use when calling the procedure on a device other than the host. If the name is specified as an identifier, it is called as if that name were specified in the language being compiled. If the name is specified as a string, the string is used for the procedure name unmodified. A bind clause on a procedure definition behaves as if it had appeared on a declaration by changing the name used to call the function on a device other than the host; however, the procedure is not compiled for the device with either the original name or the name in the bind clause.

If there is both a Fortran bind and an acc bind clause for a procedure definition then a call on the host will call the Fortran bound name and a call on another device will call the name in the bind clause.

device_type clause

The device_type clause is described in Section 2.4 Device-Specific Clauses.

nohost clause

The nohost tells the compiler not to compile a version of this procedure for the host. All calls to this procedure must appear within compute regions. If this procedure is called from other procedures, those other procedures must also have a matching routine directive with the nohost clause.

Restrictions

- Only the gang, worker, vector, seq and bind clauses may follow a device_type clause.
- At least one of the (gang, worker, vector, or seq) clauses must appear on the construct. If the device_type clause appears on the routine directive, a default level of parallelism clause must appear before the device_type clause, or a level of parallelism clause must appear following each device_type clause on the directive.
- In C and C++, function static variables are not supported in functions to which a routine directive applies.
- In Fortran, variables with the save attribute, either explicitly or implicitly, are not supported in subprograms to which a routine directive applies.
- A bind clause may not bind to a routine name that has a visible bind clause.
- If a function or subroutine has a bind clause on both the declaration and the definition then they both must bind to the same name.
2.15.2 Global Data Access

C or C++ global, file static, or extern variables or array, and Fortran module or common block variables or arrays, that are used in accelerator routines must appear in a declare directive in a create, copyin, device resident or link clause. If the data appears in a device resident clause, the routine directive for the procedure must include the nohost clause. If the data appears in a link clause, that data must have an active accelerator data lifetime by virtue of appearing in a data clause for a data construct, compute construct, or enter data directive.

2.16 Asynchronous Behavior

This section describes the async clause and the behavior of programs that use asynchronous data movement and compute constructs, and asynchronous API routines.

2.16.1 async clause

The async clause may appear on a parallel, kernels, or serial construct, or an enter data, exit data, update, or wait directive. In all cases, the async clause is optional. When there is no async clause on a compute or data construct, the local thread will wait until the compute construct or data operations for the current device are complete before executing any of the code that follows. When there is no async clause on a wait directive, the local thread will wait until all operations on the appropriate asynchronous activity queues for the current device are complete. When there is an async clause, the parallel, kernels, or serial region or data operations may be processed asynchronously while the local thread continues with the code following the construct or directive.

The async clause may have a single async-argument, where an async-argument is a nonnegative scalar integer expression (int for C or C++, integer for Fortran), or one of the special values defined below. The behavior with a negative async-argument, except the special values defined below, is implementation-defined. The value of the async-argument may be used in a wait directive, wait clause, or various runtime routines to test or wait for completion of the operation.

Two special values for async-argument are defined in the C and Fortran header files and the Fortran openacc module. These are negative values, so as not to conflict with a user-specified nonnegative async-argument. An async clause with the async-argument acc_async_noval will behave the same as if the async clause had no argument. An async clause with the async-argument acc_async_sync will behave the same as if no async clause appeared.

The async-value of any operation is the value of the async-argument, if it appears, or the value of acc-default-async-var if it is acc_async_noval or if the async clause had no value, or acc_async_sync if no async clause appeared. If the current device supports asynchronous operation with one or more device activity queues, the async-value is used to select the queue on the current device onto which to enqueue an operation. The properties of the current device and the implementation will determine how many actual activity queues are supported, and how the async-value is mapped onto the actual activity queues. Two asynchronous operations with the same current device and the same async-value will be enqueued onto the same activity queue, and therefore will be executed on the device in the order they are encountered by the local thread. Two asynchronous operations with different async-values may be enqueued onto different activity queues, and therefore may be executed on the device in either order relative to each other. If there are two or more host threads executing and sharing the same device, two asynchronous operations with the same async-
value will be enqueued on the same activity queue. If the threads are not synchronized with respect
to each other, the operations may be enqueued in either order and therefore may execute on the
device in either order. Asynchronous operations enqueued to different devices may execute in any
order, regardless of the async-value used for each.

2.16.2 wait clause

The wait clause may appear on a parallel, kernels, or serial construct, or an enter
data, exit data, or update directive. In all cases, the wait clause is optional. When there
is no wait clause, the associated compute or update operations may be enqueued or launched or
executed immediately on the device. If there is an argument to the wait clause, it must be a wait-
argument (See 2.16.3). The compute, data, or update operation may not be launched or executed
until all operations enqueued up to this point by this thread on the associated asynchronous device
activity queues have completed. One legal implementation is for the local thread to wait for all
the associated asynchronous device activity queues. Another legal implementation is for the local
thread to enqueue the compute, data, or update operation in such a way that the operation will
not start until the operations enqueued on the associated asynchronous device activity queues have
completed.

2.16.3 Wait Directive

Summary

The wait directive causes the local thread or a device activity queue on the current device to wait
for completion of asynchronous operations, such as an accelerator parallel, kernels, or serial region
or an update directive.

Syntax

In C and C++, the syntax of the wait directive is:

```
#pragma acc wait [ (wait-argument) ] [ clause-list ] new-line
```

In Fortran the syntax of the wait directive is:

```
!$acc wait [ (wait-argument) ] [ clause-list ]
```

where clause is:

```
async [ (int-expr) ]
if( condition )
```

The wait argument, if it appears, must be a wait-argument where wait-argument is:

```
[ devnum : int-expr : ] [ queues : ] int-expr-list
```

If there is no wait argument and no async clause, the local thread will wait until all operations
enqueued by this thread on any activity queue on the current device have completed.

If there are one or more int-expr expressions and no async clause, the local thread will wait
until all operations enqueued by this thread on each of the associated device activity queues have
completed. If a devnum modifier exists in the wait-argument then the device activity queues in the
int-expr expressions apply to the queues on that device number of the current device type. If no
devnum modifier exits then the expressions apply to the current device. It is an error to specify a
device number that is not between 0 and the number of available devices of the current device type minus 1.

The queues modifier within a wait-argument is optional to improve clarity of the expression list.

If there are two or more threads executing and sharing the same device, a wait directive with no async clause will cause the local thread to wait until all of the appropriate asynchronous operations previously enqueued by that thread have completed. To guarantee that operations have been enqueued by other threads requires additional synchronization between those threads. There is no guarantee that all the similar asynchronous operations initiated by other threads will have completed.

If there is an async clause, no new operation may be launched or executed on the async activity queue on the current device until all operations enqueued up to this point by this thread on the asynchronous activity queues associated with the wait argument have completed. One legal implementation is for the local thread to wait for all the associated asynchronous device activity queues. Another legal implementation is for the thread to enqueue a synchronization operation in such a way that no new operation will start until the operations enqueued on the associated asynchronous device activity queues have completed.

The if clause is optional; when there is no if clause, the implementation will generate code to perform the wait operation unconditionally. When an if clause appears, the implementation will generate code to conditionally perform the wait operation only when the condition evaluates to nonzero in C or C++, or .true. in Fortran.

A wait directive is functionally equivalent to a call to one of the acc_wait, acc_wait_async, acc_wait_all or acc_wait_all_async runtime API routines, as described in Sections 3.2.13, 3.2.15, 3.2.17 and 3.2.19

Restrictions

- The int-expr that appears in a devnum modifier must be a legal device number of the current device type.

2.17 Fortran Specific Behavior

2.17.1 Optional Arguments

This section refers to the Fortran intrinsic function PRESENT. A call to the Fortran intrinsic function PRESENT(arg) returns .true.. if arg is an optional dummy argument and an actual argument for arg was present in the argument list of the call site. This should not be confused with the OpenACC present data clause.

The appearance of a Fortran optional argument arg as a var in any of the following clauses has no effect at runtime if PRESENT(arg) is .false.:

- in data clauses on compute and data constructs;
- in data clauses on enter data and exit data directives;
- in data and device_resident clauses on declare directives;
- in use_device clauses on host_data directives;
- in self, host, and device clauses on update directives.
The appearance of a Fortran optional argument \texttt{arg} in the following situations may result in undefined behavior if \texttt{PRESENT(arg)} is \texttt{.false.} when the associated construct is executed:

- as a \texttt{var} in \texttt{private}, \texttt{firstprivate}, and \texttt{reduction} clauses;
- as a \texttt{var} in \texttt{cache} directives;
- as part of an expression in any clause or directive.

A call to the Fortran intrinsic function \texttt{PRESENT} behaves the same way in a compute construct or an accelerator routine as on the host. The function call \texttt{PRESENT(arg)} must return the same value in a compute construct as \texttt{PRESENT(arg)} would outside of the compute construct. If a Fortran optional argument \texttt{arg} appears as an actual argument in a procedure call in a compute construct or an accelerator routine, and the associated dummy argument \texttt{subarg} also has the \texttt{optional} attribute, then \texttt{PRESENT(subarg)} returns the same value as \texttt{PRESENT(subarg)} would when executed on the host.

### 2.17.2 Do Concurrent Construct

This section refers to the Fortran \texttt{do concurrent} construct that is a form of \texttt{do} construct. When \texttt{do concurrent} appears without a \texttt{loop} construct in a \texttt{kernels} construct it is treated as if it is annotated with \texttt{loop auto}. If it appears in a \texttt{parallel} construct or an accelerator routine then it is treated as if it is annotated with \texttt{loop independent}.
3. Runtime Library

This chapter describes the OpenACC runtime library routines that are available for use by program-
ners. Use of these routines may limit portability to systems that do not support the OpenACC API.
Conditional compilation using the `_OPENACC` preprocessor variable may preserve portability.

This chapter has two sections:

- Runtime library definitions
- Runtime library routines

There are four categories of runtime routines:

- Device management routines, to get the number of devices, set the current device, and so on.
- Asynchronous queue management, to synchronize until all activities on an async queue are complete, for instance.
- Device test routine, to test whether this statement is executing on the device or not.
- Data and memory management, to manage memory allocation or copy data between memo-
ries.

3.1 Runtime Library Definitions

In C and C++, prototypes for the runtime library routines described in this chapter are provided in
a header file named `openacc.h`. All the library routines are `extern` functions with “C” linkage.
This file defines:

- The prototypes of all routines in the chapter.
- Any datatypes used in those prototypes, including an enumeration type to describe the sup-
ported device types.
- The values of `acc_async_noval`, `acc_async_sync`, and `acc_async_default`.

In Fortran, interface declarations are provided in a Fortran module named `openacc`. The `openacc`
module defines:

- The integer parameter `openacc_version` with a value `yyyymm` where `yyyy` and `mm` are the
year and month designations of the version of the Accelerator programming model supported.
This value matches the value of the preprocessor variable `_OPENACC`.
- Interfaces for all routines in the chapter.
- Integer parameters to define integer kinds for arguments to and return values for those rou-
tines.
- Integer parameters to describe the supported device types.
- Integer parameters to define the values of `acc_async_noval`, `acc_async_sync`, and `acc_async_default`.
Many of the routines accept or return a value corresponding to the type of device. In C and C++, the datatype used for device type values is `acc_device_t`; in Fortran, the corresponding datatype is `integer(kind=acc_device_kind)`. The possible values for device type are implementation specific, and are defined in the C or C++ include file `openacc.h` and the Fortran module `openacc`. Five values are always supported: `acc_device_none`, `acc_device_default`, `acc_device_host`, `acc_device_not_host`, and `acc_device_current`. For other values, look at the appropriate files included with the implementation, or read the documentation for the implementation. The value `acc_device_default` will never be returned by any function; its use as an argument will tell the runtime library to use the default device type for that implementation.

### 3.2 Runtime Library Routines

In this section, for the C and C++ prototypes, pointers are typed `h_void*` or `d_void*` to designate a host memory address or device memory address, when these calls are executed on the host, as if the following definitions were included:

```c
#define h_void void
#define d_void void
```

Except for `acc_on_device`, these routines are only available on the host.

#### 3.2.1 acc_get_num_devices

**Summary**

The `acc_get_num_devices` routine returns the number of available devices of the given type.

**Format**

C or C++:

```c
int acc_get_num_devices(acc_device_t dev_type);
```

Fortran:

```fortran
integer function acc_get_num_devices(dev_type)
    integer(acc_device_kind) :: dev_type
```

**Description**

The `acc_get_num_devices` routine returns the number of available devices of device type `dev_type`.

**Restrictions**

- This routine may not be called within a compute region.

#### 3.2.2 acc_set_device_type

**Summary**

The `acc_set_device_type` routine tells the runtime which type of device to use when executing a compute region and sets the value of `acc-current-device-type-var`. This is useful when the implementation allows the program to be compiled to use more than one type of device.
The OpenACC API 3.2. Runtime Library Routines

Format
C or C++:

```c
void acc_set_device_type(acc_device_t dev_type);
```

Fortran:

```fortran
subroutine acc_set_device_type(dev_type)
  integer(acc_device_kind) :: dev_type
end subroutine
```

Description
The `acc_set_device_type` routine tells the runtime which type of device to use among those available and sets the value of `acc-current-device-type-var` for the current thread to `dev_type`. A call to `acc_set_device_type` is functionally equivalent to a `set device_type(dev_type)` directive, as described in Section 2.14.3.

Restrictions
- If the device type `dev_type` is not available, the behavior is implementation-defined; in particular, the program may abort.
- If some compute regions are compiled to only use one device type, calling this routine with a different device type may produce undefined behavior.

3.2.3 acc_get_device_type

Summary
The `acc_get_device_type` routine returns the value of `acc-current-device-type-var`, which is the device type of the current device. This is useful when the implementation allows the program to be compiled to use more than one type of device.

Format
C or C++:

```c
acc_device_t acc_get_device_type(void);
```

Fortran:

```fortran
function acc_get_device_type()
  integer(acc_device_kind) :: acc_get_device_type
end function
```

Description
The `acc_get_device_type` routine returns the value of `acc-current-device-type-var` for the current thread to tell the program what type of device will be used to run the next compute region, if one has been selected. The device type may have been selected by the program with an `acc_set_device_type` call, with an environment variable, or by the default behavior of the program.

Restrictions
- If the device type has not yet been selected, the value `acc_device_none` may be returned.

3.2.4 acc_set_device_num

Summary
The `acc_set_device_num` routine tells the runtime which device to use and sets the value of `acc-current-device-num-var`.

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Format

C or C++:

    void acc_set_device_num(int dev_num, acc_device_t dev_type);

Fortran:

    subroutine acc_set_device_num(dev_num, dev_type)
    integer :: dev_num
    integer(acc_device_kind) :: dev_type

Description

The `acc_set_device_num` routine tells the runtime which device to use among those available of the given type for compute or data regions in the current thread and sets the value of acc-current-device-num-var to dev_num. If the value of dev_num is negative, the runtime will revert to its default behavior, which is implementation-defined. If the value of the dev_type is zero, the selected device number will be used for all device types. Calling `acc_set_device_num` implies a call to `acc_set_device_type(dev_type)`. A call to `acc_set_device_num` is functionally equivalent to a `set device_type(dev_type) device_num(dev_num)` directive, as described in Section 2.14.3.

Restrictions

- If the value of dev_num is greater than or equal to the value returned by acc_get_num_devices for that device type, the behavior is implementation-defined.

3.2.5 acc_get_device_num

Summary

The `acc_get_device_num` routine returns the value of acc-current-device-num-var for the current thread.

Format

C or C++:

    int acc_get_device_num(acc_device_t dev_type);

Fortran:

    integer function acc_get_device_num(dev_type)
    integer(acc_device_kind) :: dev_type

Description

The `acc_get_device_num` routine returns the value of acc-current-device-num-var for the current thread.

3.2.6 acc_get_property

Summary

The `acc_get_property` and `acc_get_property_string` routines return the value of a device-property for the specified device.
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```
Format
C or C++:
    size_t acc_get_property(int dev_num,
                           acc_device_t dev_type,
                           acc_device_property_t property);

    const
    char* acc_get_property_string(int dev_num,
                               acc_device_t dev_type,
                               acc_device_property_t property);

Fortran:
    function acc_get_property(dev_num, dev_type, property)
    subroutine acc_get_property_string(dev_num, dev_type,&
                                           property, string)
```

Description
The acc_get_property and acc_get_property_string routines return the value of the
property. dev_num and dev_type specify the device being queried. If dev_type has the
value acc_device_current, then dev_num is ignored and the value of the property for the
current device is returned. property is an enumeration constant, defined in openacc.h, for
C or C++, or an integer parameter, defined in the openacc module, for Fortran. Integer-valued
properties are returned by acc_get_property, and string-valued properties are returned by
acc_get_property_string. In Fortran, acc_get_property_string returns the result
into the string argument.

The supported values of property are given in the following table.

<table>
<thead>
<tr>
<th>property</th>
<th>return type</th>
<th>return value</th>
</tr>
</thead>
<tbody>
<tr>
<td>acc_property_memory</td>
<td>integer</td>
<td>size of device memory in bytes</td>
</tr>
<tr>
<td>acc_property_free_memory</td>
<td>integer</td>
<td>free device memory in bytes</td>
</tr>
<tr>
<td>acc_property_shared_memory_support</td>
<td>integer</td>
<td>nonzero if the specified device supports sharing memory with the local thread</td>
</tr>
<tr>
<td>acc_property_name</td>
<td>string</td>
<td>device name</td>
</tr>
<tr>
<td>acc_property_vendor</td>
<td>string</td>
<td>device vendor</td>
</tr>
<tr>
<td>acc_property_driver</td>
<td>string</td>
<td>device driver version</td>
</tr>
</tbody>
</table>

An implementation may support additional properties for some devices.

Restrictions
- These routines may not be called within a compute region.
- If the value of property is not one of the known values for that query routine, or that
  property has no value for the specified device, acc_get_property will return 0 and
acc_get_property_string will return NULL (in C or C++) or an blank string (in Fortran).

3.2.7 acc_init

Summary

The acc_init routine tells the runtime to initialize the runtime for that device type. This can be used to isolate any initialization cost from the computational cost, when collecting performance statistics.

Format

C or C++:

```c
void acc_init(acc_device_t dev_type);
```

Fortran:

```fortran
subroutine acc_init(dev_type)
  integer(acc_device_kind) :: dev_type
```

Description

The acc_init routine also implicitly calls acc_set_device_type(dev_type). A call to acc_init is functionally equivalent to a init device_type(dev_type) directive, as described in Section 2.14.1.

Restrictions

- This routine may not be called within a compute region.
- If the device type dev_type is not available, the behavior is implementation-defined; in particular, the program may abort.
- If the routine is called more than once without an intervening acc_shutdown call, with a different value for the device type argument, the behavior is implementation-defined.
- If some accelerator regions are compiled to only use one device type, calling this routine with a different device type may produce undefined behavior.

3.2.8 acc_shutdown

Summary

The acc_shutdown routine tells the runtime to shut down any connection to devices of the given device type, and free up any runtime resources. This ends all data lifetimes in device memory, which effectively sets structured and dynamic reference counters to zero.

Format

C or C++:

```c
void acc_shutdown(acc_device_t dev_type);
```

Fortran:

```fortran
subroutine acc_shutdown(dev_type)
  integer(acc_device_kind) :: dev_type
```
Description
The acc_shutdown routine disconnects the program from any device of device type \texttt{dev_type}.
Any data that is present in the memory of any such device is immediately deallocated. A call to
acc_shutdown is functionally equivalent to a shutdown device_type\texttt{(dev_type)} directive, as described in Section 2.14.2.

Restrictions
- This routine may not be called during execution of a compute region.
- If the program attempts to execute a compute region on a device or to access any data in
  the memory of a device after a call to acc_shutdown for that device type, the behavior is
  undefined.
- If the program attempts to shut down the acc_device_host device type, the behavior is
  undefined.

3.2.9 acc_async_test
Summary
The acc_async_test routine tests for completion of all associated asynchronous operations on
the current device.

Format
C or C++:
\begin{verbatim}
int acc_async_test(int wait_arg);
\end{verbatim}

Fortran:
\begin{verbatim}
logical function acc_async_test(wait_arg)
integer(acc_handle_kind) :: wait_arg
\end{verbatim}

Description
wait_arg must be an async-argument as defined in Section 2.16.1 \texttt{async clause}. If that value
did not appear in any async clauses, or if it did appear in one or more async clauses and all
such asynchronous operations have completed on the current device, the acc_async_test rou-
tine will return with a nonzero value in C and C++, or \texttt{.true.} in Fortran. If some such asyn-
chronous operations have not completed, the acc_async_test routine will return with a zero
value in C and C++, or \texttt{.false.} in Fortran. If two or more threads share the same accelerator, the
acc_async_test routine will return with a nonzero value or \texttt{.true.} only if all matching asyn-
chronous operations initiated by this thread have completed; there is no guarantee that all matching
asynchronous operations initiated by other threads have completed.

3.2.10 acc_async_test_device
Summary
The acc_async_test_device routine tests for completion of all associated asynchronous op-
erations on a device.

Format
C or C++:
\begin{verbatim}
int acc_async_test_device(int wait_arg, int dev_num);
\end{verbatim}
Fortran:

```fortran
logical function acc_async_test_device(wait_arg, dev_num)
    integer(acc_handle_kind) :: wait_arg
    integer :: dev_num

Description
wait_arg must be an async-argument as defined in Section 2.16.1 async clause. dev_num must be a valid device number of the current device type.

If wait_arg did not appear in any async clauses, or if it did appear in one or more async clauses and all such asynchronous operations have completed on the device dev_num, the acc_async_test_device routine will return with a nonzero value in C and C++, or .true. in Fortran. If some such asynchronous operations have not completed, the acc_async_test_device routine will return with a zero value in C and C++, or .false. in Fortran. If two or more threads share the same accelerator, the acc_async_test_device routine will return with a nonzero value or .true. only if all matching asynchronous operations initiated by this thread have completed; there is no guarantee that all matching asynchronous operations initiated by other threads have completed.

3.2.11 acc_async_test_all

Summary
The acc_async_test_all routine tests for completion of all asynchronous operations.

Format
C or C++:
```int``` acc_async_test_all(void);

Fortran:
```fortran
logical function acc_async_test_all()
```

Description
If all outstanding asynchronous operations have completed, the acc_async_test_all routine will return with a nonzero value in C and C++, or .true. in Fortran. If some asynchronous operations have not completed, the acc_async_test_all routine will return with a zero value in C and C++, or .false. in Fortran. If two or more threads share the same accelerator, the acc_async_test_all routine will return with a nonzero value or .true. only if all outstanding asynchronous operations initiated by this thread have completed; there is no guarantee that all asynchronous operations initiated by other threads have completed.

3.2.12 acc_async_test_all_device

Summary
The acc_async_test_all_device routine tests for completion of all asynchronous operations.

Format
C or C++:
```int``` acc_async_test_all_device(int dev_num);

Fortran:
```fortran
logical function acc_async_test_all_device(dev_num)
    integer :: dev_num
```
Description

deve_num must be a valid device number of the current device type. If all outstanding asynchronous
operations have completed on device deve_num, the acc_async_test_all_device routine
will return with a nonzero value in C and C++, or .true. in Fortran. If some asynchronous oper-
ations have not completed, the acc_async_test_all_device routine will return with a zero
value in C and C++, or .false. in Fortran. If two or more threads share the same accelerator, the
acc_async_test_all_device routine will return with a nonzero value or .true. only if all
outstanding asynchronous operations initiated by this thread have completed; there is no guarantee
that all asynchronous operations initiated by other threads have completed.

3.2.13 acc_wait

Summary

The acc_wait routine waits for completion of all associated asynchronous operations on the cur-
rent device.

Format

C or C++:

void acc_wait(int wait_arg);

Fortran:

subroutine acc_wait(wait_arg)
  integer(acc_handle_kind) :: wait_arg

Description

wait_arg must be an async-argument as defined in Section 2.16.1 async clause. If wait_arg
appeared in one or more async clauses, the acc_wait routine will not return until the latest
such asynchronous operation has completed on the current device. If two or more threads share
the same accelerator, the acc_wait routine will return only if all matching asynchronous opera-
tions initiated by this thread have completed; there is no guarantee that all matching asynchronous
operations initiated by other threads have completed. For compatibility with version 1.0, this rou-
tine may also be spelled acc_async_wait. A call to acc_wait is functionally equivalent to a
wait(wait_arg) directive with no async clause, as described in Section 2.16.3.

3.2.14 acc_wait_device

Summary

The acc_wait_device routine waits for completion of all associated asynchronous operations
on a device.

Format

C or C++:

void acc_wait_device(int wait_arg, int dev_num);

Fortran:

subroutine acc_wait_device(wait_arg, dev_num)
  integer(acc_handle_kind) :: wait_arg
  integer :: dev_num
Description

`wait_arg` must be an async-argument as defined in Section 3.2.13 async clause. `dev_num` must be a valid device number of the current device type.

If `wait_arg` appeared in one or more async clauses, the `acc_wait` routine will not return until the latest such asynchronous operation has completed on device `dev_num`. If two or more threads share the same accelerator, the `acc_wait` routine will return only if all matching asynchronous operations initiated by this thread have completed; there is no guarantee that all matching asynchronous operations initiated by other threads have completed.

3.2.15 acc_wait_async

Summary

The `acc_wait_async` routine enqueues a wait operation on one async queue of the current device for the operations previously enqueued on another async queue.

Format

C or C++:

```c
void acc_wait_async(int wait_arg, int async_arg);
```

Fortran:

```fortran
subroutine acc_wait_async(wait_arg, async_arg)
  integer(acc_handle_kind) :: wait_arg, async_arg
```

Description

The arguments must be async-arguments, as defined in Section 3.2.13 async clause. The routine will enqueue a wait operation on the async queue associated with `async_arg`, which will wait for operations enqueued on the async queue associated with the `wait_arg`. See Section 3.2.13 Asynchronous Behavior for more information. A call to `acc_wait_async` is functionally equivalent to a `wait(wait_arg)` directive with an `async(async_arg)` clause, as described in Section 3.2.13.

3.2.16 acc_wait_device_async

Summary

The `acc_wait_device_async` routine enqueues a wait operation on one async queue of a device for the operations previously enqueued on another async queue.

Format

C or C++:

```c
void acc_wait_device_async(int wait_arg, int async_arg,
                          int dev_num);
```

Fortran:

```fortran
subroutine acc_wait_device_async(wait_arg, async_arg, dev_num)
  integer(acc_handle_kind) :: wait_arg, async_arg
  integer :: dev_num
```
The first two arguments must be async-arguments, as defined in Section 2.16.1, and dev_num must be a valid device number of the current device type.

The routine will enqueue a wait operation on the async queue associated with async_arg on the current device, which will wait for operations enqueued on the async queue associated with wait_arg on device dev_num.

See Section 2.16 Asynchronous Behavior for more information. A call to acc_wait_device_async is functionally equivalent to a wait(devnum:dev_num, queues:wait_arg) directive with an async(async_arg) clause, as described in Section 2.16.3.

3.2.17 acc_wait_all

Summary
The acc_wait_all routine waits for completion of all asynchronous operations.

Format
C or C++:

```c
void acc_wait_all(void);
```

Fortran:

```fortran
subroutine acc_wait_all()
```

Description
The acc_wait_all routine will not return until the all asynchronous operations have completed. If two or more threads share the same accelerator, the acc_wait_all routine will return only if all asynchronous operations initiated by this thread have completed; there is no guarantee that all asynchronous operations initiated by other threads have completed. For compatibility with version 1.0, this routine may also be spelled acc_async_wait_all. A call to acc_wait_all is functionally equivalent to a wait directive with no argument and no async clause, as described in Section 2.16.3.

3.2.18 acc_wait_all_device

Summary
The acc_wait_all_device routine waits for completion of all asynchronous operations the specified device.

Format
C or C++:

```c
void acc_wait_all_device(int dev_num);
```

Fortran:

```fortran
subroutine acc_wait_all_device(dev_num)
```

Description
dev_num must be a valid device number of the current device type. The acc_wait_all_device routine will not return until the all asynchronous operations have completed on device dev_num. If two or more threads share the same accelerator, the acc_wait_all_device routine will return only if all asynchronous operations initiated by this thread have completed; there is no guarantee that all asynchronous operations initiated by other threads have completed.
3.2.19 acc_wait_all_async

Summary

The `acc_wait_all_async` routine enqueues wait operations on one async queue for the operations previously enqueued on all other async queues.

Format

C or C++:

```c
void acc_wait_all_async(int async_arg);
```

Fortran:

```fortran
subroutine acc_wait_all_async(async_arg)
integer(acc_handle_kind) :: async_arg
```

Description

`async_arg` must be an async-argument as defined in Section 2.16.1[async clause]. The routine will enqueue a wait operation on the async queue associated with `async_arg` for each other async queue. See Section 2.16[Asynchronous Behavior] for more information. A call to `acc_wait_all_async` is functionally equivalent to a `wait` directive with no argument and an `async(async_arg)` clause, as described in Section 2.16.3.

3.2.20 acc_wait_all_device_async

Summary

The `acc_wait_all_device_async` routine enqueues wait operations on one async queue for the operations previously enqueued on all other async queues on the specified device.

Format

C or C++:

```c
void acc_wait_all_device_async(int async_arg, int dev_num);
```

Fortran:

```fortran
subroutine acc_wait_all_device_async(async_arg, dev_num)
integer(acc_handle_kind) :: async_arg
integer :: dev_num
```

Description

`async_arg` must be an async-argument as defined in Section 2.16.1[async clause] `dev_num` must be a valid device number of the current device type.

The routine will enqueue a wait operation on the async queue associated with `async_arg` on the current device for each async queue of device `dev_num`. See Section 2.16[Asynchronous Behavior] for more information. A call to `acc_wait_all_device_async` is functionally equivalent to a `wait(devnum:dev_num)` directive with an `async(async_arg)` clause, as described in Section 2.16.3.

3.2.21 acc_get_default_async

Summary

The `acc_get_default_async` routine returns the value of `acc-default-async-var` for the current thread.
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Format
C or C++:

```c
int acc_get_default_async(void);
```

Fortran:

```fortran
function acc_get_default_async()
  integer(acc_handle_kind) :: acc_get_default_async
```

Description
The `acc_get_default_async` routine returns the value of `acc-default-async-var` for the current thread, which is the asynchronous queue used when an `async` clause appears without an `async-argument` or with the value `acc_async_noval`.

3.2.22 acc_set_default_async

Summary
The `acc_set_default_async` routine tells the runtime which asynchronous queue to use when an `async` clause appears with no queue argument.

Format
C or C++:

```c
void acc_set_default_async(int async_arg);
```

Fortran:

```fortran
subroutine acc_set_default_async(async_arg)
  integer(acc_handle_kind) :: async_arg
```

Description
The `acc_set_default_async` routine tells the runtime to place any directives with an `async` clause that does not have an `async-argument` or with the special `acc_async_noval` value into the asynchronous activity queue associated with `async_arg` instead of the default asynchronous activity queue for that device by setting the value of `acc-default-async-var` for the current thread.

The special argument `acc_async_default` will reset the default asynchronous activity queue to the initial value, which is implementation-defined. A call to `acc_set_default_async` is functionally equivalent to a `set default_async(async_arg)` directive, as described in Section 2.14.3.

3.2.23 acc_on_device

Summary
The `acc_on_device` routine tells the program whether it is executing on a particular device.

Format
C or C++:

```c
int acc_on_device(acc_device_t dev_type);
```

Fortran:

```fortran
logical function acc_on_device(dev_type)
  integer(acc_device_kind) :: dev_type
```
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3.2.24 acc_malloc

Summary
The acc_malloc routine allocates space in the current device memory.

Format
C or C++:

    d_void* acc_malloc(size_t bytes);

Description
The acc_malloc routine may be used to allocate space in the current device memory. Pointers assigned from this routine may be used in deviceptr clauses to tell the compiler that the pointer target is resident on the device. In case of an error, acc_malloc returns a NULL pointer.

3.2.25 acc_free

Summary
The acc_free routine frees memory on the current device.

Format
C or C++:

    void acc_free(d_void* data_dev);

Description
The acc_free routine will free previously allocated space in the current device memory; data_dev should be a pointer value that was returned by a call to acc_malloc. If the argument is a NULL pointer, no operation is performed.

3.2.26 acc_copyin

Summary
The acc_copyin routines test to see if the argument is in shared memory or already present in the current device memory; if not, they allocate space in the current device memory to correspond to the specified local memory, and copy the data to that device memory.
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3.2.1.1 acc_create

```
C or C++:

d_void* acc_copyin(h_void* data_arg, size_t bytes);
void acc_copyin_async(h_void* data_arg, size_t bytes,
    int async_arg);

Fortran:

subroutine acc_copyin(data_arg)
subroutine acc_copyin(data_arg, bytes)
subroutine acc_copyin_async(data_arg, async_arg)
subroutine acc_copyin_async(data_arg, bytes, async_arg)

  type(*), dimension(..) :: data_arg
  integer :: bytes
  integer(acc_handle_kind) :: async_arg
```

Description

The acc_copyin routines are equivalent to an `enter data` directive with a `copyin` clause, as described in Section 2.7.7. In C/C++, `data_arg` is a pointer to the data, and `bytes` specifies the data size in bytes. The synchronous routine returns a pointer to the allocated device memory, as with `acc_malloc`. In Fortran, two forms are supported. In the first, `data_arg` is a variable or a contiguous array section. In the second, `data_arg` is a variable or array element and `bytes` is the length in bytes. For the _async versions of these routines, `async_arg` must be an async-argument as defined in Section 2.16.1 async clause.

The behavior of the acc_copyin routines for the data referred to by `data_arg` is:

- If the data is in shared memory, no action is taken. The C/C++ acc_copyin routine returns the incoming pointer.
- If the data is present in the current device memory, a present increment action with the dynamic reference counter is performed. The C/C++ acc_copyin routine returns a pointer to the existing device memory.
- Otherwise, a copyin action with the dynamic reference counter is performed. The C/C++ acc_copyin routine returns the device address of the newly allocated memory.

This data may be accessed using the present data clause. Pointers assigned from the C/C++ acc_copyin routine may be used in deviceptr clauses to tell the compiler that the pointer target is resident on the device.

The _async versions of these routines will perform any data transfers asynchronously on the async queue associated with `async_arg`. The routine may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

For compatibility with OpenACC 2.0, `acc_present_or_copyin` and `acc_pcopyin` are alternate names for acc_copyin.
Summary
The `acc_create` routines test to see if the argument is in shared memory or already present in the current device memory; if not, they allocate space in the current device memory to correspond to the specified local memory.

Format
C or C++:
```c
  d_void* acc_create(h_void* data_arg, size_t bytes);
  void acc_create_async(h_void* data_arg, size_t bytes, int async_arg);
```
Fortran:
```fortran
  subroutine acc_create(data_arg)
  subroutine acc_create(data_arg, bytes)
  subroutine acc_create_async(data_arg, async_arg)
  subroutine acc_create_async(data_arg, bytes, async_arg)
  type(*), dimension(..) :: data_arg
  integer :: bytes
  integer(acc_handle_kind) :: async_arg
```

Description
The `acc_create` routines are equivalent to an `enter data` directive with a `create` clause, as described in Section 2.7.9. The arguments are as for `acc_copyin`.

The behavior of the `acc_create` routines for the data referred to by `data_arg` is:

- If the data is in shared memory, no action is taken. The C/C++ `acc_create` routine returns the incoming pointer.
- If the data is present in the current device memory, a `present increment` action with the dynamic reference counter is performed. The C/C++ `acc_create` routine returns a pointer to the existing device memory.
- Otherwise, a `create` action with the dynamic reference counter is performed. The C/C++ `acc_create` routine returns the device address of the newly allocated memory.

This data may be accessed using the `present` data clause. Pointers assigned from the C/C++ `acc_create` routine may be used in `deviceptr` clauses to tell the compiler that the pointer target is resident on the device.

The `_async` versions of these routines may perform the data allocation asynchronously on the async queue associated with `async_arg`. The synchronous versions will not return until the data has been allocated.

For compatibility with OpenACC 2.0, `acc_present_or_create` and `acc_pcreate` are alternate names for `acc_create`.

3.2.28 acc_copyout

Summary
The `acc_copyout` routines test to see if the argument is in shared memory; if not, the argument must be present in the current device memory, and the routines copy data from device memory to the corresponding local memory, then deallocate that space from the device memory.
Format

C or C++:

```c
void acc_copyout(h_void* data_arg, size_t bytes);
void acc_copyout_async(h_void* data_arg, size_t bytes,
                        int async_arg);
void acc_copyout_finalize(h_void* data_arg, size_t bytes);
void acc_copyout_finalize_async(h_void* data_arg, size_t bytes,
                                int async_arg);
```

Fortran:

```fortran
subroutine acc_copyout(data_arg)
subroutine acc_copyout(data_arg, bytes)
subroutine acc_copyout_async(data_arg, async_arg)
subroutine acc_copyout_async(data_arg, bytes, async_arg)
subroutine acc_copyout_finalize(data_arg)
subroutine acc_copyout_finalize(data_arg, bytes)
subroutine acc_copyout_finalize_async(data_arg, async_arg)
subroutine acc_copyout_finalize_async(data_arg, bytes,&
                                        async_arg)
```

```fortran
type(*), dimension(..) :: data_arg
integer :: bytes
integer(acc_handle_kind) :: async_arg
```

Description

The `acc_copyout` routines are equivalent to an `exit data` directive with a `copyout` clause, and the `acc_copyout_finalize` routines are equivalent to an `exit data` directive with both `copyout` and `finalize` clauses, as described in Section 2.7.8. The arguments are as for `acc_copyin`.

The behavior of the `acc_copyout` routines for the data referred to by `data_arg` is:

- If the data is in shared memory, no action is taken.
- Otherwise, if the dynamic reference counter for the data is zero, no action is taken.
- Otherwise, a `copyout` action with the dynamic reference counter is performed (`acc_copyout`), or the dynamic reference counter is set to zero (`acc_copyout_finalize`). If both reference counters are then zero, a `copyout` action is performed.

The `_async` versions of these routines will perform any associated data transfers asynchronously on the async queue associated with `async_arg`. The routine may return before the data has been transferred or deallocated; see Section 2.16 [Asynchronous Behavior] for more details. The synchronous versions will not return until the data has been completely transferred. Even if the data has not been transferred or deallocated before the routine returns, the data will be treated as not present in the current device memory.

3.2.29 acc_delete

Summary

The `acc_delete` routines test to see if the argument is in shared memory; if not, the argument must be present in the current device memory, and the routines deallocate that space from the device.
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memory.

Format

C or C++:

    void acc_delete(h_void* data_arg, size_t bytes);
    void acc_delete_async(h_void* data_arg, size_t bytes,
                           int async_arg);
    void acc_delete_finalize(h_void* data_arg, size_t bytes);
    void acc_delete_finalize_async(h_void* data_arg,
                                      size_t bytes, int async_arg);

Fortran:

    subroutine acc_delete(data_arg)
    subroutine acc_delete(data_arg, bytes)
    subroutine acc_delete_async(data_arg, async_arg)
    subroutine acc_delete_async(data_arg, bytes, async_arg)
    subroutine acc_delete_finalize(data_arg)
    subroutine acc_delete_finalize_async(data_arg, async_arg)
    subroutine acc_delete_finalize_async(data_arg, bytes,&
                                           async_arg)

    type(*), dimension(..) :: data_arg
    integer :: bytes
    integer(acc_handle_kind) :: async_arg

Description

The acc_delete routines are equivalent to an exit data directive with a delete clause, and the acc_delete_finalize routines are equivalent to an exit data directive with both delete clause and finalize clauses, as described in Section 2.7.11. The arguments are as for acc_copyin.

The behavior of the acc_delete routines for the data referred to by data_arg is:

- If the data is in shared memory, no action is taken.
- Otherwise, if the dynamic reference counter for the data is zero, no action is taken.
- Otherwise, a present decrement action with the dynamic reference counter is performed (acc_delete), or the dynamic reference counter is set to zero (acc_delete_finalize). If both reference counters are then zero, a delete action is performed.

The _async versions of these routines may perform the data deallocation asynchronously on the async queue associated with async_arg. Even if the data has not been deallocated before the routine returns, the data will be treated as not present in the current device memory. The synchronous versions will not return until the data has been deallocated.

3.2.30 acc_update_device

Summary

The acc_update_device routines test to see if the argument is in shared memory; if not, the argument must be present in the current device memory, and the routines update the data in device memory from the corresponding local memory.
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**Format**

C or C++:

```c
void acc_update_device(h_void* data_arg, size_t bytes);
void acc_update_device_async(h_void* data_arg, size_t bytes,
                             int async_arg);
```

Fortran:

```fortran
subroutine acc_update_device(data_arg)
subroutine acc_update_device(data_arg, bytes)
subroutine acc_update_device_async(data_arg, async_arg)
subroutine acc_update_device_async(data_arg, bytes, async_arg)
```

```fortran
type(*), dimension(..) :: data_arg
integer :: bytes
integer(acc_handle_kind) :: async_arg
```

**Description**

The `acc_update_device` routine is equivalent to an `update` directive with a `device` clause, as described in Section 2.14.4. The arguments are as for `acc_copyin`. For the data referred to by `data_arg`, if data is not in shared memory, the data in the local memory is copied to the corresponding device memory. It is a runtime error to call this routine if the data is not present in the current device memory.

The `_async` versions of these routines will perform the data transfers asynchronously on the async queue associated with `async_arg`. The routine may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

### 3.2.31 acc_update_self

**Summary**

The `acc_update_self` routines test to see if the argument is in shared memory; if not, the argument must be present in the current device memory, and the routines update the data in local memory from the corresponding device memory.

**Format**

C or C++:

```c
void acc_update_self(h_void* data_arg, size_t bytes);
void acc_update_self_async(h_void* data_arg, size_t bytes,
                           int async_arg);
```

Fortran:

```fortran
subroutine acc_update_self(data_arg)
subroutine acc_update_self(data_arg, bytes)
subroutine acc_update_self_async(data_arg, async_arg)
subroutine acc_update_self_async(data_arg, bytes, async_arg)
```

```fortran
type(*), dimension(..) :: data_arg
integer :: bytes
integer(acc_handle_kind) :: async_arg
```
The \texttt{acc_update_self} routine is equivalent to an \texttt{update} directive with a \texttt{self} clause, as described in Section 2.14.4 The arguments are as for \texttt{acc_copyin}. For the data referred to by \texttt{data_arg}, if the data is not in shared memory, the data in the local memory is copied to the corresponding device memory. There must be a device copy of the data on the device when calling this routine. It is a runtime error to call this routine if the data is not present in the current device memory.

The \_async versions of these routines will perform the data transfers asynchronously on the async queue associated with \texttt{async_arg}. The routine may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

### 3.2.32 \texttt{acc_map_data}

#### Summary

The \texttt{acc_map_data} routine maps previously allocated space in the current device memory to the specified host data.

#### Format

C or C++:

```c
void acc_map_data(h_void* data_arg, d_void* data_dev, size_t bytes);
```

#### Description

The \texttt{acc_map_data} routine is similar to an \texttt{enter data} directive with a \texttt{create} clause, except that instead of allocating new device memory to start a data lifetime, the device address to use for the data lifetime is specified as an argument. \texttt{data_arg} is a host address, \texttt{data_dev} is the corresponding device address, and \texttt{bytes} is the length in bytes. \texttt{data_dev} may be the result of a call to \texttt{acc_malloc}, or may come from some other device-specific API routine. After this call, when the host data appears in a data clause, the specified device memory will be used. It is an error to call \texttt{acc_map_data} for host data that is already present in the current device memory. It is undefined to call \texttt{acc_map_data} with a device address that is already mapped to host data. After mapping the device memory, the dynamic reference count for the host data is set to one, but no data movement will occur. Memory mapped by \texttt{acc_map_data} may not have the associated dynamic reference count decremented to zero, except by a call to \texttt{acc_unmap_data}. See Section 2.6.7 Reference Counters.

### 3.2.33 \texttt{acc_unmap_data}

#### Summary

The \texttt{acc_unmap_data} routine unmaps device data from the specified host data.

#### Format

C or C++:

```c
void acc_unmap_data(h_void* data_arg);
```
Description
The `acc_unmap_data` routine is similar to an **exit data** directive with a **delete** clause, except the device memory is not deallocated. `data_arg` is a host address. A call to this routine ends the data lifetime for the specified host data. The device memory is not deallocated. It is undefined behavior to call `acc_unmap_data` with a host address unless that host address was mapped to device memory using `acc_map_data`. After unmapping memory the dynamic reference count for the pointer is set to zero, but no data movement will occur. It is an error to call `acc_unmap_data` if the structured reference count for the pointer is not zero. See Section 2.6.7 Reference Counters.

3.2.34 acc_deviceptr

Summary
The `acc_deviceptr` routine returns the device pointer associated with a specific host address.

Format
C or C++:

```c
d_void* acc_deviceptr(h_void* data_arg);
```

Description
The `acc_deviceptr` routine returns the device pointer associated with a host address. `data_arg` is the address of a host variable or array that has an active lifetime on the current device. If the data is not present in the current device memory, the routine returns a NULL value.

3.2.35 acc_hostptr

Summary
The `acc_hostptr` routine returns the host pointer associated with a specific device address.

Format
C or C++:

```c
h_void* acc_hostptr(d_void* data_dev);
```

Description
The `acc_hostptr` routine returns the host pointer associated with a device address. `data_dev` is the address of a device variable or array, such as that returned from `acc_deviceptr`, `acc_create` or `acc_copyin`. If the device address is NULL, or does not correspond to any host address, the routine returns a NULL value.

3.2.36 acc_is_present

Summary
The `acc_is_present` routine tests whether a variable or array region is accessible from the current device.

Format
C or C++:

```c
int acc_is_present(h_void* data_arg, size_t bytes);
```

Fortran:

```fortran
logical function acc_is_present(data_arg)
```

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logical function acc_is_present(data_arg, bytes)
  type(*), dimension(..) :: data_arg
  integer :: bytes

Description
The acc_is_present routine tests whether the specified host data is accessible from the current device. In C/C++, data_arg is a pointer to the data, and bytes specifies the data size in bytes; the routine returns nonzero if the specified data is fully present, and zero otherwise. In Fortran, two forms are supported. In the first, data_arg is a variable or contiguous array section. In the second, data_arg is a variable or array element and bytes is the length in bytes. The routine returns .true. if the specified data is in shared memory or is fully present, and .false. otherwise. If the byte length is zero, the routine returns nonzero in C/C++ or .true. in Fortran if the given address is in shared memory or is present at all in the current device memory.

3.2.37 acc_memcpy_to_device

Summary
The acc_memcpy_to_device routine copies data from local memory to device memory.

Format
C or C++:
  void acc_memcpy_to_device(d_void* data_dev_dest,
                            h_void* data_host_src, size_t bytes);
  void acc_memcpy_to_device_async(d_void* data_dev_dest,
                                 h_void* data_host_src, size_t bytes,
                                 int async_arg);

Description
The acc_memcpy_to_device routine copies bytes bytes of data from the local address in data_host_src to the device address in data_dev_dest. data_dev_dest must be an address accessible from the current device, such as an address returned from acc_malloc or acc_deviceptr, or an address in shared memory. The _async version of this routine will perform the data transfers asynchronously on the async queue associated with async_arg. The routine may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

3.2.38 acc_memcpy_from_device

Summary
The acc_memcpy_from_device routine copies data from device memory to local memory.

Format
C or C++:
  void acc_memcpy_from_device(h_void* data_host_dest,
                              d_void* data_dev_src, size_t bytes);
  void acc_memcpy_from_device_async(h_void* data_host_dest,
                                     d_void* data_dev_src, size_t bytes,
                                     int async_arg);
Description
The \texttt{acc_memcpy_from_device} routine copies \texttt{bytes} bytes of data from the device address in \texttt{data_dev_src} to the local address in \texttt{data_host_dest}. \texttt{data_dev_src} must be an address accessible from the current device, such as an address returned from \texttt{acc_malloc} or \texttt{acc_deviceptr}, or an address in shared memory.

The \texttt{async} version of this routine will perform the data transfers asynchronously on the async queue associated with \texttt{async_arg}. The routine may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

3.2.39 \texttt{acc_memcpy_device}

Summary
The \texttt{acc_memcpy_device} routine copies data from one memory location to another memory location on the current device.

Format
C or C++:
\begin{verbatim}
void acc_memcpy_device(d_void* data_dev_dest,  
d_void* data_dev_src, size_t bytes);
void acc_memcpy_device_async(d_void* data_dev_dest,  
d_void* data_dev_src, size_t bytes,    
int async_arg);
\end{verbatim}

Description
The \texttt{acc_memcpy_device} routine copies \texttt{bytes} bytes of data from the device address in \texttt{data_dev_src} to the device address in \texttt{data_dev_dest}. Both addresses must be addresses in the current device memory, such as would be returned from \texttt{acc_malloc} or \texttt{acc_deviceptr}. If \texttt{data_dev_dest} and \texttt{data_dev_src} overlap, the behavior is undefined.

The \texttt{async} version of this routine will perform the data transfers asynchronously on the async queue associated with \texttt{async_arg}. The routine may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

3.2.40 \texttt{acc_attach}

Summary
The \texttt{acc_attach} routine updates a pointer in device memory to point to the corresponding device copy of the host pointer target.

Format
C or C++:
\begin{verbatim}
void acc_attach(h_void** ptr_addr);
void acc_attach_async(h_void** ptr_addr, int async_arg);
\end{verbatim}
Description

`ptr_addr` must be the address of a host pointer. If the data at `**ptr_addr` is in shared memory, or if the pointer `*ptr_addr` is in shared memory or is not present in the current device memory, or the address to which the `*ptr_addr` points is not present in the current device memory, no action is taken. Otherwise, these routines perform the `attach` action (Section 2.7.2).

These routines may issue a data transfer from local memory to device memory. The `_async` version of this routine will perform the data transfers asynchronously on the async queue associated with `async_arg`. The routine may return before the data has been transferred; see Section 2.16 [Asynchronous Behavior] for more details. The synchronous version will not return until the data has been completely transferred.

### 3.2.41 acc_detach

**Summary**

The `acc_detach` routine updates a pointer in device memory to point to the host pointer target.

**Format**

C or C++:

```c
void acc_detach(h_void** ptr_addr);
void acc_detach_async(h_void** ptr_addr, int async_arg);
void acc_detach_finalize(h_void** ptr_addr);
void acc_detach_finalize_async(h_void** ptr_addr, int async_arg);
```

**Description**

The `acc_detach` routines are passed the address of a host pointer. If the data at `**ptr_addr` is in shared memory, or if the pointer `*ptr_addr` is in shared memory or is not present in the current device memory, or if the attachment counter for the pointer `*ptr_addr` is zero, no action is taken. Otherwise, these routines perform the `detach` action (Section 2.7.2).

The `acc_detach_finalize` routines are equivalent to an `exit data` directive with `detach` and `finalize` clauses, as described in Section 2.7.13 `detach clause`. If the data is in shared memory, or if the pointer `*ptr_addr` is not present in the current device memory, or if the attachment counter for the pointer `*ptr_addr` is zero, no action is taken. Otherwise, these routines perform the `immediate detach` action (Section 2.7.2).

These routines may issue a data transfer from local memory to device memory. The `_async` versions of these routines will perform the data transfers asynchronously on the async queue associated with `async_arg`. These routines may return before the data has been transferred; see Section 2.16 [Asynchronous Behavior] for more details. The synchronous versions will not return until the data has been completely transferred.

### 3.2.42 acc_memcpy_d2d

**Summary**

This `acc_memcpy_d2d` and `acc_memcpy_d2d_async` routines copy the contents of an array on one device to an array on the same or a different device without updating the value on the host.
Format

C or C++:

```c
void acc_memcpy_d2d(h_void* data_arg_dest,
                    h_void* data_arg_src, size_t bytes,
                    int dev_num_dest, int dev_num_src);
void acc_memcpy_d2d_async(h_void* data_arg_dest,
                          h_void* data_arg_src, size_t bytes,
                          int dev_num_dest, int dev_num_src,
                          int async_arg_src);
```

Fortran:

```fortran
subroutine acc_memcpy_d2d(data_arg_dest, data_arg_src,&
                          bytes, dev_num_dest, dev_num_src)
subroutine acc_memcpy_d2d_async(data_arg_dest, data_arg_src,&
                                bytes, dev_num_dest, dev_num_src,&)
                         async_arg_src)
```

Description

The `acc_memcpy_d2d` and `acc_memcpy_d2d_async` routines are passed the address of destination and source host pointers as well as integer device numbers for the destination and source devices, which must both be of the current device type. If both arrays are in shared memory, then no action is taken. If either pointer is not in shared memory, then that array must be present on its respective device. If these conditions are met, the contents of the source array on the source device are copied to the destination array on the destination device.

For `acc_memcpy_d2d_async` the value of `async_arg_src` is the number of an async queue on the source device. This routine will perform the data transfers asynchronously on the async queue associated with `async_arg_src` for device `dev_num_src`; see Section 2.16 Asynchronous Behavior for more details.
4. Environment Variables

This chapter describes the environment variables that modify the behavior of accelerator regions. The names of the environment variables must be upper case. The values assigned environment variables are case-insensitive and may have leading and trailing white space. If the values of the environment variables change after the program has started, even if the program itself modifies the values, the behavior is implementation-defined.

4.1 ACC_DEVICE_TYPE

The ACC_DEVICE_TYPE environment variable controls the default device type to use when executing parallel, kernels, and serial regions, if the program has been compiled to use more than one different type of device. The allowed values of this environment variable are implementation-defined. See the release notes for currently-supported values of this environment variable.

Example:

```
setenv ACC_DEVICE_TYPE NVIDIA
export ACC_DEVICE_TYPE=NVIDIA
```

4.2 ACCDEVICE_NUM

The ACCDEVICE_NUM environment variable controls the default device number to use when executing accelerator regions. The value of this environment variable must be a nonnegative integer between zero and the number of devices of the desired type attached to the host. If the value is greater than or equal to the number of devices attached, the behavior is implementation-defined.

Example:

```
setenv ACCDEVICE_NUM 1
export ACCDEVICE_NUM=1
```

4.3 ACC_PROFLIB

The ACC_PROFLIB environment variable specifies the profiling library. More details about the evaluation at runtime is given in section 5.3.3 Runtime Dynamic Library Loading.

Example:

```
setenv ACC_PROFLIB /path/to/proflib/libaccprof.so
export ACC_PROFLIB=/path/to/proflib/libaccprof.so
```
5. Profiling Interface

This chapter describes the OpenACC interface for tools that can be used for profile and trace data collection. Therefore it provides a set of OpenACC-specific event callbacks that are triggered during the application run. Currently, this interface does not support tools that employ asynchronous sampling. In this chapter, the term runtime refers to the OpenACC runtime library. The term library refers to the third party routines invoked at specified events by the OpenACC runtime.

There are four steps for interfacing a library to the runtime. The first is to write the data collection library callback routines. Section 5.1 Events describes the supported runtime events and the order in which callbacks to the callback routines will occur. Section 5.2 Callbacks Signature describes the signature of the callback routines for all events.

The second is to use registration routines to register the data collection callbacks for the appropriate events. The data collection and registration routines are then saved in a static or dynamic library or shared object. The third is to load the library at runtime. The library may be statically linked to the application or dynamically loaded by the application or by the runtime. This is described in Section 5.3 Loading the Library.

The fourth step is to invoke the registration routine to register the desired callbacks with the events. This may be done explicitly by the application, if the library is statically linked with the application, implicitly by including a call to the registration routine in a .init section, or by including an initialization routine in the library if it is dynamically loaded by the runtime. This is described in Section 5.4 Registering Event Callbacks.

Subsequently, the library may collect information when the callback routines are invoked by the runtime and process or store the acquired data.

5.1 Events

This section describes the events that are recognized by the runtime. Most events may have a start and end callback routine, that is, a routine that is called just before the runtime code to handle the event starts and another routine that is called just after the event is handled. The event names and routine prototypes are available in the header file acc_prof.h, which is delivered with the OpenACC implementation. Event names are prefixed with acc_ev_.

The ordering of events must reflect the order in which the OpenACC runtime actually executes them, i.e. if a runtime moves the enqueuing of data transfers or kernel launches outside the originating clauses/constructs, it needs to issue the corresponding launch callbacks when they really occur. A callback for a start event must always precede the matching end callback. The behavior of a tool receiving a callback after the runtime shutdown callback is undefined.

The events that the runtime supports can be registered with a callback and are defined in the enumeration type acc_event_t.

```c
typedef enum acc_event_t {
    acc_ev_none = 0,
    acc_ev_device_init_start = 1,
    acc_ev_device_init_end = 2,
    acc_ev_device_shutdown_start = 3,
```
The value of `acc_ev_last` will change if new events are added to the enumeration, so a library should not depend on that value.

### 5.1.1 Runtime Initialization and Shutdown

No callbacks can be registered for the runtime initialization. Instead the initialization of the tool is handled as described in Section 5.3 Loading the Library.

The runtime shutdown event name is

```c
acc_ev_runtime_shutdown
```

The `acc_ev_runtime_shutdown` event is triggered before the OpenACC runtime shuts down, either because all devices have been shutdown by calls to the `acc_shutdown` API routine, or at the end of the program.

### 5.1.2 Device Initialization and Shutdown

The device initialization event names are

```c
acc_ev_device_init_start
acc_ev_device_init_end
```

These events are triggered when a device is being initialized by the OpenACC runtime. This may be when the program starts, or may be later during execution when the program reaches an `acc_init`.
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call or an OpenACC construct. The acc_ev_device_init_start is triggered before device
initialization starts and acc_ev_device_init_end after initialization is complete.

The device shutdown event names are

acc_ev_device_shutdown_start
acc_ev_device_shutdown_end

These events are triggered when a device is shut down, most likely by a call to the OpenACC
acc_shutdown API routine. The acc_ev_device_shutdown_start is triggered before
the device shutdown process starts and acc_ev_device_shutdown_end after the device shut-
down is complete.

5.1.3 Enter Data and Exit Data

The enter data and exit data event names are

acc_ev_enter_data_start
acc_ev_enter_data_end
acc_ev_exit_data_start
acc_ev_exit_data_end

The acc_ev_enter_data_start and acc_ev_enter_data_end events are triggered at
enter data directives, entry to data constructs, and entry to implicit data regions such as those
generated by compute constructs. The acc_ev_enter_data_start event is triggered before
any data allocation, data update, or wait events that are associated with that directive or region
entry, and the acc_ev_enter_data_end is triggered after those events.

The acc_ev_exit_data_start and acc_ev_exit_data_end events are triggered at exit
data directives, exit from data constructs, and exit from implicit data regions. The
acc_ev_exit_data_start event is triggered before any data deallocation, data update, or
wait events associated with that directive or region exit, and the acc_ev_exit_data_end event
is triggered after those events.

When the construct that triggers an enter data or exit data event was generated implicitly by the
compiler the implicit field in the event structure will be set to 1. When the construct that
triggers these events was specified explicitly by the application code the implicit field in the
event structure will be set to 0.

5.1.4 Data Allocation

The data allocation event names are

acc_ev_create
acc_ev_delete
acc_ev_alloc
acc_ev_free

An acc_ev_alloc event is triggered when the OpenACC runtime allocates memory from the de-
vice memory pool, and an acc_ev_free event is triggered when the runtime frees that memory.

An acc_ev_create event is triggered when the OpenACC runtime associates device memory
with local memory, such as for a data clause (create, copyin, copy, copyout) at entry to
a data construct, compute construct, at an enter data directive, or in a call to a data API routine (acc_copyin, acc_create, ...). An acc_ev_create event may be preceded by an acc_ev_alloc event, if newly allocated memory is used for this device data, or it may not, if the runtime manages its own memory pool. An acc_ev_delete event is triggered when the OpenACC runtime disassociates device memory from local memory, such as for a data clause at exit from a data construct, compute construct, at an exit data directive, or in a call to a data API routine (acc_copyout, acc_delete, ...). An acc_ev_delete event may be followed by an acc_ev_free event, if the disassociated device memory is freed, or it may not, if the runtime manages its own memory pool.

When the action that generates a data allocation event was generated explicitly by the application code the implicit field in the event structure will be set to 0. When the data allocation event is triggered because of a variable or array with implicitly-determined data attributes or otherwise implicitly by the compiler the implicit field in the event structure will be set to 1.

5.1.5 Data Construct

The events for entering and leaving data constructs are mapped to enter data and exit data events as described in Section 5.1.3 Enter Data and Exit Data.

5.1.6 Update Directive

The update directive event names are

acc_ev_update_start
acc_ev_update_end

The acc_ev_update_start event will be triggered at an update directive, before any data update or wait events that are associated with the update directive are carried out, and the corresponding acc_ev_update_end event will be triggered after any of the associated events.

5.1.7 Compute Construct

The compute construct event names are

acc_ev_compute_construct_start
acc_ev_compute_construct_end

The acc_ev_compute_construct_start event is triggered at entry to a compute construct, before any launch events that are associated with entry to the compute construct. The acc_ev_compute_construct_end event is triggered at the exit of the compute construct, after any launch events associated with exit from the compute construct. If there are data clauses on the compute construct, those data clauses may be treated as part of the compute construct, or as part of a data construct containing the compute construct. The callbacks for data clauses must use the same line numbers as for the compute construct events.

5.1.8 Enqueue Kernel Launch

The launch event names are

acc_ev_enqueue_launch_start
acc_ev_enqueue_launch_end
The **acc_ev_enqueue_launch_start** event is triggered just before an accelerator computation is enqueued for execution on a device, and **acc_ev_enqueue_launch_end** is triggered just after the computation is enqueued. Note that these events are synchronous with the local thread enqueuing the computation to a device, not with the device executing the computation. The **acc_ev_enqueue_launch_start** event callback routine is invoked just before the computation is enqueued, not just before the computation starts execution. More importantly, the **acc_ev_enqueue_launch_end** event callback routine is invoked after the computation is enqueued, not after the computation finished executing.

**Note:** Measuring the time between the start and end launch callbacks is often unlikely to be useful, since it will only measure the time to manage the launch queue, not the time to execute the code on the device.

### 5.1.9 Enqueue Data Update (Upload and Download)

The **data update** event names are

```plaintext
acc_ev_enqueue_upload_start
acc_ev_enqueue_upload_end
acc_ev_enqueue_download_start
acc_ev_enqueue_download_end
```

The **_start** events are triggered just before each upload (data copy from local memory to device memory) operation is or download (data copy from device memory to local memory) operation is enqueued for execution on a device. The corresponding **_end** events are triggered just after each upload or download operation is enqueued.

**Note:** Measuring the time between the start and end update callbacks is often unlikely to be useful, since it will only measure the time to manage the enqueue operation, not the time to perform the actual upload or download.

When the action that generates a **data update** event was generated explicitly by the application code the **implicit** field in the event structure will be set to 0. When the **data allocation** event is triggered because of a variable or array with implicitly-determined data attributes or otherwise implicitly by the compiler the **implicit** field in the event structure will be set to 1.

### 5.1.10 Wait

The **wait** event names are

```plaintext
acc_ev_wait_start
acc_ev_wait_end
```

An **acc_ev_wait_start** will be triggered for each relevant queue before the local thread waits for that queue to be empty. A **acc_ev_wait_end** will be triggered for each relevant queue after the local thread has determined that the queue is empty.

Wait events occur when the local thread and a device synchronize, either due to a **wait** directive or by a **wait** clause on a synchronous data construct, compute construct, or **enter data, exit data**, or **update** directive. For **wait** events triggered by an explicit synchronous **wait** directive
The OpenACC runtime need not trigger wait events for queues that have not been used in the program, and need not trigger wait events for queues that have not been used by this thread since the last wait operation. For instance, an acc wait directive with no arguments is defined to wait on all queues. If the program only uses the default (synchronous) queue and the queue associated with async(1) and async(2) then an acc wait directive may trigger wait events only for those three queues. If the implementation knows that no activities have been enqueued on the async(2) queue since the last wait operation, then the acc wait directive may trigger wait events only for the default queue and the async(1) queue.

5.2 Callbacks Signature

This section describes the signature of event callbacks. All event callbacks have the same signature. The routine prototypes are available in the header file acc_prof.h, which is delivered with the OpenACC implementation.

All callback routines have three arguments. The first argument is a pointer to a struct containing general information; the same struct type is used for all callback events. The second argument is a pointer to a struct containing information specific to that callback event; there is one struct type containing information for data events, another struct type containing information for kernel launch events, and a third struct type for other events, containing essentially no information. The third argument is a pointer to a struct containing information about the application programming interface (API) being used for the specific device. For NVIDIA CUDA devices, this contains CUDA-specific information; for OpenCL devices, this contains OpenCL-specific information. Other interfaces can be supported as they are added by implementations. The prototype for a callback routine is:

```c
typedef void (*acc_prof_callback)(acc_prof_info*, acc_event_info*, acc_api_info*);
```

In the descriptions, the datatype ssize_t means a signed 32-bit integer for a 32-bit binary and a 64-bit integer for a 64-bit binary, the datatype size_t means an unsigned 32-bit integer for a 32-bit binary and a 64-bit integer for a 64-bit binary, and the datatype int means a 32-bit integer for both 32-bit and 64-bit binaries. A null pointer is the pointer with value zero.

5.2.1 First Argument: General Information

The first argument is a pointer to the acc_prof_info struct type:

```c
typedef struct acc_prof_info{
  acc_event_t event_type;
  int valid_bytes;
  int version;
  acc_device_t device_type;
  int device_number;
  int thread_id;
  ssize_t async;
  ssize_t async_queue;
  const char* src_file;
  const char* func_name;
};
```
The fields are described below.

- **acc_event_t event_type** - The event type that triggered this callback. The datatype is the enumeration type `acc_event_t`, described in the previous section. This allows the same callback routine to be used for different events.

- **int valid_bytes** - The number of valid bytes in this struct. This allows a library to interface with newer runtimes that may add new fields to the struct at the end while retaining compatibility with older runtimes. A runtime must fill in the `event_type` and `valid_bytes` fields, and must fill in values for all fields with offset less than `valid_bytes`. The value of `valid_bytes` for a struct is recursively defined as:

  \[
  \text{valid_bytes(struct)} = \text{offset(lastfield)} + \text{valid_bytes(lastfield)}
  \]

  \[
  \text{valid_bytes(type[n])} = (n-1)\times \text{sizeof(type)} + \text{valid_bytes(type)}
  \]

  \[
  \text{valid_bytes(basictype)} = \text{sizeof(basictype)}
  \]

- **int version** - A version number; the value of `OPENACC`.

- **acc_device_t device_type** - The device type corresponding to this event. The datatype is `acc_device_t`, an enumeration type of all the supported device types, defined in `openacc.h`.

- **int device_number** - The device number. Each device is numbered, typically starting at device zero. For applications that use more than one device type, the device numbers may be unique across all devices or may be unique only across all devices of the same device type.

- **int thread_id** - The host thread ID making the callback. Host threads are given unique thread ID numbers typically starting at zero. This is not necessarily the same as the OpenMP thread number.

- **ssize_t async** - The value of the `async()` clause for the directive that triggered this callback.

- **ssize_t async_queue** - If the runtime uses a limited number of asynchronous queues, this field contains the internal asynchronous queue number used for the event.

- **const char* src_file** - A pointer to null-terminated string containing the name of or path to the source file, if known, or a null pointer if not. If the library wants to save the source file name, it should allocate memory and copy the string.

- **const char* func_name** - A pointer to a null-terminated string containing the name of the function in which the event occurred, if known, or a null pointer if not. If the library wants to save the function name, it should allocate memory and copy the string.

- **int line_no** - The line number of the directive or program construct or the starting line number of the OpenACC construct corresponding to the event. A negative or zero value means the line number is not known.

- **int end_line_no** - For an OpenACC construct, this contains the line number of the end of the construct. A negative or zero value means the line number is not known.
5.2. Callbacks Signature

- **int func_line_no** - The line number of the first line of the function named in `func_name`. A negative or zero value means the line number is not known.
- **int func_end_line_no** - The last line number of the function named in `func_name`. A negative or zero value means the line number is not known.

5.2.2 Second Argument: Event-Specific Information

The second argument is a pointer to the `acc_event_info` union type.

```c
typedef union acc_event_info{
    acc_event_t event_type;
    acc_data_event_info data_event;
    acc_launch_event_info launch_event;
    acc_other_event_info other_event;
} acc_event_info;
```

The `event_type` field selects which union member to use. The first five members of each union member are identical. The second through fifth members of each union member (`valid_bytes`, `parent_construct`, `implicit`, and `tool_info`) have the same semantics for all event types:

- **int valid_bytes** - The number of valid bytes in the respective struct. (This field is similar used as discussed in Section 5.2.1 First Argument: General Information)
- **acc_construct_t parent_construct** - This field describes the type of construct that caused the event to be emitted. The possible values for this field are defined by the `acc_construct_t` enum, described at the end of this section.
- **int implicit** - This field is set to 1 for any implicit event, such as an implicit wait at a synchronous data construct or synchronous enter data, exit data or update directive. This field is set to zero when the event is triggered by an explicit directive or call to a runtime API routine.
- **void* tool_info** - This field is used to pass tool-specific information from a _start event to the matching _end event. For a _start event callback, this field will be initialized to a null pointer. The value of this field for a _end event will be the value returned by the library in this field from the matching _start event callback, if there was one, or null otherwise. For events that are neither _start or _end events, this field will be null.

### Data Events

For a data event, as noted in the event descriptions, the second argument will be a pointer to the `acc_data_event_info` struct.

```c
typedef struct acc_data_event_info{
    acc_event_t event_type;
    int valid_bytes;
    acc_construct_t parent_construct;
    int implicit;
    void* tool_info;
    const char* var_name;
} acc_data_event_info;
```
The fields specific for a data event are:

- `acc_event_t event_type` - The event type that triggered this callback. The events that use the `acc_data_event_info` struct are:
  - `acc_ev_enqueue_upload_start`
  - `acc_ev_enqueue_upload_end`
  - `acc_ev_enqueue_download_start`
  - `acc_ev_enqueue_download_end`
  - `acc_ev_create`
  - `acc_ev_delete`
  - `acc_ev_alloc`
  - `acc_ev_free`

- `const char* var_name` - A pointer to null-terminated string containing the name of the variable for which this event is triggered, if known, or a null pointer if not. If the library wants to save the variable name, it should allocate memory and copy the string.

- `size_t bytes` - The number of bytes for the data event.

- `const void* host_ptr` - If available and appropriate for this event, this is a pointer to the host data.

- `const void* device_ptr` - If available and appropriate for this event, this is a pointer to the corresponding device data.

### Launch Events

For a launch event, as noted in the event descriptions, the second argument will be a pointer to the `acc_launch_event_info` struct.

```c
typedef struct acc_launch_event_info{
  acc_event_t event_type;
  int valid_bytes;
  acc_construct_t parent_construct;
  int implicit;
  void* tool_info;
  const char* kernel_name;
  size_t num_gangs, num_workers, vector_length;
}acc_launch_event_info;
```

The fields specific for a launch event are:

- `acc_event_t event_type` - The event type that triggered this callback. The events that use the `acc_launch_event_info` struct are:
  - `acc_ev_enqueue_launch_start`
  - `acc_ev_enqueue_launch_end`
• **const char* kernel_name** - A pointer to null-terminated string containing the name of the kernel being launched, if known, or a null pointer if not. If the library wants to save the kernel name, it should allocate memory and copy the string.

• **size_t num_gangs, num_workers, vector_length** - The number of gangs, workers and vector lanes created for this kernel launch.

### Other Events

For any event that does not use the `acc_data_event_info` or `acc_launch_event_info` struct, the second argument to the callback routine will be a pointer to `acc_other_event_info` struct.

```c
typedef struct acc_other_event_info {
  acc_event_t event_type;
  int valid_bytes;
  acc_construct_t parent_construct;
  int implicit;
  void* tool_info;
} acc_other_event_info;
```

### Parent Construct Enumeration

All event structures contain a `parent_construct` member that describes the type of construct that caused the event to be emitted. The purpose of this field is to provide a means to identify the type of construct emitting the event in the cases where an event may be emitted by multiple construct types, such as is the case with data and wait events. The possible values for the `parent_construct` field are defined in the enumeration type `acc_construct_t`. In the case of combined directives, the outermost construct of the combined construct should be specified as the `parent_construct`. If the event was emitted as the result of the application making a call to the runtime api, the value will be `acc_construct_runtime_api`.

```c
typedef enum acc_construct_t {
  acc_construct_parallel = 0,
  acc_construct_kernels = 1,
  acc_construct_loop = 2,
  acc_construct_data = 3,
  acc_construct_enter_data = 4,
  acc_construct_exit_data = 5,
  acc_construct_host_data = 6,
  acc_construct_atomic = 7,
  acc_construct_declare = 8,
  acc_construct_init = 9,
  acc_construct_shutdown = 10,
  acc_construct_set = 11,
  acc_construct_update = 12,
  acc_construct_runtime = 13,
  acc_construct_wait = 14,
  acc_construct_runtime_api = 15,
};
```
acc_construct_serial = 16
}acc_construct_t;

5.2.3 Third Argument: API-Specific Information

The third argument is a pointer to the acc_api_info struct type, shown here.

typedef struct acc_api_info{
    acc_device_api device_api;
    int valid_bytes;
    acc_device_t device_type;
    int vendor;
    const void* device_handle;
    const void* context_handle;
    const void* async_handle;
}acc_api_info;

The fields are described below:

- **acc_device_api device_api** - The API in use for this device. The data type is the enumeration acc_device_api, which is described later in this section.

- **int valid_bytes** - The number of valid bytes in this struct. See the discussion above in Section 5.2.1 First Argument: General Information.

- **acc_device_t device_type** - The device type; the datatype is acc_device_t, defined in openacc.h.

- **int vendor** - An identifier to identify the OpenACC vendor; contact your vendor to determine the value used by that vendor’s runtime.

- **const void* device_handle** - If applicable, this will be a pointer to the API-specific device information.

- **const void* context_handle** - If applicable, this will be a pointer to the API-specific context information.

- **const void* async_handle** - If applicable, this will be a pointer to the API-specific async queue information.

According to the value of device_api a library can cast the pointers of the fields device_handle, context_handle and async_handle to the respective device API type. The following device APIs are defined in the interface below. Any implementation-defined device API type must have a value greater than acc_device_api_implementation_defined.

typedef enum acc_device_api{
    acc_device_api_none = 0,       /* no device API */
    acc_device_api_cuda = 1,       /* CUDA driver API */
    acc_device_api_opencl = 2,     /* OpenCL API */
    acc_device_api_other = 4,      /* other device API */
    acc_device_api_implementation_defined = 1000 /* other device API */
}acc_device_api;
5.3 Loading the Library

This section describes how a tools library is loaded when the program is run. Four methods are described:

- A tools library may be linked with the program, as any other library is linked, either as a static library or a dynamic library, and the runtime will call a predefined library initialization routine that will register the event callbacks.

- The OpenACC runtime implementation may support a dynamic tools library, such as a shared object for Linux or OS/X, or a DLL for Windows, which is then dynamically loaded at runtime under control of the environment variable ACC_PROFLIB.

- Some implementations where the OpenACC runtime is itself implemented as a dynamic library may support adding a tools library using the LD_PRELOAD feature in Linux.

- A tools library may be linked with the program, as in the first option, and the application itself may directly register event callback routines, or may invoke a library initialization routine that will register the event callbacks.

Callbacks are registered with the runtime by calling acc_prof_register for each event as described in Section 5.4 Registering Event Callbacks. The prototype for acc_prof_register is:

```c
extern void acc_prof_register
(acc_event_t event_type, acc_prof_callback cb,
 acc_register_t info);
```

The first argument to acc_prof_register is the event for which a callback is being registered (compare Section 5.1 Events). The second argument is a pointer to the callback routine:

```c
typedef void (*acc_prof_callback)
(acc_prof_info*, acc_event_info*, acc_api_info*);
```

The third argument is usually zero (or acc_reg). See Section 5.4.2 Disabling and Enabling Callbacks for cases where a nonzero value is used. The argument acc_register_t is an enum type:

```c
typedef enum acc_register_t{
  acc_reg = 0,
  acc_toggle = 1,
  acc_toggle_per_thread = 2
} acc_register_t;
```

An example of registering callbacks for launch, upload, and download events is:

```c
acc_prof_register(acc_ev_enqueue_launch_start, prof_launch, 0);
acc_prof_register(acc_ev_enqueue_upload_start, prof_data, 0);
acc_prof_register(acc_ev_enqueue_download_start, prof_data, 0);
```

As shown in this example, the same routine (prof_data) can be registered for multiple events. The routine can use the event_type field in the acc_prof_info structure to determine for what event it was invoked.
5.3.1 Library Registration

The OpenACC runtime will invoke `acc_register_library`, passing the addresses of the registration routines `acc_prof_register` and `acc_prof_unregister`, in case that routine comes from a dynamic library. In the third argument it passes the address of the lookup routine `acc_prof_lookup` to obtain the addresses of inquiry functions. No inquiry functions are defined in this profiling interface, but we preserve this argument for future support of sampling-based tools.

Typically, the OpenACC runtime will include a weak definition of `acc_register_library`, which does nothing and which will be called when there is no tools library. In this case, the library can save the addresses of these routines and/or make registration calls to register any appropriate callbacks. The prototype for `acc_register_library` is:

```c
extern void acc_register_library
    (acc_prof_reg reg, acc_prof_reg unreg,
     acc_prof_lookup_func lookup);
```

The first two arguments of this routine are of type:

```c
typedef void (*acc_prof_reg)
    (acc_event_t event_type, acc_prof_callback cb,
     acc_register_t info);
```

The third argument passes the address to the lookup function `acc_prof_lookup` to obtain the address of interface functions. It is of type:

```c
typedef void (*acc_query_fn)();
typedef acc_query_fn (*acc_prof_lookup_func)
    (const char* acc_query_fn_name);
```

The argument of the lookup function is a string with the name of the inquiry function. There are no inquiry functions defined for this interface.

5.3.2 Statically-Linked Library Initialization

A tools library can be compiled and linked directly into the application. If the library provides an external routine `acc_register_library` as specified in Section 5.3.1 Library Registration, the runtime will invoke that routine to initialize the library.

The sequence of events is:

1. The runtime invokes the `acc_register_library` routine from the library.
2. The `acc_register_library` routine calls `acc_prof_register` for each event to be monitored.
3. `acc_prof_register` records the callback routines.
4. The program runs, and your callback routines are invoked at the appropriate events.

In this mode, only one tool library is supported.
5.3.3 Runtime Dynamic Library Loading

A common case is to build the tools library as a dynamic library (shared object for Linux or OS/X, DLL for Windows). In that case, you can have the OpenACC runtime load the library during initialization. This allows you to enable runtime profiling without rebuilding or even relinking your application. The dynamic library must implement a registration routine `acc_register_library` as specified in Section 5.3.1 Library Registration.

The user may set the environment variable `ACC_PROFLIB` to the path to the library will tell the OpenACC runtime to load your dynamic library at initialization time:

**Bash:**

```
export ACC_PROFLIB=/home/user/lib/myprof.so
./myapp
```

**or**

```
ACC_PROFLIB=/home/user/lib/myprof.so ./myapp
```

**C-shell:**

```
setenv ACC_PROFLIB /home/user/lib/myprof.so
./myapp
```

When the OpenACC runtime initializes, it will read the `ACC_PROFLIB` environment variable (with `getenv`). The runtime will open the dynamic library (using `dlopen` or `LoadLibraryA`); if the library cannot be opened, the runtime may abort, or may continue execution with or without an error message. If the library is successfully opened, the runtime will get the address of the `acc_register_library` routine (using `dlsym` or `GetProcAddress`). If this routine is resolved in the library, it will be invoked passing in the addresses of the registration routine `acc_prof_register`, the deregistration routine `acc_prof_unregister`, and the lookup routine `acc_prof_lookup`. The registration routine in your library, `acc_register_library`, should register the callbacks by calling the `register` argument, and should save the addresses of the arguments (`register`, `unregister`, and `lookup`) for later use, if needed.

The sequence of events is:

1. Initialization of the OpenACC runtime.
2. OpenACC runtime reads `ACC_PROFLIB`.
3. OpenACC runtime loads the library.
4. OpenACC runtime calls the `acc_register_library` routine in that library.
5. Your `acc_register_library` routine calls `acc_prof_register` for each event to be monitored.
6. `acc_prof_register` records the callback routines.
7. The program runs, and your callback routines are invoked at the appropriate events.

If supported, paths to multiple dynamic libraries may be specified in the `ACC_PROFLIB` environment variable, separated by semicolons (`;`). The OpenACC runtime will open these libraries and invoke the `acc_register_library` routine for each, in the order they appear in `ACC_PROFLIB`.
5.3.4 Preloading with LD_PRELOAD

The implementation may also support dynamic loading of a tools library using the LD_PRELOAD feature available in some systems. In such an implementation, you need only specify your tools library path in the LD_PRELOAD environment variable before executing your program. The OpenACC runtime will invoke the acc_register_library routine in your tools library at initialization time. This requires that the OpenACC runtime include a dynamic library with a default (empty) implementation of acc_register_library that will be invoked in the normal case where there is no LD_PRELOAD setting. If an implementation only supports static linking, or if the application is linked without dynamic library support, this feature will not be available.

Bash:
```bash
export LD_PRELOAD=/home/user/lib/myprof.so
./myapp
```
or
```bash
LD_PRELOAD=/home/user/lib/myprof.so ./myapp
```
C-shell:
```csh
setenv LD_PRELOAD /home/user/lib/myprof.so
./myapp
```

The sequence of events is:
1. The operating system loader loads the library specified in LD_PRELOAD.
2. The call to acc_register_library in the OpenACC runtime is resolved to the routine in the loaded tools library.
3. OpenACC runtime calls the acc_register_library routine in that library.
4. Your acc_register_library routine calls acc_prof_register for each event to be monitored.
5. acc_prof_register records the callback routines.
6. The program runs, and your callback routines are invoked at the appropriate events.

In this mode, only a single tools library is supported, since only one acc_register_library initialization routine will get resolved by the dynamic loader.

5.3.5 Application-Controlled Initialization

An alternative to default initialization is to have the application itself call the library initialization routine, which then calls acc_prof_register for each appropriate event. The library may be statically linked to the application or your application may dynamically load the library.

The sequence of events is:
1. Your application calls the library initialization routine.
2. The library initialization routine calls acc_prof_register for each event to be monitored.
3. acc_prof_register records the callback routines.
4. The program runs, and your callback routines are invoked at the appropriate events.
5.4 Registering Event Callbacks

In this mode, multiple tools libraries can be supported, with each library initialization routine invoked by the application.

5.4.1 Event Registration and Unregistration

This section describes how to register and unregister callbacks, temporarily disabling and enabling callbacks, the behavior of dynamic registration and unregistration, and requirements on an OpenACC implementation to correctly support the interface.

In this example the prof_data routine will be invoked for each data upload and download event, and the prof_launch routine will be invoked for each launch event. The prof_data routine might start out with:

```c
void prof_data(acc_prof_info* profinfo,
               acc_event_info* eventinfo, acc_api_info* apiinfo)
{
    acc_data_event_info* datainfo;
    datainfo = (acc_data_event_info*)eventinfo;
    switch( datainfo->event_type ){
        case acc_ev_enqueue_upload_start :
            ...
    }
}
```

Multiple Callbacks

Multiple callback routines can be registered on the same event:

```c
acc_prof_register(acc_ev_enqueue_upload_start, prof_data, 0);
acc_prof_register(acc_ev_enqueue_upload_start, prof_up, 0);
```
If a callback is registered, then later unregistered, then later still registered again, the second registration is considered to be a new callback, and the callback routine will then be appended to the tail of the callback list for that event.

### Unregistering

A matching call to `acc_prof_unregister` will remove that routine from the list of callback routines for that event.

```c
acc_prof_register(acc_ev_enqueue_upload_start, prof_data, 0);
// prof_data is on the callback list for acc_ev_enqueue_upload_start
...
acc_prof_unregister(acc_ev_enqueue_upload_start, prof_data, 0);
// prof_data is removed from the callback list for acc_ev_enqueue_upload_start
```

Each entry on the callback list must also have a `ref` count. This keeps track of how many times this routine was added to this event’s callback list. If a routine is registered `n` times, it must be unregistered `n` times before it is removed from the list. Note that if a routine is registered multiple times for the same event, its `ref` count will be incremented with each registration, but it will only be invoked once for each event instance.

### 5.4.2 Disabling and Enabling Callbacks

A callback routine may be temporarily disabled on the callback list for an event, then later re-enabled. The behavior is slightly different than unregistering and later re-registering that event.

When a routine is disabled and later re-enabled, the routine’s position on the callback list for that event is preserved. When a routine is unregister and later re-registered, the routine’s position on the callback list for that event will move to the tail of the list. Also, unregistering a callback must be done `n` times if the callback routine was registered `n` times. In contrast, disabling, and enabling an event sets a toggle. Disabling a callback will immediately reset the toggle and disable calls to that routine for that event, even if it was enabled multiple times. Enabling a callback will immediately set the toggle and enable calls to that routine for that event, even if it was disabled multiple times. Registering a new callback initially sets the toggle.

A call to `acc_prof_unregister` with a value of `acc_toggle` as the third argument will disable callbacks to the given routine. A call to `acc_prof_register` with a value of `acc_toggle` as the third argument will enable those callbacks.

```c
acc_prof_unregister(acc_ev_enqueue_upload_start, prof_data, acc_toggle);
// prof_data is disabled
...
acc_prof_register(acc_ev_enqueue_upload_start, prof_data, acc_toggle);
// prof_data is re-enabled
```

A call to either `acc_prof_unregister` or `acc_prof_register` to disable or enable a callback when that callback is not currently registered for that event will be ignored with no error.

All callbacks for an event may be disabled (and re-enabled) by passing `NULL` to the second argument and `acc_toggle` to the third argument of `acc_prof_unregister` (and `acc_prof_register`).
This sets a toggle for that event, which is distinct from the toggle for each callback for that event. While the event is disabled, no callbacks for that event will be invoked. Callbacks for that event can be registered, unregistered, enabled, and disabled while that event is disabled, but no callbacks will be invoked for that event until the event itself is enabled. Initially, all events are enabled.

```
acc_prof_unregister(acc_ev_enqueue_upload_start,
   prof_data, acc_toggle);
// prof_data is disabled
...
acc_prof_unregister(acc_ev_enqueue_upload_start,
   NULL, acc_toggle);
// acc_ev_enqueue_upload_start callbacks are disabled
...
acc_prof_register(acc_ev_enqueue_upload_start,
   prof_data, acc_toggle);
// prof_data is re-enabled, but
// acc_ev_enqueue_upload_start callbacks still disabled
...
acc_prof_register(acc_ev_enqueue_upload_start, prof_up, 0);
// prof_up is registered and initially enabled, but
// acc_ev_enqueue_upload_start callbacks still disabled
...
acc_prof_register(acc_ev_enqueue_upload_start,
   NULL, acc_toggle);
// acc_ev_enqueue_upload_start callbacks are enabled
```

Finally, all callbacks can be disabled (and enabled) by passing the argument list (0, NULL, acc_toggle) to acc_prof_unregister (and acc_prof_register). This sets a global toggle disabling all callbacks, which is distinct from the toggle enabling callbacks for each event and the toggle enabling each callback routine. The behavior of passing zero as the first argument and a non-NULL value as the second argument to acc_prof_unregister or acc_prof_register is not defined, and may be ignored by the runtime without error.

All callbacks can be disabled (or enabled) for just the current thread by passing the argument list (0, NULL, acc_toggle_per_thread) to acc_prof_unregister (and acc_prof_register). This is the only thread-specific interface to acc_prof_register and acc_prof_unregister, all other calls to register, unregister, enable, or disable callbacks affect all threads in the application.

### 5.5 Advanced Topics

This section describes advanced topics such as dynamic registration and changes of the execution state for callback routines as well as the runtime and tool behavior for multiple host threads.

#### 5.5.1 Dynamic Behavior

Callback routines may be registered or unregistered, enabled or disabled at any point in the execution of the program. Calls may appear in the library itself, during the processing of an event. The OpenACC runtime must allow for this case, where the callback list for an event is modified while that event is being processed.
Dynamic Registration and Unregistration

Calls to `acc_register` and `acc_unregister` may occur at any point in the application. A callback routine can be registered or unregistered from a callback routine, either the same routine or another routine, for a different event or the same event for which the callback was invoked. If a callback routine is registered for an event while that event is being processed, then the new callback routine will be added to the tail of the list of callback routines for this event. Some events (the `_end`) events process the callback routines in reverse order, from the tail to the head. For those events, adding a new callback routine will not cause the new routine to be invoked for this instance of the event. The other events process the callback routines in registration order, from the head to the tail. Adding a new callback routine for such a event will cause the runtime to invoke that newly registered callback routine for this instance of the event. Both the runtime and the library must implement and expect this behavior.

If an existing callback routine is unregistered for an event while that event is being processed, that callback routine is removed from the list of callbacks for this event. For any event, if that callback routine had not yet been invoked for this instance of the event, it will not be invoked.

Registering and unregistering a callback routine is a global operation and affects all threads, in a multithreaded application. See Section 5.4.1 Multiple Callbacks.

Dynamic Enabling and Disabling

Calls to `acc_register` and `acc_unregister` to enable and disable a specific callback for an event, enable or disable all callbacks for an event, or enable or disable all callbacks may occur at any point in the application. A callback routine can be enabled or disabled from a callback routine, either the same routine or another routine, for a different event or the same event for which the callback was invoked. If a callback routine is enabled for an event while that event is being processed, then the new callback routine will be immediately enabled. If it appears on the list of callback routines closer to the head (for `_end` events) or closer to the tail (for other events), that newly-enabled callback routine will be invoked for this instance of this event, unless it is disabled or unregistered before that callback is reached.

If a callback routine is disabled for an event while that event is being processed, that callback routine is immediately disabled. For any event, if that callback routine had not yet been invoked for this instance of the event, it will not be invoked, unless it is enabled before that callback routine is reached in the list of callbacks for this event. If all callbacks for an event are disabled while that event is being processed, or all callbacks are disabled for all events while an event is being processed, then when this callback routine returns, no more callbacks will be invoked for this instance of the event.

Registering and unregistering a callback routine is a global operation and affects all threads, in a multithreaded application. See Section 5.4.1 Multiple Callbacks.

5.5.2 OpenACC Events During Event Processing

OpenACC events may occur during event processing. This may be because of OpenACC API routine calls or OpenACC constructs being reached during event processing, or because of multiple host threads executing asynchronously. Both the OpenACC runtime and the tool library must implement the proper behavior.
5.5.3 Multiple Host Threads

Many programs that use OpenACC also use multiple host threads, such as programs using the OpenMP API. The appearance of multiple host threads affects both the OpenACC runtime and the tools library.

Runtime Support for Multiple Threads

The OpenACC runtime must be thread-safe, and the OpenACC runtime implementation of this tools interface must also be thread-safe. All threads use the same set of callbacks for all events, so registering a callback from one thread will cause all threads to execute that callback. This means that managing the callback lists for each event must be protected from multiple simultaneous updates. This includes adding a callback to the tail of the callback list for an event, removing a callback from the list for an event, and incrementing or decrementing the ref count for a callback routine for an event.

In addition, one thread may register, unregister, enable, or disable a callback for an event while another thread is processing the callback list for that event asynchronously. The exact behavior may be dependent on the implementation, but some behaviors are expected and others are disallowed. In the following examples, there are three callbacks, A, B, and C, registered for event E in that order, where callbacks A and B are enabled and callback C is temporarily disabled. Thread T1 is dynamically modifying the callbacks for event E while thread T2 is processing an instance of event E.

- Suppose thread T1 unregisters or disables callback A for event E. Thread T2 may or may not invoke callback A for this event instance, but it must invoke callback B; if it invokes callback A, that must precede the invocation of callback B.
- Suppose thread T1 unregisters or disables callback B for event E. Thread T2 may or may not invoke callback B for this event instance, but it must invoke callback A; if it invokes callback B, that must follow the invocation of callback A.
- Suppose thread T1 unregisters or disables callback A and then unregisters or disables callback B for event E. Thread T2 may or may not invoke callback A and may or may not invoke callback B for this event instance, but if it invokes both callbacks, it must invoke callback A before it invokes callback B.
- Suppose thread T1 unregisters or disables callback B and then unregisters or disables callback A for event E. Thread T2 may or may not invoke callback A and may or may not invoke callback B for this event instance, but if it invokes callback B, it must have invoked callback A for this event instance.
- Suppose thread T1 is registering a new callback D for event E. Thread T2 may or may not invoke callback D for this event instance, but it must invoke both callbacks A and B. If it invokes callback D, that must follow the invocations of A and B.
- Suppose thread T1 is enabling callback C for event E. Thread T2 may or may not invoke callback C for this event instance, but it must invoke both callbacks A and B. If it invokes callback C, that must follow the invocations of A and B.

The acc_prof_info struct has a thread_id field, which the runtime must set to a unique value for each host thread, though it need not be the same as the OpenMP threadnum value.
Library Support for Multiple Threads

The tool library must also be thread-safe. The callback routine will be invoked in the context of the thread that reaches the event. The library may receive a callback from a thread T2 while it’s still processing a callback, from the same event type or from a different event type, from another thread T1. The `acc_prof_info` struct has a `thread_id` field, which the runtime must set to a unique value for each host thread.

If the tool library uses dynamic callback registration and unregistration, or callback disabling and enabling, recall that unregistering or disabling an event callback from one thread will unregister or disable that callback for all threads, and registering or enabling an event callback from any thread will register or enable it for all threads. If two or more threads register the same callback for the same event, the behavior is the same as if one thread registered that callback multiple times; see Section 5.4.1 Multiple Callbacks. The `acc_unregister` routine must be called as many times as `acc_register` for that callback/event pair in order to totally unregister it. If two threads register two different callback routines for the same event, unless the order of the registration calls is guaranteed by some synchronization method, the order in which the runtime sees the registration may differ for multiple runs, meaning the order in which the callbacks occur will differ as well.
6. Glossary

Clear and consistent terminology is important in describing any programming model. We define here the terms you must understand in order to make effective use of this document and the associated programming model. In particular, some terms used in this specification conflict with their usage in the base language specifications. When there is potential confusion, the term will appear here.

**Accelerator** – a device attached to a CPU and to which the CPU can offload data and compute kernels to perform compute-intensive calculations.

**Accelerator routine** – a C or C++ function or Fortran subprogram compiled for the accelerator with the `routine` directive.

**Accelerator thread** – a thread of execution that executes on the accelerator; a single vector lane of a single worker of a single gang.

**Aggregate datatype** – any non-scalar datatype such as array and composite datatypes. In Fortran, aggregate datatypes include arrays, derived types, character types. In C, aggregate datatypes include arrays, targets of pointers, structs, and unions. In C++, aggregate datatypes include arrays, targets of pointers, classes, structs, and unions.

**Aggregate variables** – a variable of any non-scalar datatype, including array or composite variables. In Fortran, this includes any variable with allocatable or pointer attribute and character variables.

**Async-argument** – an async-argument is a nonnegative scalar integer expression (`int` for C or C++, `integer` for Fortran), or one of the special values `acc_async_noval` or `acc_async_sync`.

**Barrier** – a type of synchronization where all parallel execution units or threads must reach the barrier before any execution unit or thread is allowed to proceed beyond the barrier; modeled after the starting barrier on a horse race track.

**Block construct** – a block-construct, as specified by the Fortran language.

**Composite datatype** – a derived type in Fortran, or a `struct` or `union` type in C, or a `class`, `struct`, or `union` type in C++. (This is different from the use of the term `composite data type` in the C and C++ languages.)

**Composite variable** – a variable of composite datatype. In Fortran, a composite variable must not have allocatable or pointer attributes.

**Compute construct** – a parallel construct, kernels construct, or serial construct.

**Compute intensity** – for a given loop, region, or program unit, the ratio of the number of arithmetic operations performed on computed data divided by the number of memory transfers required to move that data between two levels of a memory hierarchy.

**Compute region** – a parallel region, kernels region, or serial region.

**Construct** – a directive and the associated statement, loop, or structured block, if any.

**CUDA** – the CUDA environment from NVIDIA is a C-like programming environment used to explicitly control and program an NVIDIA GPU.
Current device – the device represented by the acc-current-device-type-var and acc-current-device-num-var ICVs

Current device type – the device type represented by the acc-current-device-type-var ICV

Data lifetime – the lifetime of a data object in device memory, which may begin at the entry to a data region, or at an enter data directive, or at a data API call such as acc_copyin or acc_create, and which may end at the exit from a data region, or at an exit data directive, or at a data API call such as acc_delete, acc_copyout, or acc_shutdown, or at the end of the program execution.

Data region – a region defined by a data construct, or an implicit data region for a function or subroutine containing OpenACC directives. Data constructs typically allocate device memory and copy data from host to device memory upon entry, and copy data from device to local memory and deallocate device memory upon exit. Data regions may contain other data regions and compute regions.

Default asynchronous queue – the asynchronous activity queue represented in the acc-default-async-var ICV

Device – a general reference to an accelerator or a multicore CPU.

Device memory – memory attached to a device, logically and physically separate from the host memory.

Device thread – a thread of execution that executes on any device.

Directive – in C or C++, a #pragma, or in Fortran, a specially formatted comment statement, that is interpreted by a compiler to augment information about or specify the behavior of the program.

Discrete memory – memory accessible from the local thread that is not accessible from the current device, or memory accessible from the current device that is not accessible from the local thread.

DMA – Direct Memory Access, a method to move data between physically separate memories; this is typically performed by a DMA engine, separate from the host CPU, that can access the host physical memory as well as an IO device or other physical memory.

GPU – a Graphics Processing Unit; one type of accelerator.

GPGPU – General Purpose computation on Graphics Processing Units.

Host – the main CPU that in this context may have one or more attached accelerators. The host CPU controls the program regions and data loaded into and executed on one or more devices.

Host thread – a thread of execution that executes on the host.

Implicit data region – the data region that is implicitly defined for a Fortran subprogram or C function. A call to a subprogram or function enters the implicit data region, and a return from the subprogram or function exits the implicit data region.

Kernel – a nested loop executed in parallel by the accelerator. Typically the loops are divided into a parallel domain, and the body of the loop becomes the body of the kernel.

Kernels region – a region defined by a kernels construct. A kernels region is a structured block which is compiled for the accelerator. The code in the kernels region will be divided by the compiler into a sequence of kernels; typically each loop nest will become a single kernel. A kernels region
may require space in device memory to be allocated and data to be copied from local memory to
device memory upon region entry, and data to be copied from device memory to local memory and
space in device memory to be deallocated upon exit.

**Level of parallelism** – The possible levels of parallelism in OpenACC are gang, worker, vector,
and sequential. One or more of gang, worker, and vector parallelism may appear on a loop con-
struct. Sequential execution corresponds to no parallelism. The `gang`, `worker`, `vector`, and
**seq** clauses specify the level of parallelism for a loop.

**Local device** – the device where the *local thread* executes.

**Local memory** – the memory associated with the *local thread*.

**Local thread** – the host thread or the accelerator thread that executes an OpenACC directive or
construct.

**Loop trip count** – the number of times a particular loop executes.

**MIMD** – a method of parallel execution (Multiple Instruction, Multiple Data) where different exe-
cution units or threads execute different instruction streams asynchronously with each other.

**OpenCL** – short for Open Compute Language, a developing, portable standard C-like programming
environment that enables low-level general-purpose programming on GPUs and other accelerators.

**Orphaned loop construct** - a loop construct that is not lexically contained in any compute con-
struct, that is, that has no parent compute construct.

**Parallel region** – a *region* defined by a `parallel` construct. A parallel region is a structured block
which is compiled for the accelerator. A parallel region typically contains one or more work-sharing
loops. A parallel region may require space in device memory to be allocated and data to be copied
from local memory to device memory upon region entry, and data to be copied from device memory
to local memory and space in device memory to be deallocated upon exit.

**Parent compute construct** – for a loop construct, the `parallel`, kernels, or `serial` con-
struct that lexically contains the loop construct and is the innermost compute construct that con-
tains that loop construct, if any.

**Present data** – data for which the sum of the structured and dynamic reference counters is greater
than zero.

**Private data** – with respect to an iterative loop, data which is used only during a particular loop
iteration. With respect to a more general region of code, data which is used within the region but is
not initialized prior to the region and is re-initialized prior to any use after the region.

**Procedure** – in C or C++, a function in the program; in Fortran, a subroutine or function.

**Region** – all the code encountered during an instance of execution of a construct. A region includes
any code in called routines, and may be thought of as the dynamic extent of a construct. This may
be a `parallel region`, kernels region, `serial region`, data region or implicit data region.

**Scalar** – a variable of scalar datatype. In Fortran, scalars must not have allocatable or pointer
attributes.

**Scalar datatype** – an intrinsic or built-in datatype that is not an array or aggregate datatype. In For-
tran, scalar datatypes are integer, real, double precision, complex, or logical. In C, scalar datatypes
are char (signed or unsigned), int (signed or unsigned, with optional short, long or long long attribute), enum, float, double, long double, _Complex (with optional float or long attribute), or any pointer datatype. In C++, scalar datatypes are char (signed or unsigned), wchar_t, int (signed or unsigned, with optional short, long or long long attribute), enum, bool, float, double, long double, or any pointer datatype. Not all implementations or targets will support all of these datatypes.

**Serial region** – a region defined by a serial construct. A serial region is a structured block which is compiled for the accelerator. A serial region contains code that is executed by a single gang of a single worker with a vector length of one. A serial region may require space in device memory to be allocated and data to be copied from local memory to device memory upon region entry, and data to be copied from device memory to local memory and space in device memory to be deallocated upon exit.

**Shared memory** – memory that is accessible from both the local thread and the current device.

**SIMD** – A method of parallel execution (single-instruction, multiple-data) where the same instruction is applied to multiple data elements simultaneously.

**SIMD operation** – a vector operation implemented with SIMD instructions.

**Structured block** – in C or C++, an executable statement, possibly compound, with a single entry at the top and a single exit at the bottom. In Fortran, a block of executable statements with a single entry at the top and a single exit at the bottom.

**Thread** – On a host CPU, a thread is defined by a program counter and stack location; several host threads may comprise a process and share host memory. On an accelerator, a thread is any one vector lane of one worker of one gang.

**var** – the name of a variable (scalar, array, or composite variable), or a subarray specification, or an array element, or a composite variable member, or the name of a Fortran common block between slashes.

**Vector operation** – a single operation or sequence of operations applied uniformly to each element of an array.

**Visible device copy** – a copy of a variable, array, or subarray allocated in device memory that is visible to the program unit being compiled.
A. Recommendations for Implementers

This section gives recommendations for standard names and extensions to use for implementations for specific targets and target platforms, to promote portability across such implementations, and recommended options that programmers find useful. While this appendix is not part of the OpenACC specification, implementations that provide the functionality specified herein are strongly recommended to use the names in this section. The first subsection describes devices, such as NVIDIA GPUs. The second subsection describes additional API routines for target platforms, such as CUDA and OpenCL. The third subsection lists several recommended options for implementations.

A.1 Target Devices

A.1.1 NVIDIA GPU Targets

This section gives recommendations for implementations that target NVIDIA GPU devices.

Accelerator Device Type

These implementations should use the name acc_device_nvidia for the acc_device_t type or return values from OpenACC Runtime API routines.

ACC_DEVICE_TYPE

An implementation should use the case-insensitive name nvidia for the environment variable ACC_DEVICE_TYPE.

device_type clause argument

An implementation should use the case-insensitive name nvidia as the argument to the device_type clause.

A.1.2 AMD GPU Targets

This section gives recommendations for implementations that target AMD GPUs.

Accelerator Device Type

These implementations should use the name acc_device_radeon for the acc_device_t type or return values from OpenACC Runtime API routines.

ACC_DEVICE_TYPE

These implementations should use the case-insensitive name radeon for the environment variable ACC_DEVICE_TYPE.

device_type clause argument

An implementation should use the case-insensitive name radeon as the argument to the device_type clause.
A.1.3 Multicore Host CPU Target

This section gives recommendations for implementations that target the multicore host CPU.

Accelerator Device Type

These implementations should use the name acc_device_host for the acc_device_t type or return values from OpenACC Runtime API routines.

ACC DEVICE TYPE

These implementations should use the case-insensitive name host for the environment variable ACC DEVICE TYPE.

device_type clause argument

An implementation should use the case-insensitive name host as the argument to the device_type clause.

A.2 API Routines for Target Platforms

These runtime routines allow access to the interface between the OpenACC runtime API and the underlying target platform. An implementation may not implement all these routines, but if it provides this functionality, it should use these function names.

A.2.1 NVIDIA CUDA Platform

This section gives runtime API routines for implementations that target the NVIDIA CUDA Runtime or Driver API.

acc get current cuda device

Summary

The acc get current cuda_device routine returns the NVIDIA CUDA device handle for the current device.

Format

C or C++:

void* acc_get_current_cuda_device();

acc get current cuda context

Summary

The acc get current cuda context routine returns the NVIDIA CUDA context handle in use for the current device.

Format

C or C++:

void* acc_get_current_cuda_context();
acc_get_cudea_stream

Summary
The acc_get_cudea_stream routine returns the NVIDIA CUDA stream handle in use for the current device for the asynchronous activity queue associated with the async argument. This argument must be an async-argument as defined in Section 2.16.1 async clause.

Format
C or C++:
void* acc_get_cudea_stream ( int async );

acc_set_cudea_stream

Summary
The acc_set_cudea_stream routine sets the NVIDIA CUDA stream handle the current device for the asynchronous activity queue associated with the async argument. This argument must be an async-argument as defined in Section 2.16.1 async clause.

Format
C or C++:
void acc_set_cudea_stream ( int async, void* stream );

A.2.2 OpenCL Target Platform

This section gives runtime API routines for implementations that target the OpenCL API on any device.

acc_get_current_opencl_device

Summary
The acc_get_current_opencl_device routine returns the OpenCL device handle for the current device.

Format
C or C++:
void* acc_get_current_opencl_device ();

acc_get_current_opencl_context

Summary
The acc_get_current_opencl_context routine returns the OpenCL context handle in use for the current device.

Format
C or C++:
void* acc_get_current_opencl_context ();

acc_get_opencl_queue

Summary
The acc_get_opencl_queue routine returns the OpenCL command queue handle in use for the current device for the asynchronous activity queue associated with the async argument. This argument must be an async-argument as defined in Section 2.16.1 async clause.
The OpenACC\textsuperscript{®} API

A.3. Recommended Options

Format
C or C++:

\begin{verbatim}
cl_command_queue acc_get_opencl_queue ( int async );
\end{verbatim}

acc_set_opencl_queue

Summary
The \texttt{acc_set_opencl_queue} routine returns the OpenCL command queue handle in use for
the current device for the asynchronous activity queue associated with the \texttt{async} argument. This
argument must be an \texttt{async-argument} as defined in Section 2.16.1 \texttt{async clause}.

Format
C or C++:

\begin{verbatim}
void acc_set_opencl_queue ( int async, cl_command_queue cmdqueue );
\end{verbatim}

A.3  Recommended Options

The following options are recommended for implementations; for instance, these may be imple-
mented as command-line options to a compiler or settings in an IDE.

A.3.1  C Pointer in Present clause

This revision of OpenACC clarifies the construct:

\begin{verbatim}
void test(int n ){
  float* p;
  ...
  #pragma acc data present(p)
  {
    // code here...
  }
\end{verbatim}

This example tests whether the pointer \texttt{p} itself is present in the current device memory. Implement-
tations before this revision commonly implemented this by testing whether the pointer target \texttt{p[0]}
was present in the current device memory, and this appears in many programs assuming such. Until
such programs are modified to comply with this revision, an option to implement \texttt{present(p)} as
\texttt{present(p[0])} for C pointers may be helpful to users.

A.3.2  Automatic Data Attributes

If an implementation implements autoscopying or another analysis to automatically determine a vari-
able’s data attributes, an option to report which variables’ data attributes are not as defined in Section
2.6 would be helpful to users. An option to disable the analysis would be helpful to promote
program portability across implementations.
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