The OpenACC® Application Programming Interface

1

2

3

4

5

Version 3.0

OpenACC-Standard.org

November, 2019

- 6 Complying with all applicable copyright laws is the responsibility of the user. Without limiting the rights under copyright,
- 7 no part of this document may be reproduced, stored in, or introduced into a retrieval system, or transmitted in any form
- 8 or by any means (electronic, mechanical, photocopying, recording, or otherwise), or for any purpose, without the express
- 9 written permission of the authors.
- 10 © 2011-2019 OpenACC-Standard.org. All rights reserved.

Contents

12	1.	Intro	oduction	9
13		1.1.	Scope	9
14		1.2.	Execution Model	9
15		1.3.	Memory Model	11
16		1.4.	Language Interoperability	13
17		1.5.	Conventions used in this document	13
18		1.6.	Organization of this document	14
19		1.7.	References	14
20		1.8.	Changes from Version 1.0 to 2.0	16
21		1.9.	Corrections in the August 2013 document	17
22		1.10.	Changes from Version 2.0 to 2.5	17
23		1.11.	Changes from Version 2.5 to 2.6	18
24		1.12.	Changes from Version 2.6 to 2.7	19
25		1.13.	Changes from Version 2.7 to 3.0	20
26		1.14.	Topics Deferred For a Future Revision	21
27	2.	Dire	ctives	23
28		2.1.	Directive Format	23
29		2.2.	Conditional Compilation	24
30		2.3.	Internal Control Variables	24
31			2.3.1. Modifying and Retrieving ICV Values	24
32		2.4.	Device-Specific Clauses	25
33		2.5.	Compute Constructs	27
34			2.5.1. Parallel Construct	27
35			2.5.2. Kernels Construct	28
36			2.5.3. Serial Construct	30
37			2.5.4. if clause	32
38			2.5.5. self clause	32
39			2.5.6. async clause	32
40			2.5.7. wait clause	32
41			2.5.8. num_gangs clause	32
42			2.5.9. num_workers clause	32
43			2.5.10. vector_length clause	33
44			2.5.11. private clause	33
45			2.5.12. firstprivate clause	33
46			2.5.13. reduction clause	33
47			2.5.14. default clause	34
48		2.6.	Data Environment	35
49			2.6.1. Variables with Predetermined Data Attributes	35
50			2.6.2. Variables with Implicitly Determined Data Attributes	35

51		2.6.3.	Data Regions and Data Lifetimes	5
52		2.6.4.	Data Structures with Pointers	5
53		2.6.5.	Data Construct	7
54		2.6.6.	Enter Data and Exit Data Directives	8
55		2.6.7.	Reference Counters	0
56		2.6.8.	Attachment Counter	1
57	2.7.	Data Cl	lauses	1
58		2.7.1.	Data Specification in Data Clauses	2
59		2.7.2.	Data Clause Actions	3
60		2.7.3.	deviceptr clause	5
61		2.7.4.	present clause	5
62		2.7.5.	copy clause	7
63		2.7.6.	copyin clause	7
64		2.7.7.	copyout clause	8
65		2.7.8.	create clause	9
66		2.7.9.	no_create clause	9
67		2.7.10.	delete clause	0
68		2.7.11.	attach clause	0
69		2.7.12.	detach clause	1
70	2.8.	Host_D	ata Construct	1
71		2.8.1.	use_device clause	2
72		2.8.2.	if clause	2
73		2.8.3.	if_present clause	2
74	2.9.	Loop C	onstruct	2
75		2.9.1.	collapse clause	3
76		2.9.2.	gang clause	4
77		2.9.3.	worker clause	4
78		2.9.4.	vector clause	5
79		2.9.5.	seq clause	5
80		2.9.6.	auto clause	5
81		2.9.7.	tile clause	5
82		2.9.8.	device_type clause	5
83		2.9.9.	independent clause	5
84		2.9.10.	private clause	7
85		2.9.11.	reduction clause	7
86	2.10.	Cache I	Directive	1
87	2.11.	Combir	ned Constructs	2
88	2.12.	Atomic	Construct	3
89	2.13.	Declare	$e \text{ Directive } \dots $	7
90		2.13.1.	device_resident clause 69	9
91		2.13.2.	create clause	9
92		2.13.3.	link clause)
93	2.14.	Executa	able Directives	1
94		2.14.1.	Init Directive 7	1
95		2.14.2.	Shutdown Directive	2
96		2.14.3.	Set Directive	3
97		2.14.4.	Update Directive	4
98		2.14.5.	Wait Directive	7

99			2.14.6.	Enter Data Directive	7
100			2.14.7.	Exit Data Directive	1
101		2.15.	Procedu	ure Calls in Compute Regions	1
102			2.15.1.	Routine Directive	1
103			2.15.2.	Global Data Access)
104		2.16.	Asynch	ronous Behavior)
105			2.16.1.	async clause)
106			2.16.2.	wait clause	L
107			2.16.3.	Wait Directive8282	2
108		2.17.	Fortran	Optional Arguments	3
	2	Dun	tima Li	bronz Of	
109	J.	nu 1	Duntim	a Library Definitions) ;
110		3.1.	Duntim	e Library Doutines	, ;
111		5.2.		e Liorary Routines)
112			3.2.1.	acc_get_hum_devices) 7
113			3.2.2.	acc_set_device_type	7
114			3.2.3.	acc_get_device_type	,
115			5.2.4. 2.2.5))
116			5.2.5. 2.2.6))
117			3.2.0.	acc_get_property	,)
118			5.2.7. 2.2.9		,
119			5.2.0. 2.2.0		-
120			5.2.9. 2 2 10	acc_async_test	
121			5.2.10. 2 2 1 1		<u>.</u>
122			3.2.11.	acc_async_test_all	<u>,</u>
123			3.2.12.	acc_asylic_lest_all_device	, 2
124			3.2.13.	acc_wall	, 1
125			5.2.14. 2 2 15		} -
126			5.2.15. 2 2 16	acc_wall_async) -
127			5.2.10. 2 2 17	acc_wait_device_async)
128			3.2.17.)
129			5.2.18. 2.2.10)
130			3.2.19.	acc_wait_all_async) 7
131			5.2.20. 2.2.21		,
132			3.2.21.	acc_get_default_async	, ,
133			<i>3.2.22</i> .	acc_set_default_async))
134			3.2.23.) \
135			5.2.24. 2 2 2 5		, ,
136			3.2.23.		, \
137			<i>3.2.20</i> .)
138			5.2.27. 2.2.29		
139			3.2.28.	acc_copyour	2
140			<i>3.2.29</i> .	acc_ucicic $\dots \dots \dots$, 1
141			3.2.30.	acc_upuale_uevice	F
142			3.2.31.	acc_upuate_sell	,
143			<i>3.2.32</i> .	acc_map_data	, ;
144			5.2.55.	acc_unnap_uata) 5
145			5.2.34.		٦

146			3.2.35. acc_hostptr
147			3.2.36. acc_is_present
148			3.2.37. acc_memcpy_to_device
149			3.2.38. acc_memcpy_from_device
150			3.2.39. acc_memcpy_device
151			3.2.40. acc_attach
152			3.2.41. acc_detach
153			3.2.42. acc_memcpy_d2d
154	4.	Env	ironment Variables 113
155		4.1.	ACC_DEVICE_TYPE
156		4.2.	ACC_DEVICE_NUM
157		4.3.	ACC_PROFLIB
158	5.	Prof	filing Interface 11
159		5.1.	Events
160			5.1.1. Runtime Initialization and Shutdown
161			5.1.2. Device Initialization and Shutdown
162			5.1.3. Enter Data and Exit Data
163			5.1.4. Data Allocation
164			5.1.5. Data Construct
165			5.1.6. Update Directive
166			5.1.7. Compute Construct
167			5.1.8. Enqueue Kernel Launch
168			5.1.9. Enqueue Data Update (Upload and Download)
169			5.1.10. Wait
170		5.2.	Callbacks Signature
171			5.2.1. First Argument: General Information
172			5.2.2. Second Argument: Event-Specific Information
173			5.2.3. Third Argument: API-Specific Information
174		5.3.	Loading the Library
175			5.3.1. Library Registration
176			5.3.2. Statically-Linked Library Initialization
177			5.3.3. Runtime Dynamic Library Loading
178			5.3.4. Preloading with LD_PRELOAD
179			5.3.5. Application-Controlled Initialization
180		5.4.	Registering Event Callbacks
181			5.4.1. Event Registration and Unregistration
182			5.4.2. Disabling and Enabling Callbacks
183		5.5.	Advanced Topics
184			5.5.1. Dynamic Behavior
185			5.5.2. OpenACC Events During Event Processing
186			5.5.3. Multiple Host Threads

187 6. Glossary

137

188	A. Recommendations for Implementors	141
189	A.1. Target Devices	141
190	A.1.1. NVIDIA GPU Targets	141
191	A.1.2. AMD GPU Targets	141
192	A.1.3. Multicore Host CPU Target	142
193	A.2. API Routines for Target Platforms	142
194	A.2.1. NVIDIA CUDA Platform	143
195	A.2.2. OpenCL Target Platform	144
196	A.3. Recommended Options	145
197	A.3.1. C Pointer in Present clause	145
198	A.3.2. Autoscoping	145
199	Index	147

1. Introduction

This document describes the compiler directives, library routines, and environment variables that 201 collectively define the OpenACCTM Application Programming Interface (OpenACC API) for writ-202 ing parallel programs in C, C++, and Fortran that run identified regions in parallel on multicore 203 CPUs or attached accelerators. The method described provides a model for parallel programming 204 that is portable across operating systems and various types of multicore CPUs and accelerators. The 205 directives extend the ISO/ANSI standard C, C++, and Fortran base languages in a way that allows 206 a programmer to migrate applications incrementally to parallel multicore and accelerator targets 207 using standards-based C, C++, or Fortran. 208

The directives and programming model defined in this document allow programmers to create applications capable of using accelerators without the need to explicitly manage data or program transfers between a host and accelerator or to initiate accelerator startup and shutdown. Rather, these details are implicit in the programming model and are managed by the OpenACC API-enabled compilers and runtime environments. The programming model allows the programmer to augment information available to the compilers, including specification of data local to an accelerator, guidance on mapping of loops for parallel execution, and similar performance-related details.

²¹⁶ **1.1. Scope**

This OpenACC API document covers only user-directed parallel and accelerator programming, where the user specifies the regions of a program to be targeted for parallel execution. The remainder of the program will be executed sequentially on the host. This document does not describe features or limitations of the host programming environment as a whole; it is limited to specification of loops and regions of code to be executed in parallel on a multicore CPU or an accelerator.

This document does not describe automatic detection of parallel regions or automatic offloading of regions of code to an accelerator by a compiler or other tool. This document does not describe splitting loops or code regions across multiple accelerators attached to a single host. While future compilers may allow for automatic parallelization or automatic offloading, or parallelizing across multiple accelerators of the same type, or across multiple accelerators of different types, these possibilities are not addressed in this document.

1.2. Execution Model

The execution model targeted by OpenACC API-enabled implementations is host-directed execution with an attached parallel accelerator, such as a GPU, or a multicore host with a host thread that initiates parallel execution on the multiple cores, thus treating the multicore CPU itself as a device. Much of a user application executes on a host thread. Compute intensive regions are offloaded to an accelerator or executed on the multiple host cores under control of a host thread. A device, either

an attached accelerator or the multicore CPU, executes parallel regions, which typically contain 234 work-sharing loops, kernels regions, which typically contain one or more loops that may be exe-235 cuted as kernels, or serial regions, which are blocks of sequential code. Even in accelerator-targeted 236 regions, the host thread may orchestrate the execution by allocating memory on the accelerator de-237 vice, initiating data transfer, sending the code to the accelerator, passing arguments to the compute 238 region, queuing the accelerator code, waiting for completion, transferring results back to the host, 239 and deallocating memory. In most cases, the host can queue a sequence of operations to be executed 240 on a device, one after the other. 241

Most current accelerators and many multicore CPUs support two or three levels of parallelism. 242 Most accelerators and multicore CPUs support coarse-grain parallelism, which is fully parallel exe-243 cution across execution units. There may be limited support for synchronization across coarse-grain 244 parallel operations. Many accelerators and some CPUs also support fine-grain parallelism, often 245 implemented as multiple threads of execution within a single execution unit, which are typically 246 rapidly switched on the execution unit to tolerate long latency memory operations. Finally, most 247 accelerators and CPUs also support SIMD or vector operations within each execution unit. The 248 execution model exposes these multiple levels of parallelism on a device and the programmer is 249 required to understand the difference between, for example, a fully parallel loop and a loop that 250 is vectorizable but requires synchronization between statements. A fully parallel loop can be pro-251 grammed for coarse-grain parallel execution. Loops with dependences must either be split to allow 252 coarse-grain parallel execution, or be programmed to execute on a single execution unit using fine-253 grain parallelism, vector parallelism, or sequentially. 254

OpenACC exposes these three *levels of parallelism* via *gang, worker*, and *vector* parallelism. Gang parallelism is coarse-grain. A number of gangs will be launched on the accelerator. Worker parallelism is fine-grain. Each gang will have one or more workers. Vector parallelism is for SIMD or vector operations within a worker.

When executing a compute region on a device, one or more gangs are launched, each with one or more workers, where each worker may have vector execution capability with one or more vector lanes. The gangs start executing in *gang-redundant* mode (GR mode), meaning one vector lane of one worker in each gang executes the same code, redundantly. When the program reaches a loop or loop nest marked for gang-level work-sharing, the program starts to execute in *gang-partitioned* mode (GP mode), where the iterations of the loop or loops are partitioned across gangs for truly parallel execution, but still with only one worker per gang and one vector lane per worker active.

When only one worker is active, in either GR or GP mode, the program is in *worker-single* mode 266 (WS mode). When only one vector lane is active, the program is in *vector-single* mode (VS mode). 267 If a gang reaches a loop or loop nest marked for worker-level work-sharing, the gang transitions to 268 worker-partitioned mode (WP mode), which activates all the workers of the gang. The iterations 269 of the loop or loops are partitioned across the workers of this gang. If the same loop is marked for 270 both gang-partitioning and worker-partitioning, then the iterations of the loop are spread across all 271 the workers of all the gangs. If a worker reaches a loop or loop nest marked for vector-level work-272 sharing, the worker will transition to vector-partitioned mode (VP mode). Similar to WP mode, the 273 transition to VP mode activates all the vector lanes of the worker. The iterations of the loop or loops 274 275 will be partitioned across the vector lanes using vector or SIMD operations. Again, a single loop may be marked for one, two, or all three of gang, worker, and vector parallelism, and the iterations 276 of that loop will be spread across the gangs, workers, and vector lanes as appropriate. 277

²⁷⁸ The program starts executing with a single initial host thread, identified by a program counter and

its stack. The initial host thread may spawn additional host threads, using OpenACC or another
mechanism, such as with the OpenMP API. On a device, a single vector lane of a single worker of a
single gang is called a device thread. When executing on an accelerator, a parallel execution context
is created on the accelerator and may contain many such threads.

The user should not attempt to implement barrier synchronization, critical sections or locks across 283 any of gang, worker, or vector parallelism. The execution model allows for an implementation that 284 executes some gangs to completion before starting to execute other gangs. This means that trying 285 to implement synchronization between gangs is likely to fail. In particular, a barrier across gangs 286 cannot be implemented in a portable fashion, since all gangs may not ever be active at the same time. 287 Similarly, the execution model allows for an implementation that executes some workers within a 288 gang or vector lanes within a worker to completion before starting other workers or vector lanes, 289 or for some workers or vector lanes to be suspended until other workers or vector lanes complete. 290 This means that trying to implement synchronization across workers or vector lanes is likely to fail. 291 In particular, implementing a barrier or critical section across workers or vector lanes using atomic 292 operations and a busy-wait loop may never succeed, since the scheduler may suspend the worker or 293 vector lane that owns the lock, and the worker or vector lane waiting on the lock can never complete. 294 Some devices, such as a multicore CPU, may also create and launch additional compute regions, 295

allowing for nested parallelism. In that case, the OpenACC directives may be executed by a host thread or a device thread. This specification uses the term *local thread* or *local memory* to mean the thread that executes the directive, or the memory associated with that thread, whether that thread executes on the host or on the accelerator. The specification uses the term *local device* to mean the device on which the *local thread* is executing.

Most accelerators can operate asynchronously with respect to the host thread. Such devices have one 301 or more activity queues. The host thread will enqueue operations onto the device activity queues, 302 such as data transfers and procedure execution. After enqueuing the operation, the host thread can 303 continue execution while the device operates independently and asynchronously. The host thread 304 may query the device activity queue(s) and wait for all the operations in a queue to complete. 305 Operations on a single device activity queue will complete before starting the next operation on the 306 same queue; operations on different activity queues may be active simultaneously and may complete 307 in any order. 308

309 1.3. Memory Model

The most significant difference between a host-only program and a host-accelerator program is that 310 the memory on an accelerator may be discrete from host memory. This is the case with most current 311 GPUs, for example. In this case, the host thread may not be able to read or write device memory 312 directly because it is not mapped into the host thread's virtual memory space. All data movement 313 between host memory and accelerator memory must be performed by the host thread through system 314 calls that explicitly move data between the separate memories, typically using direct memory access 315 (DMA) transfers. Similarly, it is not valid to assume the accelerator can read or write host memory, 316 though this is supported by some accelerators, often with significant performance penalty. 317

The concept of discrete host and accelerator memories is very apparent in low-level accelerator programming languages such as CUDA or OpenCL, in which data movement between the memories can dominate user code. In the OpenACC model, data movement between the memories can be implicit and managed by the compiler, based on directives from the programmer. However, the programmer must be aware of the potentially discrete memories for many reasons, including but not limited to:

• Memory bandwidth between host memory and accelerator memory determines the level of compute intensity required to effectively accelerate a given region of code.

• The user should be aware that a discrete device memory is usually significantly smaller than the host memory, prohibiting offloading regions of code that operate on very large amounts of data.

Host addresses stored to pointers on the host may only be valid on the host; addresses stored to pointers in accelerator memory may only be valid on that device. Explicitly transferring pointer values between host and accelerator memory is not advised. Dereferencing host pointers on an accelerator or dereferencing accelerator pointers on the host is likely to be invalid on such targets.

OpenACC exposes the discrete memories through the use of a device data environment. Device data 334 has an explicit lifetime, from when it is allocated or created until it is deleted. If a device shares 335 memory with the local thread, its device data environment will be shared with the local thread. In 336 that case, the implementation need not create new copies of the data for the device and no data 337 movement need be done. If a device has a discrete memory and shares no memory with the local 338 thread, the implementation will allocate space in device memory and copy data between the local 339 memory and device memory, as appropriate. The local thread may share some memory with a 340 device and also have some memory that is not shared with that device. In that case, data in shared 341 memory may be accessed by both the local thread and the device. Data not in shared memory will 342 be copied to device memory as necessary. 343

Some accelerators (such as current GPUs) implement a weak memory model. In particular, they do not support memory coherence between operations executed by different threads; even on the same execution unit, memory coherence is only guaranteed when the memory operations are separated by an explicit memory fence. Otherwise, if one thread updates a memory location and another reads the same location, or two threads store a value to the same location, the hardware may not guarantee the same result for each execution. While a compiler can detect some potential errors of this nature, it is nonetheless possible to write a compute region that produces inconsistent numerical results.

Similarly, some accelerators implement a weak memory model for memory shared between the host and the accelerator, or memory shared between multiple accelerators. Programmers need to be very careful that the program uses appropriate synchronization to ensure that an assignment or modification by a thread on any device to data in shared memory is complete and available before that data is used by another thread on the same or another device.

Some current accelerators have a software-managed cache, some have hardware managed caches, and most have hardware caches that can be used only in certain situations and are limited to readonly data. In low-level programming models such as CUDA or OpenCL languages, it is up to the programmer to manage these caches. In the OpenACC model, these caches are managed by the compiler with hints from the programmer in the form of directives.

1.4. Language Interoperability

The specification supports programs written using OpenACC in two or more of Fortran, C, and C++ languages. The parts of the program in any one base language will interoperate with the parts written in the other base languages as described here. In particular:

• Data made present in one base language on a device will be seen as present by any base language.

• A region that starts and ends in a procedure written in one base language may directly or indirectly call procedures written in any base language. The execution of those procedures are part of the region.

1.5. Conventions used in this document

Some terms are used in this specification that conflict with their usage as defined in the base languages. When there is potential confusion, the term will appear in the Glossary.

³⁷³ Keywords and punctuation that are part of the actual specification will appear in typewriter font:

#pragma acc

³⁷⁴ Italic font is used where a keyword or other name must be used:

#pragma acc directive-name

³⁷⁵ For C and C++, *new-line* means the newline character at the end of a line:

#pragma acc directive-name new-line

Optional syntax is enclosed in square brackets; an option that may be repeated more than once is

377 followed by ellipses:

#pragma acc directive-name [clause [[,] clause]...] new-line

- ³⁷⁸ In this spec, a *var* (in italics) is one of the following:
- a variable name (a scalar, array, or composite variable name);
- a subarray specification with subscript ranges;
- an array element;
- a member of a composite variable;
- a common block name between slashes.

Not all options are allowed in all clauses; the allowable options are clarified for each use of the term *var*.

- ³⁸⁶ To simplify the specification and convey appropriate constraint information, a *pqr-list* is a comma-
- 387 separated list of *pqr* items. For example, an *int-expr-list* is a comma-separated list of one or more
- integer expressions, and a var-list is a comma-separated list of one or more vars. The one exception
- is *clause-list*, which is a list of one or more clauses optionally separated by commas.

#pragma acc directive-name [clause-list] new-line

1.6. Organization of this document

- ³⁹¹ The rest of this document is organized as follows:
- Chapter 2 Directives, describes the C, C++, and Fortran directives used to delineate accelerator regions and augment information available to the compiler for scheduling of loops and classification of data.
- ³⁹⁵ Chapter 3 Runtime Library, defines user-callable functions and library routines to query the accel-³⁹⁶ erator features and control behavior of accelerator-enabled programs at runtime.
- ³⁹⁷ Chapter 4 Environment Variables, defines user-settable environment variables used to control be-³⁹⁸ havior of accelerator-enabled programs at execution.
- Chapter 5 Profiling Interface, describes the OpenACC interface for tools that can be used for profile
 and trace data collection.
- ⁴⁰¹ Chapter 6 Glossary, defines common terms used in this document.
- 402 Appendix A Recommendations for Implementors, gives advice to implementers to support more
- ⁴⁰³ portability across implementations and interoperability with other accelerator APIs.

404 1.7. References

Each language version inherits the limitations that remain in previous versions of the language in this list.

- American National Standard Programming Language C, ANSI X3.159-1989 (ANSI C).
- ISO/IEC 9899:1999, Information Technology Programming Languages C, (C99).
- ISO/IEC 9899:2011, Information Technology Programming Languages C, (C11).
- The use of the following C11 features may result in unspecified behavior.
- 411 Threads
- 412 Thread-local storage
- 413 Parallel memory model
- 414 Atomic
- ISO/IEC 9899:2018, Information Technology Programming Languages C, (C18).
- The use of the following C18 features may result in unspecified behavior.
- 417 Thread related features
- ISO/IEC 14882:1998, Information Technology Programming Languages C++.
- ISO/IEC 14882:2011, Information Technology Programming Languages C++, (C++11).
- The use of the following C++11 features may result in unspecified behavior.

421	– Extern templates
422	 – copy and rethrow exceptions
423	 memory model
424	– atomics
425	– move semantics
426	 range based loops
427	– std::thread
428	 thread-local storage
429	• ISO/IEC 14882:2014, Information Technology – Programming Languages – C++, (C++14).
430	• ISO/IEC 14882:2017, Information Technology – Programming Languages – C++, (C++17).
431 432	• ISO/IEC 1539-1:2004, Information Technology – Programming Languages – Fortran – Part 1: Base Language, (Fortran 2003).
433 434	• ISO/IEC 1539-1:2010, Information Technology – Programming Languages – Fortran – Part 1: Base Language, (Fortran 2008).
435	The use of the following Fortran 2008 features may result in unspecified behavior.
436	– Coarrays
437	– Do concurrent
438	 Simply contiguous arrays rank remapping to rank>1 target
439	 Allocatable components of recursive type
440	– The block construct
441	 Polymorphic assignment
442 443	• ISO/IEC 1539-1:2018, Information Technology – Programming Languages – Fortran – Part 1: Base Language, (Fortran 2018).
444	The use of the following Fortran 2018 features may result in unspecified behavior.
445	– Interoperability with C
446	* C functions declared in ISO Fortran binding.h
447	* Assumed rank
448	 All additional parallel/coarray features
449	• OpenMP Application Program Interface, version 5.0, November 2018
449 450	 OpenMP Application Program Interface, version 5.0, November 2018 NVIDIA CUDATM C Programming Guide, version 10.1, May 2019

1.8.	Changes from V	ersion 1.0 to 2.0
	1.8.	1.8. Changes from V

453	• _OPENACC value updated to 201306
454	• default (none) clause on parallel and kernels directives
455	• the implicit data attribute for scalars in parallel constructs has changed
456 457	• the implicit data attribute for scalars in loops with loop directives with the independent attribute has been clarified
458	 acc_async_sync and acc_async_noval values for the async clause
459	• Clarified the behavior of the reduction clause on a gang loop
460 461	 Clarified allowable loop nesting (gang may not appear inside worker, which may not appear within vector)
462	• wait clause on parallel, kernels and update directives
463	• async clause on the wait directive
464	• enter data and exit data directives
465	• Fortran <i>common block</i> names may now appear in many data clauses
466	• link clause for the declare directive
467	• the behavior of the declare directive for global data
468	• the behavior of a data clause with a C or C++ pointer variable has been clarified
469	• predefined data attributes
470	• support for multidimensional dynamic C/C++ arrays
471	• tile and auto loop clauses
472	• update self introduced as a preferred synonym for update host
473	• routine directive and support for separate compilation
474	 device_type clause and support for multiple device types
475 476	 nested parallelism using parallel or kernels region containing another parallel or kernels re- gion
477	• atomic constructs
478 479	 new concepts: gang-redundant, gang-partitioned; worker-single, worker-partitioned; vector- single, vector-partitioned; thread
480	• new API routines:
481	– acc_wait, acc_wait_all instead of acc_async_wait and acc_async_wait_all
482	- acc_wait_async
483	- acc_copyin, acc_present_or_copyin
484	- acc_create, acc_present_or_create
485	- acc_copyout, acc_delete

- 486 acc_map_data, acc_unmap_data
- 487 acc_deviceptr, acc_hostptr
- 488 acc_is_present
- 489 acc_memcpy_to_device, acc_memcpy_from_device
- 490 acc_update_device, acc_update_self
- defined behavior with multiple host threads, such as with OpenMP
- recommendations for specific implementations
- clarified that no arguments are allowed on the **vector** clause in a parallel region

1.9. Corrections in the August 2013 document

- corrected the **atomic capture** syntax for C/C++
- fixed the name of the acc_wait and acc_wait_all procedures
- fixed description of the **acc_hostptr** procedure

1.10. Changes from Version 2.0 to 2.5

- The **_OPENACC** value was updated to **201510**; see Section 2.2 Conditional Compilation.
- The num_gangs, num_workers, and vector_length clauses are now allowed on the kernels construct; see Section 2.5.2 Kernels Construct.
- Reduction on C++ class members, array elements, and struct elements are explicitly disallowed; see Section 2.5.13 reduction clause.
- Reference counting is now used to manage the correspondence and lifetime of device data; see Section 2.6.7 Reference Counters.
- The behavior of the **exit data** directive has changed to decrement the dynamic reference counter. A new optional **finalize** clause was added to set the dynamic reference counter to zero. See Section 2.6.6 Enter Data and Exit Data Directives.
- The copy, copyin, copyout, and create data clauses were changed to behave like
 present_or_copy, etc. The present_or_copy, pcopy, present_or_copyin,
 pcopyin, present_or_copyout, pcopyout, present_or_create, and pcreate
 data clauses are no longer needed, though will be accepted for compatibility; see Section 2.7
 Data Clauses.
- Reductions on orphaned gang loops are explicitly disallowed; see Section 2.9 Loop Construct.
- The description of the **loop auto** clause has changed; see Section 2.9.6 auto clause.
- Text was added to the **private** clause on a **loop** construct to clarify that a copy is made for each gang or worker or vector lane, not each thread; see Section 2.9.10 private clause.
- The description of the **reduction** clause on a **loop** construct was corrected; see Section 2.9.11 reduction clause.

- A restriction was added to the **cache** clause that all references to that variable must lie within the region being cached; see Section 2.10 Cache Directive.
- Text was added to the **private** and **reduction** clauses on a combined construct to clarify that they act like **private** and **reduction** on the **loop**, not **private** and **reduction** on the **parallel** or **reduction** on the **kernels**; see Section 2.11 Combined Constructs.
- The **declare create** directive with a Fortran **allocatable** has new behavior; see Section 2.13.2 create clause.
- New init, shutdown, set directives were added; see Section 2.14.1 Init Directive, 2.14.2 Shutdown Directive, and 2.14.3 Set Directive.
- A new **if_present** clause was added to the **update** directive, which changes the behavior when data is not present from a runtime error to a no-op; see Section 2.14.4 Update Directive.
- The **routine bind** clause definition changed; see Section 2.15.1 Routine Directive.
- An acc routine without gang/worker/vector/seq is now defined as an error; see Section 2.15.1 Routine Directive.
- A new **default (present)** clause was added for compute constructs; see Section 2.5.14 default clause.
- The Fortran header file **openacc_lib**. **h** is no longer supported; the Fortran module **openacc** should be used instead; see Section 3.1 Runtime Library Definitions.
- New API routines were added to get and set the default async queue value; see Section 3.2.21 acc_get_default_async and 3.2.22 acc_set_default_async.
- The acc_copyin, acc_create, acc_copyout, and acc_delete API routines were
 changed to behave like acc_present_or_copyin, etc. The acc_present_or_names
 are no longer needed, though will be supported for compatibility. See Sections 3.2.26 and fol lowing.
- Asynchronous versions of the data API routines were added; see Sections 3.2.26 and following.
- A new API routine added, **acc_memcpy_device**, to copy from one device address to another device address; see Section 3.2.37 acc_memcpy_to_device.
- A new OpenACC interface for profile and trace tools was added; see Chapter 5 Profiling Interface.

549 1.11. Changes from Version 2.5 to 2.6

- The **_OPENACC** value was updated to **201711**.
- A new **serial** compute construct was added. See Section 2.5.3 Serial Construct.
- A new runtime API query routine was added. **acc_get_property** may be called from the host and returns properties about any device. See Section 3.2.6.
- The text has clarified that if a variable is in a reduction which spans two or more nested loops, each **loop** directive on any of those loops must have a **reduction** clause that contains the variable; see Section 2.9.11 reduction clause.

uct.

571

• An optional if or if_present clause is now allowed on the host_data construct. See 557 Section 2.8 Host_Data Construct. 558 • A new no_create data clause is now allowed on compute and data constructs. See Sec-559 tion 2.7.9 no_create clause. 560 • The behavior of Fortran optional arguments in data clauses and in routine calls has been 561 specified; see Section 2.17 Fortran Optional Arguments. 562 • The descriptions of some of the Fortran versions of the runtime library routines were simpli-563 fied; see Section 3.2 Runtime Library Routines. 564 • To allow for manual deep copy of data structures with pointers, new *attach* and *detach* be-565 havior was added to the data clauses, new attach and detach clauses were added, and 566 matching acc_attach and acc_detach runtime API routines were added; see Sections 567 2.6.4, 2.7.11-2.7.12 and 3.2.40-3.2.41. 568 The Intel Coprocessor Offload Interface target and API routine sections were removed from 569 the Section A Recommendations for Implementors, since Intel no longer produces this prod-570

572 1.12. Changes from Version 2.6 to 2.7

- The _OPENACC value was updated to 201811.
- The specification allows for hosts that share some memory with the device but not all memory.
 The wording in the text now discusses whether local thread data is in shared memory (memory shared between the local thread and the device) or discrete memory (local thread memory that is not shared with the device), instead of shared-memory devices and non-shared memory devices. See Sections 1.3 Memory Model and 2.6 Data Environment.
- The text was clarified to allow an implementation that treats a multicore CPU as a device, either an additional device or the only device.
- The **readonly** modifier was added to the **copyin** data clause and **cache** directive. See Sections 2.7.6 and 2.10.
- The term *local device* was defined; see Section 1.2 Execution Model and the Glossary.
- The term *var* is used more consistently throughout the specification to mean a variable name, array name, subarray specification, array element, composite variable member, or Fortran common block name between slashes. Some uses of *var* allow only a subset of these options, and those limitations are given in those cases.
- The **self** clause was added to the compute constructs; see Section 2.5.5 self clause.
- The appearance of a **reduction** clause on a compute construct implies a **copy** clause for each reduction variable; see Sections 2.5.13 reduction clause and 2.11 Combined Constructs.
- The **default (none)** and **default (present)** clauses were added to the **data** construct; see Section 2.6.5 Data Construct.
- Data is defined to be *present* based on the values of the structured and dynamic reference counters; see Section 2.6.7 Reference Counters and the Glossary.

- The interaction of the **acc_map_data** and **acc_unmap_data** runtime API calls on the present counters is defined; see Section 2.7.2, 3.2.32, and 3.2.33.
- A restriction clarifying that a **host_data** construct must have at least one **use_device** clause was added.
- Arrays, subarrays and composite variables are now allowed in **reduction** clauses; see Sections 2.9.11 reduction clause and 2.5.13 reduction clause.
- Changed behavior of ICVs to support nested compute regions and host as a device semantics. See Section 2.3.

1.13. Changes from Version 2.7 to 3.0

- Updated **_OPENACC** value to **201911**.
- Updated the normative references to the most recent standards for all base langauges. See Section 1.7.
- Changed the text to clarify uses and limitations of the **device_type** clause and added examples; see Section 2.4.
- Clarified the conflict between the implicit **copy** clause for variables in a **reduction** clause and the implicit **firstprivate** for scalar variables not in a data clause but used in a **parallel** or **serial** construct; see Sections 2.5.1 and 2.5.3.
- Required at least one data clause on a **data** construct, an **enter data** directive, or an **exit** data directive; see Sections 2.6.5 and 2.6.6.
- Added text describing how a C++ *lambda* invoked in a compute region and the variables captured by the *lambda* are handled; see Section 2.6.2.
- Added a **zero** modifier to **create** and **copyout** data clauses that zeros the device memory after it is allocated; see Sections 2.7.7 and 2.7.8.
- Added a new restriction on the loop directive allowing only one of the seq, independent, and auto clauses to appear; see Section 2.9.
- Added a new restriction on the **loop** directive disallowing a **gang**, **worker**, or **vector** clause to appear if a **seq** clause appears; see Section 2.9.
- Allowed variables to be modified in an atomic region in a loop where the iterations must otherwise be data independent, such as loops with a **loop independent** clause or a **loop** directive in a **parallel** construct; see Sections 2.9.2, 2.9.3, 2.9.4, and 2.9.9.
- Clarified the behavior of the **auto** and **independent** clauses on the **loop** directive; see Sections 2.9.6 and 2.9.9.
- Clarified that an orphaned loop construct, or a loop construct in a **parallel** construct with no **auto** or **seq** clauses is treated as if an **independent** clause appears; see Section 2.9.9.
- For a variable in a **reduction** clause, clarified when the update to the original variable is complete, and added examples; see Section 2.9.11.
- Clarified that a variable in an orphaned **reduction** clause must be private; see Section 2.9.11.

- Required at least one clause on a **declare** directive; see Section 2.13.
- Added an **if** clause to **init**, **shutdown**, **set**, and **wait** directives; see Sections 2.14.1, 2.14.2, 2.14.3, and 2.16.3.
- Required at least one clause on a **set** directive; see Section 2.14.3.
- Added a *devnum* modifier to the **wait** directive and clause to specify a device to which the wait operation applies; see Section 2.16.3.
- Allowed a **routine** directive to include a C++ *lambda* name or to appear before a C++ *lambda* definition, and defined implicit **routine** directive behavior when a C++ *lambda* is called in a compute region or an *accelerator routine*; see Section 2.15.
- Added runtime API routine **acc_memcpy_d2d** for copying data directly between two device arrays on the same or different devices; see Section 3.2.42.
- Defined the values for the **acc_construct_t** and **acc_device_api** enumerations for cross-implementation compatibility; see Sections 5.2.2 and 5.2.3.
- Changed the return type of acc_set_cuda_stream from int (values were not specified)
 to void; see Section A.2.1.
- Edited and expanded Section 1.14 Topics Deferred For a Future Revision.

1.14. Topics Deferred For a Future Revision

The following topics are under discussion for a future revision. Some of these are known to be important, while others will depend on feedback from users. Readers who have feedback or want to participate may post a message at the forum at www.openacc.org, or may send email to technical@openacc.org or feedback@openacc.org. No promises are made or implied that all these items will be available in the next revision.

- Directives to define implicit *deep copy* behavior for pointer-based data structures.
- Defined behavior when data in data clauses on a directive are aliases of each other.
- Clarifying when data becomes *present* or *not present* on the device for **enter data** or **exit** data directives with an **async** clause.
- Clarifying the behavior of Fortran **pointer** variables in data clauses.
- Allowing Fortran **pointer** variables to appear in **deviceptr** clauses.
- Defining the behavior of data clauses and runtime API routines for pointers that are **NULL**, or Fortran **pointer** variables that are not associated, or Fortran **allocatable** variables that are not allocated.
- Support for attaching C/C++ pointers that point to an address past the end of a memory region.
- Fully defined interaction with multiple host threads.
- Optionally removing the synchronization or barrier at the end of vector and worker loops.
- Allowing an **if** clause after a **device_type** clause.
- A **shared** clause (or something similar) for the loop directive.

- Better support for multiple devices from a single thread, whether of the same type or of different types.
- An *auto* construct (by some name), to allow **kernels**-like auto-parallelization behavior inside **parallel** constructs or accelerator routines.
- A **begin declare** ... **end declare** construct that behaves like putting any global variables declared inside the construct in a **declare** clause.
- Defining the behavior of parallelism constructs in the base languages when used inside a compute construct or accelerator routine.
- Optimization directives or clauses, such as an *unroll* directive or clause.
- Define runtime error behavior and allowing a user-defined error handlers.
- Extended reductions.
- Fortran bindings for all the API routines.
- A **linear** clause for the **loop** directive.
- Allowing two or more of gang, worker, vector, or seq clause on an acc routine directive.
- Requiring the implementation to imply an **acc routine** directive for procedures called within a compute construct or accelerator routine.
- A single list of all devices of all types, including the host device.
- A memory allocation API for specific types of memory, including device memory, host pinned memory, and unified memory.
- A restricted, acceptable form of a loop in a **loop** construct.
- Bindings to other languages.

Directives

This chapter describes the syntax and behavior of the OpenACC directives. In C and C++, Open-ACC directives are specified using the **#pragma** mechanism provided by the language. In Fortran,

⁶⁹⁴ OpenACC directives are specified using special comments that are identified by a unique sentinel.

⁶⁹⁵ Compilers will typically ignore OpenACC directives if support is disabled or not provided.

696 2.1. Directive Format

In C and C++, OpenACC directives are specified with the **#pragma** mechanism. The syntax of an OpenACC directive is:

#pragma acc directive-name [clause-list] new-line

Each directive starts with **#pragma acc**. The remainder of the directive follows the C and C++ conventions for pragmas. White space may be used before and after the **#**; white space may be required to separate words in a directive. Preprocessing tokens following the **#pragma acc** are subject to macro replacement. Directives are case-sensitive.

⁷⁰³ In Fortran, OpenACC directives are specified in free-form source files as

!\$acc *directive-name* [*clause-list*]

The comment prefix (!) may appear in any column, but may only be preceded by white space 704 (spaces and tabs). The sentinel (**!\$acc**) must appear as a single word, with no intervening white 705 space. Line length, white space, and continuation rules apply to the directive line. Initial directive 706 lines must have white space after the sentinel. Continued directive lines must have an ampersand (&) 707 as the last nonblank character on the line, prior to any comment placed in the directive. Continuation 708 directive lines must begin with the sentinel (possibly preceded by white space) and may have an 709 ampersand as the first non-white space character after the sentinel. Comments may appear on the 710 same line as a directive, starting with an exclamation point and extending to the end of the line. If 711 the first nonblank character after the sentinel is an exclamation point, the line is ignored. 712

⁷¹³ In Fortran fixed-form source files, OpenACC directives are specified as one of

!\$acc directive-name [clause-list]
c\$acc directive-name [clause-list]
*\$acc directive-name [clause-list]

The sentinel (**!\$acc**, **c\$acc**, or ***\$acc**) must occupy columns 1-5. Fixed form line length, white space, continuation, and column rules apply to the directive line. Initial directive lines must have a space or zero in column 6, and continuation directive lines must have a character other than a
space or zero in column 6. Comments may appear on the same line as a directive, starting with an
exclamation point on or after column 7 and continuing to the end of the line.

In Fortran, directives are case-insensitive. Directives cannot be embedded within continued statements, and statements must not be embedded within continued directives. In this document, free form is used for all Fortran OpenACC directive examples.

722 Only one *directive-name* can appear per directive, except that a combined directive name is consid-

real a single *directive-name*. The order in which clauses appear is not significant unless otherwise specified. Clauses may be repeated unless otherwise specified. Some clauses have an argument that

725 can contain a list.

726 2.2. Conditional Compilation

The **_OPENACC** macro name is defined to have a value *yyyymm* where *yyyy* is the year and *mm* is

⁷²⁸ the month designation of the version of the OpenACC directives supported by the implementation.

This macro must be defined by a compiler only when OpenACC directives are enabled. The version

730 described here is 201911.

731 2.3. Internal Control Variables

An OpenACC implementation acts as if there are internal control variables (ICVs) that control the behavior of the program. These ICVs are initialized by the implementation, and may be given values through environment variables and through calls to OpenACC API routines. The program can retrieve values through calls to OpenACC API routines.

736 The ICVs are:

- *acc-current-device-type-var* controls which type of device is used.
- *acc-current-device-num-var* controls which device of the selected type is used.
- *acc-default-async-var* controls which asynchronous queue is used when none appears in an async clause.

741 2.3.1. Modifying and Retrieving ICV Values

The following table shows environment variables or procedures to modify the values of the internal
 control variables, and procedures to retrieve the values:

ICV	Ways to modify values	Way to retrieve value
acc-current-device-type-var	<pre>acc_set_device_type</pre>	acc_get_device_type
	set device_type	
	ACC_DEVICE_TYPE	
acc-current-device-num-var	acc_set_device_num	<pre>acc_get_device_num</pre>
	set device_num	
	ACC_DEVICE_NUM	
acc-default-async-var	acc_set_default_async	acc_get_default_async
	<pre>set default_async</pre>	
	ICV acc-current-device-type-var acc-current-device-num-var acc-default-async-var	ICVWays to modify valuesacc-current-device-type-varacc_set_device_typeset device_typeACC_DEVICE_TYPEacc-current-device-num-varacc_set_device_numset device_numACC_DEVICE_NUMacc-default-async-varacc_set_default_asyncset default_asyncset default_async

The initial values are implementation-defined. After initial values are assigned, but before any OpenACC construct or API routine is executed, the values of any environment variables that were set by the user are read and the associated ICVs are modified accordingly. There is one copy of each ICV for each host thread that is not generated by a compute construct. For threads that are generated by a compute construct the initial value for each ICV is inherited from the local thread. The behavior for each ICV is as if there is a copy for each thread. If an ICV is modified, then a unique copy of that ICV must be created for the modifying thread.

752 2.4. Device-Specific Clauses

753 OpenACC directives can specify different clauses or clause arguments for different devices using

⁷⁵⁴ the **device_type** clause. Clauses that precede any **device_type** clause are *default clauses*.

⁷⁵⁵ Clauses that follow a **device_type** clause up to the end of the directive or up to the next

756 **device_type** clause are *device-specific clauses* for the device types specified in the **device_type**

argument. For each directive, only certain clauses may be device-specific clauses. If a directive has

at least one device-specific clause, it is *device-dependent*, and otherwise it is *device-independent*.

The argument to the **device_type** clause is a comma-separated list of one or more device ar-

⁷⁶⁰ chitecture name identifiers, or an asterisk. An asterisk indicates all device types that are not named

⁷⁶¹ in any other **device_type** clause on that directive. A single directive may have one or several

762 device_type clauses. The device_type clauses may appear in any order.

Except where otherwise noted, the rest of this document describes device-independent directives, on

which all clauses apply when compiling for any device type. When compiling a device-dependent

⁷⁶⁵ directive for a particular device type, the directive is treated as if the only clauses that appear are (a)

the clauses specific to that device type and (b) all default clauses for which there are no like-named clauses specific to that device type. If, for any device type, the resulting directive is non-conforming,

then the original directive is non-conforming.

The supported device types are implementation-defined. Depending on the implementation and the compiling environment, an implementation may support only a single device type, or may support

multiple device types but only one at a time, or may support multiple device types in a single

772 compilation.

A device architecture name may be generic, such as a vendor, or more specific, such as a partic-

⁷⁷⁴ ular generation of device; see Appendix A Recommendations for Implementors for recommended

names. When compiling for a particular device, the implementation will use the clauses associated

with the **device_type** clause that specifies the most specific architecture name that applies for

this device; clauses associated with any other **device_type** clause are ignored. In this context,

- ⁷⁷⁸ the asterisk is the least specific architecture name.
- 779 **Syntax** The syntax of the **device_type** clause is

```
device_type( * )
device_type( device-type-list )
```

⁷⁸⁰ The **device_type** clause may be abbreviated to **dtype**.

```
781
    Examples
782
       • On the following directive, worker appears as a device-specific clause for devices of type
783
         foo, but gang appears as a default clause and so applies to all device types, including foo.
784
              #pragma acc loop gang device_type(foo) worker
785
       • The first directive below is identical to the previous directive except that loop is replaced
786
         with routine. Unlike loop, routine does not permit gang to appear with worker,
787
         but both apply for device type foo, so the directive is non-conforming. The second directive
788
         below is conforming because gang there applies to all device types except foo.
789
              // non-conforming: gang and worker are not permitted together
790
              #pragma acc routine gang device_type(foo) worker
791
792
              // conforming: gang and worker apply to different device types
793
              #pragma acc routine device_type(foo) worker \
794
                                         device_type(*)
                                                                 gang
795
       • On the directive below, the value of num_gangs is 4 for device type foo, but it is 2 for all
796
         other device types, including bar. That is, foo has a device-specific num_gangs clause,
797
         so the default num_gangs clause does not apply to foo.
798
               !$acc parallel
                                                          num_gangs(2)
                                                                             &
799
               !$acc
                                   device_type(foo) num_gangs(4)
                                                                             &
800
               !$acc
                                   device_type(bar) num_workers(8)
801
       • The directive below is the same as the previous directive except that num_gangs (2) has
802
         moved after device_type (*) and so now does not apply to foo or bar.
803
               !$acc parallel device_type(*)
                                                          num_gangs(2)
                                                                             &
804
               !$acc
                                   device_type(foo) num_gangs(4)
                                                                             &
805
               !$acc
                                   device_type(bar) num_workers(8)
806
807
808
```

2.5. Compute Constructs

810 2.5.1. Parallel Construct

811 **Summary** This fundamental construct starts parallel execution on the current device.

812 Syntax In C and C++, the syntax of the OpenACC parallel construct is

#pragma acc parallel [clause-list] new-line structured block

and in Fortran, the syntax is

!\$acc parallel [clause-list]
 structured block
!\$acc end parallel

814 where *clause* is one of the following:

```
async [( int-expr )]
wait [( int-expr-list )]
num_gangs ( int-expr )
num_workers( int-expr )
vector_length( int-expr )
device_type ( device-type-list )
if ( condition )
self [( condition )]
reduction( operator:var-list )
copy (var-list)
copyin( [readonly:]var-list )
copyout ( [zero:]var-list )
create( [zero:]var-list )
no_create( var-list )
present( var-list )
deviceptr( var-list )
attach( var-list )
private( var-list )
firstprivate( var-list )
default ( none | present )
```

Description When the program encounters an accelerator **parallel** construct, one or more gangs of workers are created to execute the accelerator parallel region. The number of gangs, and the number of workers in each gang and the number of vector lanes per worker remain constant for the duration of that parallel region. Each gang begins executing the code in the structured block in gang-redundant mode. This means that code within the parallel region, but outside of a loop construct with gang-level worksharing, will be executed redundantly by all gangs. 821 One worker in each gang begins executing the code in the structured block of the construct. Note:

⁸²² Unless there is a **loop** construct within the parallel region, all gangs will execute all the code within

823 the region redundantly.

If the **async** clause does not appear, there is an implicit barrier at the end of the accelerator parallel region, and the execution of the local thread will not proceed until all gangs have reached the end of the parallel region.

If there is no **default (none)** clause on the construct, the compiler will implicitly determine data 827 attributes for variables that are referenced in the compute construct that do not have predetermined 828 data attributes and do not appear in a data clause on the compute construct, a lexically containing 829 data construct, or a visible declare directive. If there is no default (present) clause 830 on the construct, an array or composite variable referenced in the **parallel** construct that does 831 not appear in a data clause for the construct or any enclosing data construct will be treated as if 832 it appeared in a copy clause for the parallel construct. If there is a default (present) 833 clause on the construct, the compiler will implicitly treat all arrays and composite variables without 834 predetermined data attributes as if they appeared in a present clause. A scalar variable referenced 835 in the **parallel** construct that does not appear in a data clause for the construct or any enclosing 836 data construct will be treated as if it appeared in a firstprivate clause unless a reduction 837 would otherwise imply a **copy** clause for it. 838

839 **Restrictions**

- A program may not branch into or out of an OpenACC **parallel** construct.
- A program must not depend on the order of evaluation of the clauses, or on any side effects of the evaluations.
- Only the async, wait, num_gangs, num_workers, and vector_length clauses may follow a device_type clause.
- At most one **if** clause may appear. In Fortran, the condition must evaluate to a scalar logical value; in C or C++, the condition must evaluate to a scalar integer value.
- At most one **default** clause may appear, and it must have a value of either **none** or **present**.

The copy, copyin, copyout, create, no_create, present, deviceptr, and attach data clauses are described in Section 2.7 Data Clauses. The private and firstprivate clauses are described in Sections 2.5.11 and Sections 2.5.12. The device_type clause is described in Section 2.4 Device-Specific Clauses.

853 2.5.2. Kernels Construct

Summary This construct defines a region of the program that is to be compiled into a sequence of kernels for execution on the current device.

Syntax In C and C++, the syntax of the OpenACC kernels construct is

#pragma acc kernels [clause-list] new-line structured block and in Fortran, the syntax is

```
!$acc kernels [clause-list]
    structured block
!$acc end kernels
```

⁸⁵⁸ where *clause* is one of the following:

```
async[( int-expr )]
wait [ ( int-expr-list )]
num_gangs ( int-expr )
num workers ( int-expr )
vector_length( int-expr )
device_type( device-type-list )
if ( condition )
self [( condition )]
copy ( var-list )
copyin( [readonly:]var-list )
copyout ( [zero:]
                      var-list )
create( [zero:]
                     var-list )
no_create( var-list )
present ( var-list )
deviceptr(var-list)
attach( var-list )
default ( none | present )
```

Description The compiler will split the code in the kernels region into a sequence of accelerator kernels. Typically, each loop nest will be a distinct kernel. When the program encounters a **kernels** construct, it will launch the sequence of kernels in order on the device. The number and configuration of gangs of workers and vector length may be different for each kernel.

⁸⁶³ If the **async** clause does not appear, there is an implicit barrier at the end of the kernels region, and ⁸⁶⁴ the local thread execution will not proceed until all kernels have completed execution.

If there is no **default (none)** clause on the construct, the compiler will implicitly determine data 865 attributes for variables that are referenced in the compute construct that do not have predetermined 866 data attributes and do not appear in a data clause on the compute construct, a lexically containing 867 data construct, or a visible declare directive. If there is no default (present) clause 868 on the construct, an array or composite variable referenced in the **kernels** construct that does 869 not appear in a data clause for the construct or any enclosing **data** construct will be treated as 870 if it appeared in a copy clause for the kernels construct. If there is a default (present) 871 clause on the construct, the compiler will implicitly treat all arrays and composite variables without 872 predetermined data attributes as if they appeared in a present clause. A scalar variable referenced 873 in the **kernels** construct that does not appear in a data clause for the construct or any enclosing 874 data construct will be treated as if it appeared in a copy clause. 875

876 **Restrictions**

- A program may not branch into or out of an OpenACC kernels construct.
- A program must not depend on the order of evaluation of the clauses, or on any side effects of the evaluations.
- Only the async, wait, num_gangs, num_workers, and vector_length clauses may follow a device_type clause.
- At most one **if** clause may appear. In Fortran, the condition must evaluate to a scalar logical value; in C or C++, the condition must evaluate to a scalar integer value.
- At most one **default** clause may appear, and it must have a value of either **none** or **present**.

The copy, copyin, copyout, create, no_create, present, deviceptr, and attach data clauses are described in Section 2.7 Data Clauses. The device_type clause is described in Section 2.4 Device-Specific Clauses.

889 2.5.3. Serial Construct

Summary This construct defines a region of the program that is to be executed sequentially on the current device.

892 **Syntax** In C and C++, the syntax of the OpenACC serial construct is

#pragma acc serial [clause-list] new-line structured block

and in Fortran, the syntax is

!\$acc serial [clause-list]
 structured block
!\$acc end serial

⁸⁹⁴ where *clause* is one of the following:

```
async [( int-expr )]
wait [( int-expr-list )]
device_type( device-type-list )
if( condition )
self [( condition )]
reduction( operator:var-list )
copy( var-list )
copyin( [readonly:]var-list )
copyout( [zero:] var-list )
create( [zero:] var-list )
no_create( var-list )
```

```
present( var-list )
deviceptr( var-list )
private( var-list )
firstprivate( var-list )
attach( var-list )
default( none | present )
```

Description When the program encounters an accelerator **serial** construct, one gang of one worker with a vector length of one is created to execute the accelerator serial region sequentially. The single gang begins executing the code in the structured block in gang-redundant mode, even though there is a single gang. The **serial** construct executes as if it were a **parallel** construct with clauses **num_gangs(1) num_workers(1) vector_length(1)**.

⁹⁰⁰ If the **async** clause does not appear, there is an implicit barrier at the end of the accelerator serial ⁹⁰¹ region, and the execution of the local thread will not proceed until the gang has reached the end of ⁹⁰² the serial region.

If there is no **default (none)** clause on the construct, the compiler will implicitly determine data 903 attributes for variables that are referenced in the compute construct that do not have predetermined 904 data attributes and do not appear in a data clause on the compute construct, a lexically containing 905 data construct, or a visible declare directive. If there is no default (present) clause 906 on the construct, an array or composite variable referenced in the **serial** construct that does 907 not appear in a data clause for the construct or any enclosing **data** construct will be treated as 908 if it appeared in a copy clause for the serial construct. If there is a default (present) 909 clause on the construct, the compiler will implicitly treat all arrays and composite variables without 910 predetermined data attributes as if they appeared in a present clause. A scalar variable referenced 911 in the **serial** construct that does not appear in a data clause for the construct or any enclosing 912 data construct will be treated as if it appeared in a firstprivate clause unless a reduction 913 would otherwise imply a **copy** clause for it. 914

915 **Restrictions**

- A program may not branch into or out of an OpenACC **serial** construct.
- A program must not depend on the order of evaluation of the clauses, or on any side effects of the evaluations.
- Only the **async** and **wait** clauses may follow a **device_type** clause.
- At most one **if** clause may appear. In Fortran, the condition must evaluate to a scalar logical value; in C or C++, the condition must evaluate to a scalar integer value.
- At most one **default** clause may appear, and it must have a value of either **none** or **present**.

The copy, copyin, copyout, create, no_create, present, deviceptr, and attach data clauses are described in Section 2.7 Data Clauses. The private and firstprivate clauses are described in Sections 2.5.11 and Sections 2.5.12. The device_type clause is described in Section 2.4 Device-Specific Clauses.

928 2.5.4. if clause

929 The **if** clause is optional.

When the *condition* in the **if** clause evaluates to nonzero in C or C++, or **.true**. in Fortran, the region will execute on the current device. When the *condition* in the **if** clause evaluates to zero in C or C++, or **.false**. in Fortran, the local thread will execute the region.

933 **2.5.5. self clause**

⁹³⁴ The **self** clause is optional.

The **self** clause may have a single *condition-argument*. If the *condition-argument* is not present it is assumed to be nonzero in C or C++, or .true. in Fortran. When both an if clause and a **self** clause appear and the *condition* in the if clause evaluates to 0 in C or C++ or .false. in Fortran, the **self** clause has no effect.

When the *condition* evaluates to nonzero in C or C++, or .true. in Fortran, the region will execute on the local device. When the *condition* in the **self** clause evaluates to zero in C or C++, or .false. in Fortran, the region will execute on the current device.

942 2.5.6. async clause

⁹⁴³ The **async** clause is optional; see Section 2.16 Asynchronous Behavior for more information.

944 **2.5.7. wait clause**

⁹⁴⁵ The wait clause is optional; see Section 2.16 Asynchronous Behavior for more information.

946 2.5.8. num_gangs clause

The **num_gangs** clause is allowed on the **parallel** and **kernels** constructs. The value of the integer expression defines the number of parallel gangs that will execute the parallel region, or that will execute each kernel created for the kernels region. If the clause does not appear, an implementation-defined default will be used; the default may depend on the code within the construct. The implementation may use a lower value than specified based on limitations imposed by the target architecture.

953 2.5.9. num_workers clause

The num_workers clause is allowed on the parallel and kernels constructs. The value of the integer expression defines the number of workers within each gang that will be active after a gang transitions from worker-single mode to worker-partitioned mode. If the clause does not appear, an implementation-defined default will be used; the default value may be 1, and may be different for each parallel construct or for each kernel created for a kernels construct. The implementation may use a different value than specified based on limitations imposed by the target architecture.

961 2.5.10. vector_length clause

The **vector_length** clause is allowed on the **parallel** and **kernels** constructs. The value of the integer expression defines the number of vector lanes that will be active after a worker transitions from vector-single mode to vector-partitioned mode. This clause determines the vector length to use for vector or SIMD operations. If the clause does not appear, an implementation-defined default will be used. This vector length will be used for loop constructs annotated with the **vector** clause, as well as loops automatically vectorized by the compiler. The implementation may use a different value than specified based on limitations imposed by the target architecture.

969 2.5.11. private clause

The **private** clause is allowed on the **parallel** and **serial** constructs; it declares that a copy of each item on the list will be created for each gang.

972 **Restrictions**

• See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in private clauses.

975 2.5.12. firstprivate clause

The **firstprivate** clause is allowed on the **parallel** and **serial** constructs; it declares that a copy of each item on the list will be created for each gang, and that the copy will be initialized with the value of that item on the local thread when a **parallel** or **serial** construct is encountered.

979 **Restrictions**

See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in
 firstprivate clauses.

982 2.5.13. reduction clause

The reduction clause is allowed on the parallel and serial constructs. It specifies a 983 reduction operator and one or more vars. It implies a **copy** data clause for each reduction var, 984 unless a data clause for that variable appears on the compute construct. For each reduction *var*, a 985 private copy is created for each parallel gang and initialized for that operator. At the end of the 986 region, the values for each gang are combined using the reduction operator, and the result combined 987 with the value of the original var and stored in the original var. If the reduction var is an array or 988 subarray, the array reduction operation is logically equivalent to applying that reduction operation 989 to each element of the array or subarray individually. If the reduction var is a composite variable, 990 the reduction operation is logically equivalent to applying that reduction operation to each member 991 of the composite variable individually. The reduction result is available after the region. 992

The following table lists the operators that are valid and the initialization values; in each case, the initialization value will be cast into the data type of the *var*. For **max** and **min** reductions, the

initialization values are the least representable value and the largest representable value for that data 995 type, respectively. At a minimum, the supported data types include Fortran logical as well as 996 the numerical data types in C (e.g., _Bool, char, int, float, double, float _Complex, 997 double _Complex), C++ (e.g., bool, char, wchar_t, int, float, double), and Fortran 998 (e.g., integer, real, double precision, complex). However, for each reduction operator, 999 the supported data types include only the types permitted as operands to the corresponding operator 1000 in the base language where (1) for max and min, the corresponding operator is less-than and (2) for 1001 other operators, the operands and the result are the same type. 1002

C a	and C++	Fortran	
operator	initialization	operator	initialization
	value		value
+	0	+	0
*	1	*	1
max	least	max	least
min	largest	min	largest
æ	~0	iand	all bits on
I	0	ior	0
^	0	ieor	0
& &	1	.and.	.true.
11	0	.or.	.false.
		.eqv.	.true.
		.neqv.	.false.

1003

1004 **Restrictions**

• A *var* in a **reduction** clause must be a scalar variable name, a composite variable name, an array name, an array element, or a subarray (refer to Section 2.7.1).

• If the reduction *var* is an array element or a subarray, accessing the elements of the array outside the specified index range results in unspecified behavior.

- If the reduction *var* is a composite variable, each member of the composite variable must be a supported datatype for the reduction operation.
- See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in
 reduction clauses.

1014 2.5.14. default clause

The **default** clause is optional. The **none** argument tells the compiler to require that all variables used in the compute construct that do not have predetermined data attributes to explicitly appear in a data clause on the compute construct, a **data** construct that lexically contains the compute construct, or a visible **declare** directive. The **present** argument causes all arrays or composite variables used in the compute construct that have implicitly determined data attributes to be treated as if they appeared in a **present** clause.

[•] The reduction *var* may not be a member of a composite variable.

1021 2.6. Data Environment

This section describes the data attributes for variables. The data attributes for a variable may be predetermined, implicitly determined, or explicitly determined. Variables with predetermined data attributes may not appear in a data clause that conflicts with that data attribute. Variables with implicitly determined data attributes may appear in a data clause that overrides the implicit attribute. Variables with explicitly determined data attributes are those which appear in a data clause on a data construct, a compute construct, or a declare directive.

OpenACC supports systems with accelerators that have discrete memory from the host, systems 1028 with accelerators that share memory with the host, as well as systems where an accelerator shares 1029 some memory with the host but also has some discrete memory that is not shared with the host. 1030 In the first case, no data is in shared memory. In the second case, all data is in shared memory. 1031 In the third case, some data may be in shared memory and some data may be in discrete memory, 1032 although a single array or aggregate data structure must be allocated completely in shared or discrete 1033 memory. When a nested OpenACC construct is executed on the device, the default target device for 1034 that construct is the same device on which the encountering accelerator thread is executing. In that 1035 case, the target device shares memory with the encountering thread. 1036

1037 2.6.1. Variables with Predetermined Data Attributes

The loop variable in a C **for** statement or Fortran **do** statement that is associated with a loop directive is predetermined to be private to each thread that will execute each iteration of the loop. Loop variables in Fortran **do** statements within a compute construct are predetermined to be private to the thread that executes the loop.

Variables declared in a C block that is executed in *vector-partitioned* mode are private to the thread associated with each vector lane. Variables declared in a C block that is executed in *workerpartitioned vector-single* mode are private to the worker and shared across the threads associated with the vector lanes of that worker. Variables declared in a C block that is executed in *workersingle* mode are private to the gang and shared across the threads associated with the workers and vector lanes of that gang.

A procedure called from a compute construct will be annotated as **seq**, **vector**, **worker**, or **gang**, as described Section 2.15 Procedure Calls in Compute Regions. Variables declared in **seq** routine are private to the thread that made the call. Variables declared in **vector** routine are private to the worker that made the call and shared across the threads associated with the vector lanes of that worker. Variables declared in **worker** or **gang** routine are private to the gang that made the call and shared across the threads associated with the vector lanes of that gang.

1054 2.6.2. Variables with Implicitly Determined Data Attributes

If a C++ *lambda* is called in a compute region and does not appear in a data clause, then it is treated as if it appears in a **copyin** clause on the current construct. A variable captured by a *lambda* is processed according to its data types: a pointer type variable is treated as if it appears in a **no_create** clause; a reference type variable is treated as if it appears in a **present** clause; for a struct or a class type variable, any pointer member is treated as if it appears in a **no_create** clause on the current construct. If the variable is defined as global or file or function static, it must 1061 appear in a **declare** directive.

1062 2.6.3. Data Regions and Data Lifetimes

Data in shared memory is accessible from the current device as well as to the local thread. Such 1063 1064 data is available to the accelerator for the lifetime of the variable. Data not in shared memory must be copied to and from device memory using data constructs, clauses, and API routines. A data 1065 *lifetime* is the duration from when the data is first made available to the accelerator until it becomes 1066 unavailable. For data in shared memory, the data lifetime begins when the data is allocated and 1067 ends when it is deallocated; for statically allocated data, the data lifetime begins when the program 1068 begins and does not end. For data not in shared memory, the data lifetime begins when it is made 1069 present and ends when it is no longer present. 1070

¹⁰⁷¹ There are four types of data regions. When the program encounters a **data** construct, it creates a data region.

¹⁰⁷³ When the program encounters a compute construct with explicit data clauses or with implicit data ¹⁰⁷⁴ allocation added by the compiler, it creates a data region that has a duration of the compute construct.

When the program enters a procedure, it creates an implicit data region that has a duration of the procedure. That is, the implicit data region is created when the procedure is called, and exited when

the program returns from that procedure invocation. There is also an implicit data region associated with the execution of the program itself. The implicit program data region has a duration of the

1079 execution of the program.

In addition to data regions, a program may create and delete data on the accelerator using **enter data** and **exit data** directives or using runtime API routines. When the program executes an **enter data** directive, or executes a call to a runtime API **acc_copyin** or **acc_create** routine, each *var* on the directive or the variable on the runtime API argument list will be made live on accelerator.

2.6.4. Data Structures with Pointers

This section describes the behavior of data structures that contain pointers. A pointer may be a C or C++ pointer (e.g., float*), a Fortran pointer or array pointer (e.g., real, pointer, dimension(:)), or a Fortran allocatable (e.g., real, allocatable, dimension(:)).

When a data object is copied to device memory, the values are copied exactly. If the data is a data structure that includes a pointer, or is just a pointer, the pointer value copied to device memory will be the host pointer value. If the pointer target object is also allocated in or copied to device memory, the pointer itself needs to be updated with the device address of the target object before dereferencing the pointer in device memory.

An *attach* action updates the pointer in device memory to point to the device copy of the data that the host pointer targets; see Section 2.7.2. For Fortran array pointers and allocatable arrays, this includes copying any associated descriptor (dope vector) to the device copy of the pointer. When the device pointer target is deallocated, the pointer in device memory should be restored to the host value, so it can be safely copied back to host memory. A *detach* action updates the pointer in device memory to have the same value as the corresponding pointer in local memory; see Section 2.7.2. The *attach* and *detach* actions are performed by the **copy**, **copyin**, **copyout**,
create, attach, and detach data clauses (Sections 2.7.3-2.7.12), and the acc_attach and
acc_detach runtime API routines (Sections 3.2.40 and 3.2.41). The *attach* and *detach* actions
use attachment counters to determine when the pointer in device memory needs to be updated; see
Section 2.6.8.

1105 2.6.5. Data Construct

Summary The **data** construct defines *vars* to be allocated in the current device memory for the duration of the region, whether data should be copied from local memory to the current device memory upon region entry, and copied from device memory to local memory upon region exit.

1109 Syntax In C and C++, the syntax of the OpenACC data construct is

#pragma acc data [clause-list] new-line structured block

and in Fortran, the syntax is

!\$acc data [clause-list]
 structured block
!\$acc end data

1111 where *clause* is one of the following:

```
if ( condition )
copy( var-list )
copyin([readonly:]var-list )
copyout( [zero:]var-list )
create( [zero:]var-list )
no_create( var-list )
present( var-list )
deviceptr( var-list )
attach( var-list )
default( none | present )
```

Description Data will be allocated in the memory of the current device and copied from local memory to device memory, or copied back, as required. The data clauses are described in Section 2.7 Data Clauses. Structured reference counters are incremented for data when entering a data region, and decremented when leaving the region, as described in Section 2.6.7 Reference Counters.

1116 **Restrictions**

At least one copy, copyin, copyout, create, no_create, present, deviceptr,
 attach, or default clause must appear on a data construct.

1119 if clause

The **if** clause is optional; when there is no **if** clause, the compiler will generate code to allocate space in the current device memory and move data from and to the local memory as required. When an **if** clause appears, the program will conditionally allocate memory in and move data to and/or from device memory. When the *condition* in the **if** clause evaluates to zero in C or C++, or **ifalse**. in Fortran, no device memory will be allocated, and no data will be moved. When the *condition* evaluates to nonzero in C or C++, or **.true**. in Fortran, the data will be allocated and moved as specified. At most one **if** clause may appear.

1127 default clause

The **default** clause is optional. If the **default** clause is present, then for each compute contruct that is lexically contained within the data construct the behavior will be as if a **default** clause with the same value appeared on the compute construct, unless a **default** clause already appears on the compute construct. At most one **default** clause may appear.

1132 2.6.6. Enter Data and Exit Data Directives

Summary An enter data directive may be used to define *vars* to be allocated in the current device memory for the remaining duration of the program, or until an exit data directive that deallocates the data. They also tell whether data should be copied from local memory to device memory at the enter data directive, and copied from device memory to local memory at the exit data directive. The dynamic range of the program between the enter data directive and the matching exit data directive is the data lifetime for that data.

1139 Syntax In C and C++, the syntax of the OpenACC enter data directive is

#pragma acc enter data clause-list new-line

and in Fortran, the syntax is

!\$acc enter data clause-list

¹¹⁴¹ where *clause* is one of the following:

if(condition)
async [(int-expr)]
wait [(wait-argument)]
copyin(var-list)
create([zero:]var-list)
attach(var-list)

¹¹⁴² In C and C++, the syntax of the OpenACC exit data directive is

#pragma acc exit data clause-list new-line

1143 and in Fortran, the syntax is

!\$acc exit data clause-list

1144 where *clause* is one of the following:

if(condition)
async [(int-expr)]
wait [(wait-argument)]
copyout(var-list)
delete(var-list)
detach(var-list)
finalize

Description At an **enter data** directive, data may be allocated in the current device memory and copied from local memory to device memory. This action enters a data lifetime for those *vars*, and will make the data available for **present** clauses on constructs within the data lifetime. Dynamic reference counters are incremented for this data, as described in Section 2.6.7 Reference Counters. Pointers in device memory may be *attached* to point to the corresponding device copy of the host pointer target.

At an **exit data** directive, data may be copied from device memory to local memory and deallocated from device memory. If no **finalize** clause appears, dynamic reference counters are decremented for this data. If a **finalize** clause appears, the dynamic reference counters are set to zero for this data. Pointers in device memory may be *detached* so as to have the same value as the original host pointer.

The data clauses are described in Section 2.7 Data Clauses. Reference counting behavior is described in Section 2.6.7 Reference Counters.

1158 **Restrictions**

- At least one **copyin**, **create**, or **attach** clause must appear on an **enter data** directive.
- At least one **copyout**, **delete**, or **detach** clause must appear on an **exit data** directive.

1163 if clause

The **if** clause is optional; when there is no **if** clause, the compiler will generate code to allocate or deallocate space in the current device memory and move data from and to local memory. When an **if** clause appears, the program will conditionally allocate or deallocate device memory and move data to and/or from device memory. When the *condition* in the **if** clause evaluates to zero in C or C++, or **.false.** in Fortran, no device memory will be allocated or deallocated, and no data will be moved. When the *condition* evaluates to nonzero in C or C++, or **.true.** in Fortran, the data will be allocated or deallocated and moved as specified.

1171 async clause

1172 The **async** clause is optional; see Section 2.16 Asynchronous Behavior for more information.

1173 wait clause

1174 The wait clause is optional; see Section 2.16 Asynchronous Behavior for more information.

1175 finalize clause

The **finalize** clause is allowed on the **exit data** directive and is optional. When no **finalize** clause appears, the **exit data** directive will decrement the dynamic reference counters for *vars* appearing in **copyout** and **delete** clauses, and will decrement the attachment counters for pointers appearing in **detach** clauses. If a **finalize** clause appears, the **exit data** directive will set the dynamic reference counters to zero for *vars* appearing in **detach** clauses, and will set the attachment counters to zero for pointers appearing in **detach** clauses.

1182 2.6.7. Reference Counters

When device memory is allocated for data not in shared memory due to data clauses or OpenACC
API routine calls, the OpenACC implementation keeps track of that device memory and its relationship to the corresponding data in host memory.

Each section of device memory will be associated with two reference counters per device, a struc-1186 tured reference counter and a dynamic reference counter. The structured and dynamic reference 1187 counters are used to determine when to allocate or deallocate data in device memory. The struc-1188 tured reference counter for a block of data keeps track of how many nested data regions have been 1189 entered for that data. The initial value of the structured reference counter for static data in device 1190 memory (in a global **declare** directive) is one; for all other data, the initial value is zero. The 1191 dynamic reference counter for a block of data keeps track of how many dynamic data lifetimes are 1192 currently active in device memory for that block. The initial value of the dynamic reference counter 1193 is zero. Data is considered *present* if the sum of the structured and dynamic reference counters is 1194 greater than zero. 1195

A structured reference counter is incremented when entering each data or compute region that con-1196 tain an explicit data clause or implicitly-determined data attributes for that block of memory, and 1197 is decremented when exiting that region. A dynamic reference counter is incremented for each 1198 enter data copyin or create clause, or each acc_copyin or acc_create API routine 1199 call for that block of memory. The dynamic reference counter is decremented for each exit data 1200 copyout or delete clause when no finalize clause appears, or each acc_copyout or 1201 acc delete API routine call for that block of memory. The dynamic reference counter will be 1202 set to zero with an **exit data copyout** or **delete** clause when a **finalize** clause appears, 1203 or each acc_copyout_finalize or acc_delete_finalize API routine call for the block 1204 1205 of memory. The reference counters are modified synchronously with the local thread, even if the data directives include an **async** clause. When both structured and dynamic reference counters 1206 reach zero, the data lifetime in device memory for that data ends. 1207

1208 2.6.8. Attachment Counter

Since multiple pointers can target the same address, each pointer in device memory is associated with an *attachment counter* per device. The *attachment counter* for a pointer is initialized to zero when the pointer is allocated in device memory. The *attachment counter* for a pointer is set to one whenever the pointer is *attached* to new target address, and incremented whenever an *attach* action for that pointer is performed for the same target address. The *attachment counter* is decremented whenever a *detach* action occurs for the pointer, and the pointer is *detached* when the *attachment counter* reaches zero. This is described in more detail in Section 2.7.2 Data Clause Actions.

A pointer in device memory can be assigned a device address in two ways. The pointer can be attached to a device address due to data clauses or API routines, as described in Section 2.7.2 Data Clause Actions, or the pointer can be assigned in a compute region executed on that device. Unspecified behavior may result if both ways are used for the same pointer.

Pointer members of structs, classes, or derived types in device or host memory can be overwritten due to update directives or API routines. It is the user's responsibility to ensure that the pointers have the appropriate values before or after the data movement in either direction. The behavior of the program is undefined if any of the pointer members are attached when an update of a composite variable is performed.

1225 2.7. Data Clauses

These data clauses may appear on the parallel construct, kernels construct, serial con-1226 struct, data construct, the enter data and exit data directives, and declare directives. 1227 In the descriptions, the *region* is a compute region with a clause appearing on a **parallel**, 1228 kernels, or serial construct, a data region with a clause on a data construct, or an implicit 1229 data region with a clause on a **declare** directive. If the **declare** directive appears in a global 1230 context, the corresponding implicit data region has a duration of the program. The list argument to 1231 each data clause is a comma-separated collection of vars. For all clauses except **deviceptr** and 1232 **present**, the list argument may include a Fortran *common block* name enclosed within slashes, 1233 if that *common block* name also appears in a **declare** directive **link** clause. In all cases, the 1234 compiler will allocate and manage a copy of the var in the memory of the current device, creating a 1235 visible device copy of that var, for data not in shared memory. 1236

OpenACC supports accelerators with discrete memories from the local thread. However, if the accelerator can access the local memory directly, the implementation may avoid the memory allocation and data movement and simply share the data in local memory. Therefore, a program that uses and assigns data on the host and uses and assigns the same data on the accelerator within a data region without update directives to manage the coherence of the two copies may get different answers on different accelerators or implementations.

1243 **Restrictions**

- Data clauses may not follow a **device_type** clause.
- See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in data clauses.

1247 2.7.1. Data Specification in Data Clauses

In C and C++, a subarray is an array name followed by an extended array range specification in brackets, with start and length, such as

AA[2:n]

If the lower bound is missing, zero is used. If the length is missing and the array has known size, the size of the array is used; otherwise the length is required. The subarray **AA**[2:n] means element **AA**[2], **AA**[3], ..., **AA**[2+n-1].

¹²⁵³ In C and C++, a two dimensional array may be declared in at least four ways:

- Statically-sized array: float AA[100][200];
- Pointer to statically sized rows: typedef float row[200]; row* BB;
- Statically-sized array of pointers: **float*** **CC[200]**;
- Pointer to pointers: **float**** **DD**;

Each dimension may be statically sized, or a pointer to dynamically allocated memory. Each of these may be included in a data clause using subarray notation to specify a rectangular array:

- **AA**[2:n][0:200]
- BB[2:n][0:m]
- CC[2:n][0:m]
- DD[2:n][0:m]

Multidimensional rectangular subarrays in C and C++ may be specified for any array with any combination of statically-sized or dynamically-allocated dimensions. For statically sized dimensions, all dimensions except the first must specify the whole extent, to preserve the contiguous data restriction, discussed below. For dynamically allocated dimensions, the implementation will allocate pointers in device memory corresponding to the pointers in local memory, and will fill in those pointers as appropriate.

¹²⁷⁰ In Fortran, a subarray is an array name followed by a comma-separated list of range specifications ¹²⁷¹ in parentheses, with lower and upper bound subscripts, such as

arr(1:high, low:100)

If either the lower or upper bounds are missing, the declared or allocated bounds of the array, if known, are used. All dimensions except the last must specify the whole extent, to preserve the contiguous data restriction, discussed below.

1275 **Restrictions**

• In Fortran, the upper bound for the last dimension of an assumed-size dummy array must be specified.

1278 1279	• In C and C++, the length for dynamically allocated dimensions of an array must be explicitly specified.
1280 1281	• In C and C++, modifying pointers in pointer arrays during the data lifetime, either on the host or on the device, may result in undefined behavior.
1282 1283	• If a subarray appears in a data clause, the implementation may choose to allocate memory for only that subarray on the accelerator.
1284 1285	• In Fortran, array pointers may appear, but pointer association is not preserved in device memory.
1286 1287	• Any array or subarray in a data clause, including Fortran array pointers, must be a contiguous block of memory, except for dynamic multidimensional C arrays.
1288 1289 1290	• In C and C++, if a variable or array of composite type appears, all the data members of the struct or class are allocated and copied, as appropriate. If a composite member is a pointer type, the data addressed by that pointer are not implicitly copied.
1291 1292 1293	• In Fortran, if a variable or array of composite type appears, all the members of that derived type are allocated and copied, as appropriate. If any member has the allocatable or pointer attribute, the data accessed through that member are not copied.
1294 1295 1296	• If an expression is used in a subscript or subarray expression in a clause on a data construct, the same value is used when copying data at the end of the data region, even if the values of variables in the expression change during the data region.

1297 2.7.2. Data Clause Actions

Most of the data clauses perform one or more the following actions. The actions test or modify one or both of the structured and dynamic reference counters, depending on the directive on which the data clause appears.

1301 Present Increment Action

A present increment action is one of the actions that may be performed for a **present** (Section 2.7.4), **copy** (Section 2.7.5), **copyin** (Section 2.7.6), **copyout** (Section 2.7.7), **create** (Section 2.7.8), or **no_create** (Section 2.7.9) clause, or for a call to an **acc_copyin** (Section 3.2.26) or **acc_create** (Section 3.2.27) API routine. See those sections for details.

¹³⁰⁶ A *present increment* action for a *var* occurs only when *var* is already present in device memory.

¹³⁰⁷ A *present increment* action for a *var* increments the structured or dynamic reference counter for *var*.

1308 **Present Decrement Action**

A present decrement action is one of the actions that may be performed for a **present** (Section 2.7.4), **copy** (Section 2.7.5), **copyin** (Section 2.7.6), **copyout** (Section 2.7.7), **create** (Section 2.7.8), **no_create** (Section 2.7.9), or **delete** (Section 2.7.10) clause, or for a call to an **acc_copyout** (Section 3.2.28) or **acc_delete** (Section 3.2.29) API routine. See those sections for details.

¹³¹⁴ A *present decrement* action for a *var* occurs only when *var* is already present in device memory.

A present decrement action for a var decrements the structured or dynamic reference counter for var, if its value is greater than zero. If the device memory associated with var was mapped to the device using acc_map_data, the dynamic reference count may not be decremented to zero, except by a call to acc_unmap_data. If the reference counter is already zero, its value is left unchanged.

1320 Create Action

A *create* action is one of the actions that may be performed for a **copyout** (Section 2.7.7) or **create** (Section 2.7.8) clause, or for a call to an **acc_create** API routine (Section 3.2.27). See those sections for details.

- A *create* action for a *var* occurs only when *var* is not already present in device memory.
- 1325 A *create* action for a *var*:
- allocates device memory for *var*; and
- sets the structured or dynamic reference counter to one.

1328 Copyin Action

A *copyin* action is one of the actions that may be performed for a **copy** (Section 2.7.5) or **copyin** (Section 2.7.6) clause, or for a call to an **acc_copyin** API routine (Section 3.2.26). See those sections for details.

- 1332 A *copyin* action for a *var* occurs only when *var* is not already present in device memory.
- 1333 A *copyin* action for a *var*:
- allocates device memory for *var*;
- initiates a copy of the data for *var* from the local thread memory to the corresponding device
 memory; and
- sets the structured or dynamic reference counter to one.

¹³³⁸ The data copy may complete asynchronously, depending on other clauses on the directive.

1339 Copyout Action

A *copyout* action is one of the actions that may be performed for a **copy** (Section 2.7.5) or **copyout** (Section 2.7.7) clause, or for a call to an **acc_copyout** API routine (Section 3.2.28). See those sections for details.

- A *copyout* action for a *var* occurs only when *var* is present in device memory.
- 1344 A *copyout* action for a *var*:
- performs an *immediate detach* action for any pointer in *var*;
- initiates a copy of the data for *var* from device memory to the corresponding local thread
 memory; and

• deallocates device memory for *var*.

The data copy may complete asynchronously, depending on other clauses on the directive, in which case the memory is deallocated when the data copy is complete.

1351 Delete Action

A *delete* action is one of the actions that may be performed for a **present** (Section 2.7.4), **copyin** (Section 2.7.6), **create** (Section 2.7.8), **no_create** (Section 2.7.9), or **delete** (Section 2.7.10) clause, or for a call to an **acc_delete** API routine (Section 3.2.29). See those sections for details.

A *delete* action for a *var* occurs only when *var* is present in device memory.

- 1356 A *delete* action for *var*:
- performs an *immediate detach* action for any pointer in *var*; and
- deallocates device memory for *var*.

1359 Attach Action

An *attach* action is one of the actions that may be performed for a **present** (Section 2.7.4),

¹³⁶¹ copy (Section 2.7.5), copyin (Section 2.7.6), copyout (Section 2.7.7), create (Section 2.7.8),

no_create (Section 2.7.9), or attach (Section 2.7.10) clause, or for a call to an acc_attach

API routine (Section 3.2.40). See those sections for details.

1364 An *attach* action for a *var* occurs only when *var* is a pointer reference.

If the pointer *var* is in shared memory or is not present in the current device memory, or if the 1365 address to which var points is not present in the current device memory, no action is taken. If the 1366 attachment counter for var is nonzero and the pointer in device memory already points to the device 1367 copy of the data in *var*, the *attachment counter* for the pointer *var* is incremented. Otherwise, the 1368 pointer in device memory is *attached* to the device copy of the data by initiating an update for the 1369 pointer in device memory to point to the device copy of the data and setting the *attachment counter* 1370 for the pointer var to one. The update may complete asynchronously, depending on other clauses 1371 on the directive. The pointer update must follow any data copies due to *copyin* actions that are 1372 performed for the same directive. 1373

1374 Detach Action

A detach action is one of the actions that may be performed for a present (Section 2.7.4),
copy (Section 2.7.5), copyin (Section 2.7.6), copyout (Section 2.7.7), create (Section 2.7.8),
no_create (Section 2.7.9), delete (Section 2.7.10), or detach (Section 2.7.10) clause, or for
a call to an acc_detach API routine (Section 3.2.41). See those sections for details.

1379 A *detach* action for a *var* occurs only when *var* is a pointer reference.

If the pointer *var* is in shared memory or is not present in the current device memory, or if the *attachment counter* for *var* for the pointer is zero, no action is taken. Otherwise, the *attachment counter* for the pointer *var* is decremented. If the *attachment counter* is decreased to zero, the pointer is *detached* by initiating an update for the pointer *var* in device memory to have the same value as the corresponding pointer in local memory. The update may complete asynchronously,
depending on other clauses on the directive. The pointer update must precede any data copies due
to *copyout* actions that are performed for the same directive.

1387 Immediate Detach Action

An *immediate detach* action is one of the actions that may be performed for a **detach** (Section 2.7.10) clause, or for a call to an **acc_detach_finalize** API routine (Section 3.2.41). See those sections for details.

An *immediate detach* action for a *var* occurs only when *var* is a pointer reference and is present in device memory.

If the *attachment counter* for the pointer is zero, the *immediate detach* action has no effect. Otherwise, the *attachment counter* for the pointer set to zero and the pointer is *detached* by initiating an update for the pointer in device memory to have the same value as the corresponding pointer in local memory. The update may complete asynchronously, depending on other clauses on the directive. The pointer update must precede any data copies due to *copyout* actions that are performed for the same directive.

1399 2.7.3. deviceptr clause

The deviceptr clause may appear on structured data and compute constructs and declare
 directives.

The **deviceptr** clause is used to declare that the pointers in *var-list* are device pointers, so the data need not be allocated or moved between the host and device for this pointer.

¹⁴⁰⁴ In C and C++, the *vars* in *var-list* must be pointer variables.

In Fortran, the *vars* in *var-list* must be dummy arguments (arrays or scalars), and may not have the Fortran **pointer**, **allocatable**, or **value** attributes.

¹⁴⁰⁷ For data in shared memory, host pointers are the same as device pointers, so this clause has no effect.

1409 **2.7.4. present clause**

The **present** clause may appear on structured **data** and compute constructs and **declare** directives. The **present** clause specifies that *vars* in *var-list* are in shared memory or are already present in the current device memory due to data regions or data lifetimes that contain the construct on which the **present** clause appears.

For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory, the **present** clause behaves as follows:

- At entry to the region:
- If *var* is not present in the current device memory, a runtime error is issued.
- Otherwise, a *present increment* action with the structured reference counter is performed.
 If *var* is a pointer reference, an *attach* action is performed.

• At exit from the region:

- If *var* is not present in the current device memory, a runtime error is issued.
- Otherwise, a *present decrement* action with the structured reference counter is performed. If *var* is a pointer reference, a *detach* action is performed. If both structured and dynamic reference counters are zero, a *delete* action is performed.

1425 **Restrictions**

- If only a subarray of an array is present in the current device memory, the present clause must specify the same subarray, or a subarray that is a proper subset of the subarray in the data lifetime.
- It is a runtime error if the subarray in *var-list* clause includes array elements that are not part of the subarray specified in the data lifetime.

¹⁴³¹ **2.7.5. copy clause**

The **copy** clause may appear on structured **data** and compute constructs and on **declare** directives.

- For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory, the **copy** clause behaves as follows:
- At entry to the region:
- If *var* is present, a *present increment* action with the structured reference counter is performed. If *var* is a pointer reference, an *attach* action is performed.
- Otherwise, a *copyin* action with the structured reference counter is performed. If *var* is a pointer reference, an *attach* action is performed.
- At exit from the region:
- If *var* is not present in the current device memory, a runtime error is issued.
- Otherwise, a *present decrement* action with the structured reference counter is performed. If *var* is a pointer reference, a *detach* action is performed. If both structured and dynamic reference counters are zero, a *copyout* action is performed.

1446 The restrictions regarding subarrays in the **present** clause apply to this clause.

For compatibility with OpenACC 2.0, **present_or_copy** and **pcopy** are alternate names for **copy**.

1449 **2.7.6. copyin clause**

The **copyin** clause may appear on structured **data** and compute constructs, on **declare** directives, and on **enter data** directives.

¹⁴⁵² For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory,

1453 the **copyin** clause behaves as follows:

- At entry to a region, the structured reference counter is used. On an **enter data** directive, the dynamic reference counter is used.
- If *var* is present, a *present increment* action with the appropriate reference counter is
 performed. If *var* is a pointer reference, an *attach* action is performed.
- Otherwise, a *copyin* action with the appropriate reference counter is performed. If *var* is a pointer reference, an *attach* action is performed.
- At exit from the region:
- If *var* is not present in the current device memory, a runtime error is issued.
- Otherwise, a *present decrement* action with the structured reference counter is performed. If *var* is a pointer reference, a *detach* action is performed. If both structured and dynamic reference counters are zero, a *delete* action is performed.

¹⁴⁶⁵ If the optional **readonly** modifier appears, then the implementation may assume that the data ¹⁴⁶⁶ referenced by *var-list* is never written to within the applicable region.

¹⁴⁶⁷ The restrictions regarding subarrays in the **present** clause apply to this clause.

For compatibility with OpenACC 2.0, present_or_copyin and pcopyin are alternate names for copyin.

- An enter data directive with a copyin clause is functionally equivalent to a call to the acc_copyin
- 1471 API routine, as described in Section 3.2.26.

1472 **2.7.7. copyout clause**

The **copyout** clause may appear on structured **data** and compute constructs, on **declare** directives, and on **exit data** directives. The clause may optionally have a **zero** modifier if the **copyout** clause appears on a structured **data** or compute construct.

For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory, the **copyout** clause behaves as follows:

- At entry to a region:
- If *var* is present, a *present increment* action with the structured reference counter is performed. If *var* is a pointer reference, an *attach* action is performed.
- Otherwise, a *create* action with the structured reference is performed. If *var* is a pointer reference, an *attach* action is performed. If a **zero** modifier appears, the memory is zeroed after the *create* action.
- At exit from a region, the structured reference counter is used. On an **exit data** directive, the dynamic reference counter is used.
- If *var* is not present in the current device memory, a runtime error is issued.
- Otherwise, the reference counter is updated:
- * On an exit data directive with a finalize clause, the dynamic reference counter is set to zero.
- * Otherwise, a *present decrement* action with the appropriate reference counter is

1491 performed.

1492If var is a pointer reference, a detach action is performed. If both structured and dynamic1493reference counters are zero, a copyout action is performed.

1494 The restrictions regarding subarrays in the **present** clause apply to this clause.

For compatibility with OpenACC 2.0, present_or_copyout and pcopyout are alternate names for copyout.

An **exit data** directive with a **copyout** clause and with or without a **finalize** clause is functionally equivalent to a call to the **acc_copyout_finalize** or **acc_copyout** API routine, respectively, as described in Section 3.2.28.

1500 2.7.8. create clause

The **create** clause may appear on structured **data** and compute constructs, on **declare** directives, and on **enter data** directives. The clause may optionally have a **zero** modifier.

For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory, the **create** clause behaves as follows:

- At entry to a region, the structured reference counter is used. On an **enter data** directive, the dynamic reference counter is used.
- If *var* is present, a *present increment* action with the appropriate reference counter is
 performed. If *var* is a pointer reference, an *attach* action is performed.
- Otherwise, a *create* action with the appropriate reference counter is performed. If *var* is a pointer reference, an *attach* action is performed. If a **zero** modifier appears, the memory is zeroed after the *create* action.
- At exit from the region:
- If *var* is not present in the current device memory, a runtime error is issued.
- Otherwise, a *present decrement* action with the structured reference counter is performed. If *var* is a pointer reference, a *detach* action is performed. If both structured and dynamic reference counters are zero, a *delete* action is performed.

¹⁵¹⁷ The restrictions regarding subarrays in the **present** clause apply to this clause.

For compatibility with OpenACC 2.0, present_or_create and pcreate are alternate names for create.

An enter data directive with a create clause is functionally equivalent to a call to the acc_create API routine, as described in Section 3.2.27.

1522 **2.7.9. no_create clause**

¹⁵²³ The **no_create** clause may appear on structured **data** and compute constructs.

For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory, the **no_create** clause behaves as follows:

• At entry to the region:

- If *var* is present, a *present increment* action with the structured reference counter is performed. If *var* is a pointer reference, an *attach* action is performed.
- Otherwise, no action is performed, and any device code in this construct will use the
 local memory address for *var*.
- At exit from the region:
- If *var* is not present in the current device memory, no action is performed.
- Otherwise, a *present decrement* action with the structured reference counter is performed. If *var* is a pointer reference, a *detach* action is performed. If both structured and dynamic reference counters are zero, a *delete* action is performed.
- ¹⁵³⁶ The restrictions regarding subarrays in the **present** clause apply to this clause.

1537 2.7.10. delete clause

¹⁵³⁸ The **delete** clause may appear on **exit data** directives.

For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory, the **delete** clause behaves as follows:

- If *var* is not present in the current device memory, a runtime error is issued.
- Otherwise, the dynamic reference counter is updated:
- On an exit data directive with a finalize clause, the dynamic reference counter
 is set to zero.
- Otherwise, a *present decrement* action with the dynamic reference counter is performed.

1546 If *var* is a pointer reference, a *detach* action is performed. If both structured and dynamic 1547 reference counters are zero, a *delete* action is performed.

An **exit data** directive with a **delete** clause and with or without a **finalize** clause is functionally equivalent to a call to the **acc_delete_finalize** or **acc_delete** API routine, respectively, as described in Section 3.2.29.

1551 2.7.11. attach clause

The **attach** clause may appear on structured **data** and compute constructs and on **enter data** directives. Each *var* argument to an **attach** clause must be a C or C++ pointer or a Fortran variable or array with the **pointer** or **allocatable** attribute.

For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory, the **attach** clause behaves as follows:

- At entry to a region or at an **enter data** directive, an *attach* action is performed.
- At exit from the region, a *detach* action is performed.

1559 2.7.12. detach clause

The **detach** clause may appear on **exit data** directives. Each *var* argument to a **detach** clause must be a C or C++ pointer or a Fortran variable or array with the **pointer** or **allocatable** attribute.

For each *var* in *varlist*, if *var* is in shared memory, no action is taken; if *var* is not in shared memory, the **detach** clause behaves as follows:

- If there is a **finalize** clause on the **exit data** directive, an *immediate detach* action is performed.
- Otherwise, a *detach* action is performed.

1568 2.8. Host_Data Construct

1569 Summary The host_data construct makes the address of data in device memory available on
 1570 the host.

1571 Syntax In C and C++, the syntax of the OpenACC host_data construct is

#pragma acc host_data clause-list new-line structured block

1572 and in Fortran, the syntax is

!\$acc host_data clause-list
 structured block
!\$acc end host_data

¹⁵⁷³ where *clause* is one of the following:

```
use_device( var-list )
if( condition )
if_present
```

1574 **Description** This construct is used to make the address of data in device memory available in
 1575 host code.

1576 **Restrictions**

• A *var* in a **use_device** clause must be the name of a variable or array.

• At least one **use_device** clause must appear.

• At most one **if** clause may appear. In Fortran, the condition must evaluate to a scalar logical value; in C or C++, the condition must evaluate to a scalar integer value.

See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in
 use_device clauses.

1583 2.8.1. use_device clause

The **use_device** clause tells the compiler to use the current device address of any *var* in *var-list* in code within the construct. In particular, this may be used to pass the device address of *var* to optimized procedures written in a lower-level API. When there is no **if_present** clause, and either there is no **if** clause or the condition in the **if** clause evaluates to nonzero (in C or C++) or **.true**. (in Fortran), the *var* in *var-list* must be present in the accelerator memory due to data regions or data lifetimes that contain this construct. For data in shared memory, the device address is the same as the host address.

1591 2.8.2. if clause

The **if** clause is optional. When an **if** clause appears and the condition evaluates to zero in C or C++, or **.false**. in Fortran, the compiler will not replace the addresses of any *var* in code within the construct. When there is no **if** clause, or when an **if** clause appears and the condition evaluates to nonzero in C or C++, or **.true**. in Fortran, the compiler will replace the addresses as described in the previous subsection.

1597 2.8.3. if_present clause

When an **if_present** clause appears on the directive, the compiler will only replace the address of any *var* which appears in *var-list* that is present in the current device memory.

1600 2.9. Loop Construct

Summary The OpenACC **loop** construct applies to a loop which must immediately follow this directive. The **loop** construct can describe what type of parallelism to use to execute the loop and declare private *vars* and reduction operations.

¹⁶⁰⁴ Syntax In C and C++, the syntax of the loop construct is

#pragma acc loop [clause-list] new-line
 for loop

¹⁶⁰⁵ In Fortran, the syntax of the **loop** construct is

!\$acc loop [clause-list] do loop

1606 where *clause* is one of the following:

```
collapse( n )
gang [( gang-arg-list )]
worker [( [num:]int-expr )]
vector [( [length:]int-expr )]
seq
independent
auto
tile( size-expr-list )
device_type( device-type-list )
private( var-list )
reduction( operator:var-list )
```

1607 where *gang-arg* is one of:

[num:]int-expr
static:size-expr

and gang-arg-list may have at most one num and one static argument,

1609 and where *size-expr* is one of:

```
*
int-expr
```

1610 Some clauses are only valid in the context of a **kernels** construct; see the descriptions below.

¹⁶¹¹ An *orphaned* **loop** construct is a **loop** construct that is not lexically enclosed within a compute ¹⁶¹² construct. The parent compute construct of a **loop** construct is the nearest compute construct that ¹⁶¹³ lexically contains the **loop** construct.

1614 **Restrictions**

- Only the collapse, gang, worker, vector, seq, independent, auto, and tile clauses may follow a device_type clause.
- The *int-expr* argument to the **worker** and **vector** clauses must be invariant in the kernels region.
- A loop associated with a **loop** construct that does not have a **seq** clause must be written such that the loop iteration count is computable when entering the **loop** construct.
- Only one of the **seq**, **independent**, and **auto** clauses may appear.
- A gang, worker, or vector clause may not appear if a seq clause appears.

1623 2.9.1. collapse clause

The **collapse** clause is used to specify how many tightly nested loops are associated with the loop construct. The argument to the **collapse** clause must be a constant positive integer expression. If no collapse clause appears, only the immediately following loop is associated with the
 loop construct.

If more than one loop is associated with the **loop** construct, the iterations of all the associated loops are all scheduled according to the rest of the clauses. The trip count for all loops associated with the **collapse** clause must be computable and invariant in all the loops.

1631 It is implementation-defined whether a **gang**, **worker** or **vector** clause on the construct is ap-1632 plied to each loop, or to the linearized iteration space.

1633 2.9.2. gang clause

When the parent compute construct is a **parallel** construct, or on an orphaned **loop** construct, 1634 the gang clause specifies that the iterations of the associated loop or loops are to be executed in 1635 parallel by distributing the iterations among the gangs created by the **parallel** construct. A 1636 **loop** construct with the **gang** clause transitions a compute region from gang-redundant mode to 1637 gang-partitioned mode. The number of gangs is controlled by the parallel construct; only the 1638 static argument is allowed. The loop iterations must be data independent, except for vars which 1639 appear in a **reduction** clause or which are modified in an atomic region. The region of a loop 1640 with the gang clause may not contain another loop with the gang clause unless within a nested 1641 compute region. 1642

When the parent compute construct is a **kernels** construct, the **gang** clause specifies that the iterations of the associated loop or loops are to be executed in parallel across the gangs. An argument with no keyword or with the **num** keyword is allowed only when the **num_gangs** does not appear on the **kernels** construct. If an argument with no keyword or an argument after the **num** keyword appears, it specifies how many gangs to use to execute the iterations of this loop. The region of a loop with the **gang** clause may not contain another loop with a **gang** clause unless within a nested compute region.

The scheduling of loop iterations to gangs is not specified unless the **static** modifier appears as 1650 an argument. If the **static** modifier appears with an integer expression, that expression is used 1651 as a *chunk* size. If the static modifier appears with an asterisk, the implementation will select a 1652 *chunk* size. The iterations are divided into chunks of the selected *chunk* size, and the chunks are 1653 assigned to gangs starting with gang zero and continuing in round-robin fashion. Two gang loops 1654 in the same parallel region with the same number of iterations, and with **static** clauses with the 1655 same argument, will assign the iterations to gangs in the same manner. Two **gang** loops in the 1656 same kernels region with the same number of iterations, the same number of gangs to use, and with 1657 **static** clauses with the same argument, will assign the iterations to gangs in the same manner. 1658

1659 2.9.3. worker clause

When the parent compute construct is a **parallel** construct, or on an orphaned **loop** construct, the **worker** clause specifies that the iterations of the associated loop or loops are to be executed in parallel by distributing the iterations among the multiple workers within a single gang. A **loop** construct with a **worker** clause causes a gang to transition from worker-single mode to workerpartitioned mode. In contrast to the **gang** clause, the **worker** clause first activates additional worker-level parallelism and then distributes the loop iterations across those workers. No argument is allowed. The loop iterations must be data independent, except for *vars* which appear in a **reduction** clause or which are modified in an atomic region. The region of a loop with the **worker** clause may not contain a loop with the **gang** or **worker** clause unless within a nested compute region.

When the parent compute construct is a **kernels** construct, the **worker** clause specifies that the iterations of the associated loop or loops are to be executed in parallel across the workers within a single gang. An argument is allowed only when the **num_workers** does not appear on the **kernels** construct. The optional argument specifies how many workers per gang to use to execute the iterations of this loop. The region of a loop with the **worker** clause may not contain a loop with a **gang** or **worker** clause unless within a nested compute region.

¹⁶⁷⁶ All workers will complete execution of their assigned iterations before any worker proceeds beyond ¹⁶⁷⁷ the end of the loop.

1678 2.9.4. vector clause

When the parent compute construct is a **parallel** construct, or on an orphaned **loop** construct, 1679 the **vector** clause specifies that the iterations of the associated loop or loops are to be executed 1680 in vector or SIMD mode. A **loop** construct with a **vector** clause causes a worker to transition 1681 from vector-single mode to vector-partitioned mode. Similar to the worker clause, the vector 1682 clause first activates additional vector-level parallelism and then distributes the loop iterations across 1683 those vector lanes. The operations will execute using vectors of the length specified or chosen for 1684 the parallel region. The loop iterations must be data independent, except for vars which appear in 1685 a **reduction** clause or which are modified in an atomic region. The region of a loop with the 1686 vector clause may not contain a loop with the gang, worker, or vector clause unless within 1687 a nested compute region. 1688

When the parent compute construct is a **kernels** construct, the **vector** clause specifies that the iterations of the associated loop or loops are to be executed with vector or SIMD processing. An argument is allowed only when the **vector_length** does not appear on the **kernels** construct. If an argument appears, the iterations will be processed in vector strips of that length; if no argument appears, the implementation will choose an appropriate vector length. The region of a loop with the **vector** clause may not contain a loop with a **gang**, **worker**, or **vector** clause unless within a nested compute region.

All vector lanes will complete execution of their assigned iterations before any vector lane proceeds beyond the end of the loop.

1698 2.9.5. seq clause

The **seq** clause specifies that the associated loop or loops are to be executed sequentially by the accelerator. This clause will override any automatic parallelization or vectorization.

1701 **2.9.6. auto clause**

The **auto** clause specifies that the implementation must analyze the loop and determine whether the loop iterations are data-independent. If it determines that the loop iterations are data-independent, the implementation must treat the **auto** clause as if it is an **independent** clause. If not, or if it is unable to make a determination, it must treat the **auto** clause as if it is a **seq** clause, and it must ignore any **gang**, **worker**, or **vector** clauses on the loop construct.

When the parent compute construct is a **kernels** construct, a **loop** construct with no **independent** or **seq** clause is treated as if it has the **auto** clause.

1709 **2.9.7. tile clause**

The tile clause specifies that the implementation should split each loop in the loop nest into two 1710 loops, with an outer set of *tile* loops and an inner set of *element* loops. The argument to the **tile** 1711 clause is a list of one or more tile sizes, where each tile size is a constant positive integer expression 1712 or an asterisk. If there are *n* tile sizes in the list, the **loop** construct must be immediately followed 1713 by *n* tightly-nested loops. The first argument in the *size-expr-list* corresponds to the innermost loop 1714 of the *n* associated loops, and the last element corresponds to the outermost associated loop. If the 1715 tile size is an asterisk, the implementation will choose an appropriate value. Each loop in the nest 1716 will be split or *strip-mined* into two loops, an outer *tile* loop and an inner *element* loop. The trip 1717 count of the element loop will be limited to the corresponding tile size from the *size-expr-list*. The 1718 tile loops will be reordered to be outside all the *element* loops, and the *element* loops will all be 1719 inside the *tile* loops. 1720

1721 If the **vector** clause appears on the **loop** construct, the **vector** clause is applied to the *element* 1722 loops. If the **gang** clause appears on the **loop** construct, the **gang** clause is applied to the *tile* 1723 loops. If the **worker** clause appears on the **loop** construct, the **worker** clause is applied to the 1724 *element* loops if no **vector** clause appears, and to the *tile* loops otherwise.

1725 2.9.8. device_type clause

¹⁷²⁶ The **device_type** clause is described in Section 2.4 Device-Specific Clauses.

1727 2.9.9. independent clause

The **independent** clause tells the implementation that the loop iterations must be data independent, except for *vars* which appear in a **reduction** clause or which are modified in an atomic region. This allows the implementation to generate code to execute the iterations in parallel with no synchronization.

A loop construct with no **auto** or **seq** clause is treated as if it has the **independent** clause when it is an orphaned **loop** construct or its parent compute construct is a **parallel** construct.

1734 Note

- It is likely a programming error to use the **independent** clause on a loop if any iteration writes to a variable or array element that any other iteration also writes or reads, except for *vars* which appear in a **reduction** clause or which are modified in an atomic region.
- The implementation may be restricted in the levels of parallelism it can apply by the presence of **loop** constructs with **gang**, **worker**, or **vector** clauses for outer or inner loops.

1740 2.9.10. private clause

The **private** clause on a **loop** construct specifies that a copy of each item in *var-list* will be created. If the body of the loop is executed in *vector-partitioned* mode, a copy of the item is created for each thread associated with each vector lane. If the body of the loop is executed in *workerpartitioned vector-single* mode, a copy of the item is created for and shared across the set of threads associated with all the vector lanes of each worker. Otherwise, a copy of the item is created for and shared across the set of threads associated with all the vector lanes of all the workers of each gang.

1747 **Restrictions**

See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in
 private clauses.

1750 **2.9.11. reduction clause**

The **reduction** clause specifies a reduction operator and one or more *vars*. For each reduction 1751 *var*, a private copy is created in the same manner as for a **private** clause on the **loop** construct, 1752 and initialized for that operator; see the table in Section 2.5.13 reduction clause. After the loop, the 1753 values for each thread are combined using the specified reduction operator, and the result combined 1754 with the value of the original var and stored in the original var. If the original var is not private, 1755 this update occurs by the end of the compute region, and any access to the original var is undefined 1756 within the compute region. Otherwise, the update occurs at the end of the loop. If the reduction 1757 *var* is an array or subarray, the reduction operation is logically equivalent to applying that reduction 1758 operation to each array element of the array or subarray individually. If the reduction var is a com-1759 posite variable, the reduction operation is logically equivalent to applying that reduction operation 1760 to each member of the composite variable individually. 1761

1762 If a variable is involved in a reduction that spans multiple nested loops where two or more of those 1763 loops have associated **loop** directives, a **reduction** clause containing that variable must appear 1764 on each of those **loop** directives.

1765 **Restrictions**

- A *var* in a **reduction** clause must be a scalar variable name, a composite variable name, an array name, an array element, or a subarray (refer to Section 2.7.1).
 Reduction clauses on nested constructs for the same reduction *var* must have the same reduction operator.
- Every *var* in a **reduction** clause appearing on an orphaned **loop** construct must be private.
- The restrictions for a **reduction** clause on a compute construct listed in in Section 2.5.13 reduction clause also apply to a **reduction** clause on a loop construct.
- See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in reduction clauses.

```
1775
     Examples
1776
         • x is not private at the loop directive below, so its reduction normally updates x at the end
1777
           of the parallel region, where gangs synchronize. When possible, the implementation might
1778
           choose to partially update x at the loop exit instead, or fully if num_gangs (1) were added
1779
           to the parallel directive. However, portable applications cannot rely on such early up-
1780
           dates, so accesses to \mathbf{x} are undefined within the parallel region outside the loop.
1781
                int x = 0;
1782
                #pragma acc parallel copy(x)
1783
                {
1784
                   // gang-shared x undefined
1785
                   #pragma acc loop gang worker vector reduction(+:x)
1786
                   for (int i = 0; i < I; ++i)</pre>
1787
                      x += 1; // vector-private x modified
1788
                   // gang-shared x undefined
1789
                } // gang-shared x updated for gang/worker/vector reduction
1790
                // x = I
1791
         • x is private at each of the innermost two loop directives below, so each of their reductions
1792
           updates \mathbf{x} at the loop's exit. However, \mathbf{x} is not private at the outer loop directive, so its
1793
           reduction updates \mathbf{x} by the end of the parallel region instead.
1794
                int x = 0;
1795
                #pragma acc parallel copy(x)
1796
                ł
1797
                   // gang-shared x undefined
1798
                   #pragma acc loop gang reduction(+:x)
1799
                   for (int i = 0; i < I; ++i) {</pre>
1800
                      #pragma acc loop worker reduction(+:x)
1801
                      for (int j = 0; j < J; ++j) {
1802
                         #pragma acc loop vector reduction(+:x)
1803
                         for (int k = 0; k < K; ++k) {
1804
                            x += 1; // vector-private x modified
1805
                         } // worker-private x updated for vector reduction
1806
                      } // gang-private x updated for worker reduction
1807
                   }
1808
                   // gang-shared x undefined
1809
                } // gang-shared x updated for gang reduction
1810
                // x = I * J * K
1811
        • At each loop directive below, x is private due to its implicit firstprivate attribute on
1812
           the parallel directive, but y is not private due to its copy clause on the parallel
1813
           directive. Thus, each reduction updates \mathbf{x} at the loop exit, but each reduction updates \mathbf{y} by
1814
           the end of the parallel region instead.
1815
                int x = 0, y = 0;
1816
                #pragma acc parallel copy(y) // firstprivate(x) implied
1817
1818
                {
```

```
// gang-private x = 0; gang-shared y undefined
1819
                  #pragma acc loop seq reduction(+:x,y)
1820
                  for (int i = 0; i < I; ++i) {
1821
                     x += 1; y += 2; // loop-private x and y modified
1822
                  } // gang-private x updated for seq reduction (trivial reduction)
1823
                  // gang-private x = I; gang-shared y undefined
1824
                  #pragma acc loop worker reduction(+:x,y)
1825
                  for (int i = 0; i < I; ++i) {</pre>
1826
                     x += 1; y += 2; // worker-private x and y modified
1827
                  } // gang-private x updated for worker reduction
1828
                  // gang-private x = 2 * I; gang-shared y undefined
1829
                  #pragma acc loop vector reduction(+:x,y)
1830
                  for (int i = 0; i < I; ++i) {</pre>
1831
                     x += 1; y += 2; // vector-private x and y modified
1832
                  } // gang-private x updated for vector reduction
1833
                  // gang-private x = 3 * I; gang-shared y undefined
1834
                } // gang-shared y updated for gang/seq/worker/vector reductions
1835
               // x = 0; y = 3 * I * 2
1836
        • The examples below are equivalent. That is, the reduction clause on the combined con-
1837
          struct applies to the loop construct but implies a copy clause on the parallel construct. Thus,
1838
          x is not private at the loop directive, so the reduction updates x by the end of the parallel
1839
          region.
1840
                int x = 0;
1841
                #pragma acc parallel loop worker reduction(+:x)
1842
                for (int i = 0; i < I; ++i) {
1843
                  x += 1; // worker-private x modified
1844
                } // gang-shared x updated for gang/worker reduction
1845
               // x = I
1846
1847
                int x = 0;
1848
                #pragma acc parallel copy(x)
1849
1850
                ł
                  // gang-shared x undefined
1851
                  #pragma acc loop worker reduction(+:x)
1852
                  for (int i = 0; i < I; ++i)
                                                          {
1853
                     x += 1; // worker-private x modified
1854
                  }
1855
                  // gang-shared x undefined
1856
                } // gang-shared x updated for gang/worker reduction
1857
               // x = I
1858
        • If the implementation treats the auto clause below as independent, the loop executes in
1859
          gang-partitioned mode and thus examines every element of arr once to compute arr's max-
1860
```

inum. However, if the implementation treats auto as seq, the gangs redundantly compute
 arr's maximum, but the combined result is still arr's maximum. Either way, because x is
 not private at the loop directive, the reduction updates x by the end of the parallel region.

```
int x = 0;
1864
                const int *arr = /*array of I values*/;
1865
                #pragma acc parallel copy(x)
1866
                {
1867
                   // gang-shared x undefined
1868
                   #pragma acc loop auto gang reduction(max:x)
1869
                   for (int i = 0; i < I; ++i) {
1870
                      // complex loop body
1871
                      x = x < arr[i] ? arr[i] : x; // gang or loop-private x modified
1872
                   }
1873
                   // gang-shared x undefined
1874
                } // gang-shared x updated for gang or gang/seq reduction
1875
                // x = arr maximum
1876
        • The following example is the same as the previous one except that the reduction operator is
1877
           now +. While gang-partitioned mode sums the elements of arr once, gang-redundant mode
1878
           sums them once per gang, producing a result many times arr's sum. This example shows
1879
           that, for some reduction operators, combining auto, gang, and reduction is typically
1880
           non-portable.
1881
                int x = 0;
1882
                const int *arr = /*array of I values*/;
1883
                #pragma acc parallel copy(x)
1884
1885
                ł
                   // gang-shared x undefined
1886
                   #pragma acc loop auto gang reduction(+:x)
1887
                   for (int i = 0; i < I; ++i) {
1888
                      // complex loop body
1889
                      x += arr[i]; // gang or loop-private x modified
1890
                   }
1891
                   // gang-shared x undefined
1892
                } // gang-shared x updated for gang or gang/seq reduction
1893
                // x = arr sum possibly times number of gangs
1894
        • At the following loop directive, x and z are private, so the loop reductions are not across
1895
           gangs even though the loop is gang-partitioned. Nevertheless, the reduction clause on the
1896
           loop directive is important as the loop is also vector-partitioned. These reductions are only
1897
           partial reductions relative to the full set of values computed by the loop, so the reduction
1898
           clause is needed on the parallel directive to reduce across gangs.
1899
                int x = 0, y = 0;
1900
                #pragma acc parallel copy(x) reduction(+:x,y)
1901
1902
                ł
                   int z = 0;
1903
                   #pragma acc loop gang vector reduction(+:x,z)
1904
                   for (int i = 0; i < I; ++i) {</pre>
1905
                      \mathbf{x} += \mathbf{1}; \mathbf{z} += \mathbf{2}; // \text{vector-private } \mathbf{x} \text{ and } \mathbf{z} \text{ modified}
1906
                   } // gang-private x and z updated for vector reduction (trivial 1-gang reduction)
1907
                   y += z; // gang-private y modified
1908
```

1909		} // gang-shared x and y updated for gang reduction
1910		// x = I; y = I * 2
1911	•	

1912

1913 2.10. Cache Directive

Summary The **cache** directive may appear at the top of (inside of) a loop. It specifies array elements or subarrays that should be fetched into the highest level of the cache for the body of the loop.

1917 **Syntax** In C and C++, the syntax of the **cache** directive is

#pragma acc cache([readonly:]var-list) new-line

¹⁹¹⁸ In Fortran, the syntax of the **cache** directive is

```
!$acc cache( [readonly:]var-list )
```

A *var* in a **cache** directive must be a single array element or a simple subarray. In C and C++, a simple subarray is an array name followed by an extended array range specification in brackets, with start and length, such as

arr[lower:length]

where the lower bound is a constant, loop invariant, or the **for** loop index variable plus or minus a constant or loop invariant, and the length is a constant.

¹⁹²⁴ In Fortran, a simple subarray is an array name followed by a comma-separated list of range specifi-¹⁹²⁵ cations in parentheses, with lower and upper bound subscripts, such as

arr(lower:upper, lower2:upper2)

The lower bounds must be constant, loop invariant, or the **do** loop index variable plus or minus a constant or loop invariant; moreover the difference between the corresponding upper and lower bounds must be a constant.

¹⁹²⁹ If the optional **readonly** modifier appears, then the implementation may assume that the data ¹⁹³⁰ referenced by any *var* in that directive is never written to within the applicable region.

1931 **Restrictions**

• If an array element or subarray is listed in a **cache** directive, all references to that array during execution of that loop iteration must not refer to elements of the array outside the index range specified in the **cache** directive.

See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in
 cache directives.

1937 2.11. Combined Constructs

Summary The combined OpenACC parallel loop, kernels loop, and serial loop constructs are shortcuts for specifying a loop construct nested immediately inside a parallel, kernels, or serial construct. The meaning is identical to explicitly specifying a parallel, kernels, or serial construct containing a loop construct. Any clause that is allowed on a parallel or loop construct is allowed on the parallel loop construct; any clause allowed on a kernels or loop construct is allowed on a kernels loop construct; and any clause allowed on a serial or loop construct is allowed on a serial loop construct.

1945 **Syntax** In C and C++, the syntax of the **parallel loop** construct is

#pragma acc parallel loop [clause-list] new-line for loop

¹⁹⁴⁶ In Fortran, the syntax of the **parallel loop** construct is

!\$acc parallel loop [clause-list]
 do loop
[!\$acc end parallel loop]

The associated structured block is the loop which must immediately follow the directive. Any of the **parallel** or **loop** clauses valid in a parallel region may appear.

¹⁹⁴⁹ In C and C++, the syntax of the **kernels loop** construct is

#pragma acc kernels loop [clause-list] new-line
 for loop

¹⁹⁵⁰ In Fortran, the syntax of the **kernels loop** construct is

!\$acc kernels loop [clause-list]
 do loop
[!\$acc end kernels loop]

¹⁹⁵¹ The associated structured block is the loop which must immediately follow the directive. Any of ¹⁹⁵² the **kernels** or **loop** clauses valid in a kernels region may appear.

¹⁹⁵³ In C and C++, the syntax of the **serial loop** construct is

#pragma acc serial loop [clause-list] new-line
 for loop

¹⁹⁵⁴ In Fortran, the syntax of the **serial loop** construct is

!\$acc serial loop [clause-list]
 do loop
[!\$acc end serial loop]

The associated structured block is the loop which must immediately follow the directive. Any of the **serial** or **loop** clauses valid in a serial region may appear.

A **private** or **reduction** clause on a combined construct is treated as if it appeared on the loop construct. In addition, a **reduction** clause on a combined construct implies a **copy** data clause for each reduction variable, unless a data clause for that variable appears on the combined construct.

1961 **Restrictions**

• The restrictions for the **parallel**, **kernels**, **serial**, and **loop** constructs apply.

1963 2.12. Atomic Construct

Summary An **atomic** construct ensures that a specific storage location is accessed and/or updated atomically, preventing simultaneous reading and writing by gangs, workers, and vector threads that could result in indeterminate values.

1967 **Syntax** In C and C++, the syntax of the **atomic** constructs is:

#pragma acc atomic [atomic-clause] new-line
 expression-stmt

1968 Or:

#pragma acc atomic update capture new-line
 structured-block

Where *atomic-clause* is one of **read**, **write**, **update**, or **capture**. The *expression-stmt* is an expression statement with one of the following forms:

1971 If the *atomic-clause* is **read**:

 $\mathbf{v} = \mathbf{x};$

1972 If the *atomic-clause* is write:

 $\mathbf{x} = expr;$

¹⁹⁷³ If the *atomic-clause* is **update** or no clause appears:

x++; x--; ++x; --x; x binop= expr; x = x binop expr; x = expr binop x;

1974 If the *atomic-clause* is **capture**:

v = x++;
v = x--;
v = ++x;
v = --x;
v = x binop = expr;
v = x = x binop expr;
v = x = expr binop x;

¹⁹⁷⁵ The *structured-block* is a structured block with one of the following forms:

{v = x; x binop= expr;}
{x binop= expr; v = x;}
{v = x; x = x binop expr;}
{v = x; x = expr binop x;}
{x = x binop expr; v = x;}
{x = expr binop x; v = x;}
{v = x; x = expr;}
{v = x; x ++;}
{v = x; ++x;}
{v = x; ++x;}
{v = x; ++x;}
{v = x; --x;}
{v = x; --x;}
{x --; v = x;}

¹⁹⁷⁶ In the preceding expressions:

```
• x and v (as applicable) are both l-value expressions with scalar type.
```

- During the execution of an atomic region, multiple syntactic occurrences of x must designate
 the same storage location.
- Neither of **v** and *expr* (as applicable) may access the storage location designated by **x**.
- Neither of \mathbf{x} and *expr* (as applicable) may access the storage location designated by \mathbf{v} .
- *expr* is an expression with scalar type.
- *binop* is one of +, *, -, /, &, ^, |, <<, or >>.
- *binop*, *binop*=, ++, and -- are not overloaded operators.

- The expression \mathbf{x} binop expr must be mathematically equivalent to \mathbf{x} binop (expr). This requirement is satisfied if the operators in expr have precedence greater than binop, or by using parentheses around expr or subexpressions of expr.
- The expression *expr binop* \mathbf{x} must be mathematically equivalent to (*expr*) *binop* \mathbf{x} . This requirement is satisfied if the operators in *expr* have precedence equal to or greater than *binop*, or by using parentheses around *expr* or subexpressions of *expr*.
- For forms that allow multiple occurrences of **x**, the number of times that **x** is evaluated is unspecified.

¹⁹⁹³ In Fortran the syntax of the **atomic** constructs is:

!\$acc atomic read capture-statement [!\$acc end atomic]

1994 Or

!\$acc atomic write
 write-statement
[!\$acc end atomic]

1995 Or

!\$acc atomic [update]
 update-statement
[!\$acc end atomic]

1996 Or

!\$acc atomic capture update-statement capture-statement !\$acc end atomic

1997 Or

!\$acc atomic capture capture-statement update-statement !\$acc end atomic

1998 Or

!\$acc atomic capture capture-statement write-statement !\$acc end atomic where *write-statement* has the following form (if *atomic-clause* is **write** or **capture**):

$\mathbf{x} = \mathbf{expr}$

where *capture-statement* has the following form (if *atomic-clause* is **capture** or **read**):

 $\mathbf{v} = \mathbf{x}$

and where *update-statement* has one of the following forms (if *atomic-clause* is **update**, **capture**, or no clause appears):

x = x operator expr x = expr operator x x = intrinsic_procedure_name(x, expr-list) x = intrinsic_procedure_name(expr-list, x)

²⁰⁰³ In the preceding statements:

- **x** and **v** (as applicable) are both scalar variables of intrinsic type.
- **x** must not be an allocatable variable.
- During the execution of an atomic region, multiple syntactic occurrences of **x** must designate the same storage location.
- None of **v**, *expr*, and *expr-list* (as applicable) may access the same storage location as **x**.
- None of **x**, *expr*, and *expr-list* (as applicable) may access the same storage location as **v**.
- *expr* is a scalar expression.
- *expr-list* is a comma-separated, non-empty list of scalar expressions. If *intrinsic_procedure_name* refers to **iand**, **ior**, or **ieor**, exactly one expression must appear in *expr-list*.
- *intrinsic_procedure_name* is one of max, min, iand, ior, or ieor. *operator* is one of +,
 *, -, /, .and., .or., .eqv., or .neqv..
- The expression **x** operator expr must be mathematically equivalent to **x** operator (expr). This requirement is satisfied if the operators in expr have precedence greater than operator, or by using parentheses around expr or subexpressions of expr.
- The expression *expr operator* \mathbf{x} must be mathematically equivalent to (*expr*) operator \mathbf{x} . This requirement is satisfied if the operators in *expr* have precedence equal to or greater than *operator*, or by using parentheses around *expr* or subexpressions of *expr*.
- *intrinsic_procedure_name* must refer to the intrinsic procedure name and not to other program entities.
- *operator* must refer to the intrinsic operator and not to a user-defined operator. All assignments must be intrinsic assignments.

• For forms that allow multiple occurrences of **x**, the number of times that **x** is evaluated is unspecified.

An **atomic** construct with the **read** clause forces an atomic read of the location designated by **x**. An **atomic** construct with the **write** clause forces an atomic write of the location designated by **x**.

An **atomic** construct with the **update** clause forces an atomic update of the location designated by **x** using the designated operator or intrinsic. Note that when no clause appears, the semantics are equivalent to **atomic update**. Only the read and write of the location designated by **x** are performed mutually atomically. The evaluation of *expr* or *expr-list* need not be atomic with respect to the read or write of the location designated by **x**.

An **atomic** construct with the **capture** clause forces an atomic update of the location designated 2035 by \mathbf{x} using the designated operator or intrinsic while also capturing the original or final value of 2036 the location designated by \mathbf{x} with respect to the atomic update. The original or final value of the 2037 location designated by \mathbf{x} is written into the location designated by \mathbf{v} depending on the form of the 2038 atomic construct structured block or statements following the usual language semantics. Only 2039 the read and write of the location designated by \mathbf{x} are performed mutually atomically. Neither the 2040 evaluation of *expr* or *expr-list*, nor the write to the location designated by \mathbf{v} , need to be atomic with 2041 respect to the read or write of the location designated by **x**. 2042

For all forms of the **atomic** construct, any combination of two or more of these **atomic** constructs enforces mutually exclusive access to the locations designated by **x**. To avoid race conditions, all accesses of the locations designated by **x** that could potentially occur in parallel must be protected with an **atomic** construct.

Atomic regions do not guarantee exclusive access with respect to any accesses outside of atomic regions to the same storage location \mathbf{x} even if those accesses occur during the execution of a reduction clause.

If the storage location designated by \mathbf{x} is not size-aligned (that is, if the byte alignment of \mathbf{x} is not a multiple of the size of \mathbf{x}), then the behavior of the atomic region is implementation-defined.

2052 **Restrictions**

• All atomic accesses to the storage locations designated by **x** throughout the program are required to have the same type and type parameters.

• Storage locations designated by **x** must be less than or equal in size to the largest available native atomic operator width.

2057 2.13. Declare Directive

Summary A **declare** directive is used in the declaration section of a Fortran subroutine, function, or module, or following a variable declaration in C or C++. It can specify that a *var* is to be allocated in device memory for the duration of the implicit data region of a function, subroutine or program, and specify whether the data values are to be transferred from local memory to device memory upon entry to the implicit data region, and from device memory to local memory upon exit from the implicit data region. These directives create a visible device copy of the *var*. 2064 **Syntax** In C and C++, the syntax of the **declare** directive is:

#pragma acc declare clause-list new-line

²⁰⁶⁵ In Fortran the syntax of the **declare** directive is:

!\$acc declare clause-list

²⁰⁶⁶ where *clause* is one of the following:

```
copy( var-list )
copyin( [readonly:]var-list )
copyout( var-list )
create( var-list )
present( var-list )
deviceptr( var-list )
device_resident( var-list )
link( var-list )
```

The associated region is the implicit region associated with the function, subroutine, or program in which the directive appears. If the directive appears in the declaration section of a Fortran *module* subprogram or in a C or C++ global scope, the associated region is the implicit region for the whole program. The **copy**, **copyin**, **copyout**, **present**, and **deviceptr** data clauses are described in Section 2.7 Data Clauses.

2072 **Restrictions**

2073 2074	• A declare directive must appear in the same scope as any <i>var</i> in any of the data clauses on the directive.
2075	• At least one clause must appear on a declare directive.
2076 2077	• A <i>var</i> in a declare declare must be a variable or array name, or a Fortran <i>common block</i> name between slashes.
2078 2079	• A <i>var</i> may appear at most once in all the clauses of declare directives for a function, subroutine, program, or module.
2080	• In Fortran, assumed-size dummy arrays may not appear in a declare directive.
2081 2082	• In Fortran, pointer arrays may appear, but pointer association is not preserved in device memory.
2083 2084	• In a Fortran <i>module</i> declaration section, only create , copyin , device_resident , and link clauses are allowed.
2085 2086	• In C or C++ global scope, only create, copyin, deviceptr, device_resident and link clauses are allowed.
2087 2088	• C and C++ <i>extern</i> variables may only appear in create, copyin, deviceptr, device_resident and link clauses on a declare directive.

- In C and C++, only global and *extern* variables may appear in a link clause. In Fortran, only *module* variables and *common* block names (enclosed in slashes) may appear in a link clause.
- In C or C++, a longjmp call in the region must return to a set jmp call within the region.
- In C++, an exception thrown in the region must be handled within the region.
- See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional dummy arguments in data clauses, including **device_resident** clauses.

2096 2.13.1. device_resident clause

Summary The **device_resident** clause specifies that the memory for the named variables should be allocated in the current device memory and not in local memory. The host may not be able to access variables in a **device_resident** clause. The accelerator data lifetime of global variables or common blocks that appear in a **device_resident** clause is the entire execution of the program.

In Fortran, if the variable has the Fortran *allocatable* attribute, the memory for the variable will be allocated in and deallocated from the current device memory when the host thread executes an **allocate** or **deallocate** statement for that variable, if the current device is a non-shared memory device. If the variable has the Fortran *pointer* attribute, it may be allocated or deallocated by the host in the current device memory, or may appear on the left hand side of a pointer assignment statement, if the right hand side variable itself appears in a **device_resident** clause.

In Fortran, the argument to a **device_resident** clause may be a *common block* name enclosed in slashes; in this case, all declarations of the common block must have a matching **device_resident** clause. In this case, the *common block* will be statically allocated in device memory, and not in local memory. The *common block* will be available to accelerator routines; see Section 2.15 Procedure Calls in Compute Regions.

In a Fortran *module* declaration section, a *var* in a **device_resident** clause will be available to accelerator subprograms.

In C or C++ global scope, a *var* in a **device_resident** clause will be available to accelerator routines. A C or C++ *extern* variable may appear in a **device_resident** clause only if the actual declaration and all *extern* declarations are also followed by **device_resident** clauses.

2118 2.13.2. create clause

2119 For data in shared memory, no action is taken.

For data not in shared memory, the **create** clause on a **declare** directive behaves as follows, for each *var* in *var-list*:

• At entry to an implicit data region where the **declare** directive appears:

- If *var* is present, a *present increment* action with the structured reference counter is performed. If *var* is a pointer reference, an *attach* action is performed.
- Otherwise, a *create* action with the structured reference counter is performed. If *var* is a pointer reference, an *attach* action is performed.

• At exit from an implicit data region where the **declare** directive appears:

- If *var* is not present in the current device memory, a runtime error is issued.
- Otherwise, a *present decrement* action with the structured reference counter is per formed. If *var* is a pointer reference, a *detach* action is performed. If both structured and dynamic reference counters are zero, a *delete* action is performed.

²¹³² If the **declare** directive appears in a global context, then the data in *var-list* is statically allocated ²¹³³ in device memory and the structured reference counter is set to one.

²¹³⁴ In Fortran, if a variable *var* in *var-list* has the Fortran *allocatable* or *pointer* attribute, then:

- An **allocate** statement for *var* will allocate memory in both local memory as well as in the current device memory, for a non-shared memory device, and the dynamic reference counter will be set to one.
- A **deallocate** statement for *var* will deallocate memory from both local memory as well as the current device memory, for a non-shared memory device, and the dynamic reference counter will be set to zero. If the structured reference counter is not zero, a runtime error is issued.
- In Fortran, if a variable *var* in *var-list* has the Fortran *pointer* attribute, then it may appear on the left hand side of a pointer assignment statement, if the right hand side variable itself appears in a **create** clause.

2145 **2.13.3. link clause**

The **link** clause is used for large global host static data that is referenced within an accelerator 2146 routine and that should have a dynamic data lifetime on the device. The **link** clause specifies that 2147 only a global link for the named variables should be statically created in accelerator memory. The 2148 host data structure remains statically allocated and globally available. The device data memory will 2149 be allocated only when the global variable appears on a data clause for a **data** construct, compute 2150 construct, or **enter data** directive. The arguments to the **link** clause must be global data. In C 2151 or C++, the link clause must appear at global scope, or the arguments must be extern variables. 2152 In Fortran, the **link** clause must appear in a *module* declaration section, or the arguments must be 2153 common block names enclosed in slashes. A common block that is listed in a **link** clause must be 2154 declared with the same size in all program units where it appears. A **declare link** clause must be 2155 visible everywhere the global variables or common block variables are explicitly or implicitly used 2156 in a data clause, compute construct, or accelerator routine. The global variable or common block 2157 variables may be used in accelerator routines. The accelerator data lifetime of variables or common 2158 blocks that appear in a **link** clause is the data region that allocates the variable or common block 2159 with a data clause, or from the execution of the **enter data** directive that allocates the data until 2160 an **exit data** directive deallocates it or until the end of the program. 2161

2162 2.14. Executable Directives

2163 **2.14.1. Init Directive**

Summary The **init** directive tells the runtime to initialize the runtime for that device type. This can be used to isolate any initialization cost from the computational cost, when collecting performance statistics. If no device type appears all devices will be initialized. An **init** directive may be used in place of a call to the **acc_init** runtime API routine, as described in Section 3.2.7.

2168 **Syntax** In C and C++, the syntax of the init directive is:

#pragma acc init [clause-list] new-line

2169 In Fortran the syntax of the **init** directive is:

!\$acc init [clause-list]

²¹⁷⁰ where *clause* is one of the following:

device_type (device-type-list)
device_num (int-expr)
if (condition)

2171 device_type clause

The **device_type** clause specifies the type of device that is to be initialized in the runtime. If the **device_type** clause appears, then the *acc-current-device-type-var* for the current thread is set to the argument value. If no **device_num** clause appears then all devices of this type are initialized.

2175 device_num clause

The **device_num** clause specifies the device id to be initialized. If the **device_num** clause appears, then the *acc-current-device-num-var* for the current thread is set to the argument value. If no **device_type** clause appears, then the specified device id will be initialized for all available device types.

2180 if clause

The **if** clause is optional; when there is no **if** clause, the implementation will generate code to perform the initialization unconditionally. When an **if** clause appears, the implementation will generate code to conditionally perform the initialization only when the *condition* evaluates to nonzero in C or C++, or .**true**. in Fortran.

2185 **Restrictions**

- This directive may not be called within a compute region.
- If the device type specified is not available, the behavior is implementation-defined; in particular, the program may abort.
- If the directive is called more than once without an intervening **acc_shutdown** call or **shutdown** directive, with a different value for the device type argument, the behavior is implementation-defined.
- If some accelerator regions are compiled to only use one device type, using this directive with a different device type may produce undefined behavior.

2194 2.14.2. Shutdown Directive

Summary The **shutdown** directive tells the runtime to shut down the connection to the given accelerator, and free any runtime resources. A **shutdown** directive may be used in place of a call to the **acc_shutdown** runtime API routine, as described in Section 3.2.8.

2198 **Syntax** In C and C++, the syntax of the **shutdown** directive is:

#pragma acc shutdown [clause-list] new-line

2199 In Fortran the syntax of the **shutdown** directive is:

!\$acc shutdown [clause-list]

where *clause* is one of the following:

device_type (device-type-list)
device_num (int-expr)
if (condition)

2201 device_type clause

- ²²⁰² The **device_type** clause specifies the type of device that is to be disconnected from the runtime.
- ²²⁰³ If no **device_num** clause appears then all devices of this type are disconnected.

2204 device_num clause

- ²²⁰⁵ The **device_num** clause specifies the device id to be disconnected.
- ²²⁰⁶ If no clauses appear then all available devices will be disconnected.
2207 if clause

The **if** clause is optional; when there is no **if** clause, the implementation will generate code to perform the shutdown unconditionally. When an **if** clause appears, the implementation will generate code to conditionally perform the shutdown only when the *condition* evaluates to nonzero in C or C++, or .**true**. in Fortran.

2212 **Restrictions**

• This directive may not be used during the execution of a compute region.

2214 **2.14.3. Set Directive**

Summary The **set** directive provides a means to modify internal control variables using directives. Each form of the **set** directive is functionally equivalent to a matching runtime API routine.

2217 **Syntax** In C and C++, the syntax of the **set** directive is:

#pragma acc set [clause-list] new-line

²²¹⁸ In Fortran the syntax of the **set** directive is:

!\$acc set [clause-list]

2219 where *clause* is one of the following

default_async (int-expr)
device_num (int-expr)
device_type (device-type-list)
if(condition)

default_async clause

The **default_async** clause specifies the asynchronous queue that should be used if no queue appears and changes the value of *acc-default-async-var* for the current thread to the argument value. If the value is **acc_async_default**, the value of *acc-default-async-var* will revert to the initial value, which is implementation-defined. A **set default_async** directive is functionally equivalent to a call to the **acc_set_default_async** runtime API routine, as described in Section 3.2.22.

2227 device_num clause

The **device_num** clause specifies the device number to set as the default device for accelerator regions and changes the value of *acc-current-device-num-var* for the current thread to the argument value. If the value of **device_num** argument is negative, the runtime will revert to the default behavior, which is implementation-defined. A **set device_num** directive is functionally equivalent to the **acc_set_device_num** runtime API routine, as described in Section 3.2.4.

2233 device_type clause

The **device_type** clause specifies the device type to set as the default device type for accelerator regions and sets the value of *acc-current-device-type-var* for the current thread to the argument value. If the value of the **device_type** argument is zero or the clause does not appear, the selected device number will be used for all attached accelerator types. A **set device_type** directive is functionally equivalent to a call to the **acc_set_device_type** runtime API routine, as described in Section 3.2.2.

2240 if clause

The **if** clause is optional; when there is no **if** clause, the implementation will generate code to perform the set operation unconditionally. When an **if** clause appears, the implementation will generate code to conditionally perform the set operation only when the *condition* evaluates to nonzero in C or C++, or **.true.** in Fortran.

2245 **Restrictions**

- This directive may not be used within a compute region.
- Passing default_async the value of acc_async_noval has no effect.
- Passing **default_async** the value of **acc_async_sync** will cause all asynchronous directives in the default asynchronous queue to become synchronous.
- Passing **default_async** the value of **acc_async_default** will restore the default asynchronous queue to the initial value, which is implementation-defined.
- If the value of **device_num** is larger than the maximum supported value for the given type, the behavior is implementation-defined.
- At least one **default_async**, **device_num**, or **device_type** clause must appear.
- Two instances of the same clause may not appear on the same directive.

2256 2.14.4. Update Directive

Summary The update directive is used during the lifetime of accelerator data to update *vars* in local memory with values from the corresponding data in device memory, or to update *vars* in device memory with values from the corresponding data in local memory.

2260 **Syntax** In C and C++, the syntax of the update directive is:

#pragma acc update clause-list new-line

²²⁶¹ In Fortran the syntax of the **update** data directive is:

!\$acc update clause-list

where *clause* is one of the following:

```
async [( int-expr )]
wait [( wait-argument )]
device_type( device-type-list )
if( condition )
if_present
self( var-list )
host( var-list )
device( var-list )
```

Multiple subarrays of the same array may appear in a *var-list* of the same or different clauses on the same directive. The effect of an **update** clause is to copy data from device memory to local memory for **update self**, and from local memory to device memory for **update device**. The updates are done in the order in which they appear on the directive.

2267 **Restrictions**

• At least one **self**, **host**, or **device** clause must appear on an **update** directive.

2269 self clause

The **self** clause specifies that the *vars* in *var-list* are to be copied from the current device memory to local memory for data not in shared memory. For data in shared memory, no action is taken. An **update** directive with the **self** clause is equivalent to a call to the **acc_update_self** routine, described in Section 3.2.31.

2273 described in Section 5.2.51

2274 host clause

²²⁷⁵ The **host** clause is a synonym for the **self** clause.

2276 device clause

2277 The **device** clause specifies that the *vars* in *var-list* are to be copied from local memory to the cur-

²²⁷⁸ rent device memory, for data not in shared memory. For data in shared memory, no action is taken.

- 2279 An update directive with the device clause is equivalent to a call to the acc_update_device
- routine, described in Section 3.2.30.

2281 if clause

The **if** clause is optional; when there is no **if** clause, the implementation will generate code to perform the updates unconditionally. When an **if** clause appears, the implementation will generate code to conditionally perform the updates only when the *condition* evaluates to nonzero in C or C++, or **.true.** in Fortran.

2286 async clause

²²⁸⁷ The **async** clause is optional; see Section 2.16 Asynchronous Behavior for more information.

2288 wait clause

²²⁸⁹ The **wait** clause is optional; see Section 2.16 Asynchronous Behavior for more information.

2290 if_present clause

When an **if_present** clause appears on the directive, no action is taken for a *var* which appears in *var-list* that is not present in the current device memory. When no **if_present** clause appears, all *vars* in a **device** or **self** clause must be present in the current device memory, and an implementation may halt the program with an error message if some data is not present.

2295 **Restrictions**

- The **update** directive is executable. It must not appear in place of the statement following an *if*, *while*, *do*, *switch*, or *label* in C or C++, or in place of the statement following a logical *if* in Fortran.
- If no **if_present** clause appears on the directive, each *var* in *var-list* must be present in the current device memory.
- Only the **async** and **wait** clauses may follow a **device_type** clause.
- At most one **if** clause may appear. In Fortran, the condition must evaluate to a scalar logical value; in C or C++, the condition must evaluate to a scalar integer value.
- Noncontiguous subarrays may appear. It is implementation-specific whether noncontiguous regions are updated by using one transfer for each contiguous subregion, or whether the non-contiguous data is packed, transferred once, and unpacked, or whether one or more larger subarrays (no larger than the smallest contiguous region that contains the specified subarray) are updated.
- In C and C++, a member of a struct or class may appear, including a subarray of a member.
 Members of a subarray of struct or class type may not appear.
- In C and C++, if a subarray notation is used for a struct member, subarray notation may not be used for any parent of that struct member.
- In Fortran, members of variables of derived type may appear, including a subarray of a member. Members of subarrays of derived type may not appear.

- In Fortran, if array or subarray notation is used for a derived type member, array or subarray notation may not be used for a parent of that derived type member.
- See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in
 self, host, and device clauses.

2319 2.14.5. Wait Directive

2320 See Section 2.16 Asynchronous Behavior for more information.

2321 2.14.6. Enter Data Directive

2322 See Section 2.6.6 Enter Data and Exit Data Directives for more information.

2323 2.14.7. Exit Data Directive

2324 See Section 2.6.6 Enter Data and Exit Data Directives for more information.

2325 2.15. Procedure Calls in Compute Regions

This section describes how routines are compiled for an accelerator and how procedure calls are compiled in compute regions. See Section 2.17 Fortran Optional Arguments for discussion of Fortran optional arguments in procedure calls inside compute regions.

2329 2.15.1. Routine Directive

Summary The **routine** directive is used to tell the compiler to compile a given procedure or a C++ *lambda* for an accelerator as well as for the host. In a file or routine with a procedure call, the **routine** directive tells the implementation the attributes of the procedure when called on the accelerator.

2334 **Syntax** In C and C++, the syntax of the **routine** directive is:

#pragma acc routine clause-list new-line
#pragma acc routine (name) clause-list new-line

In C and C++, the **routine** directive without a name may appear immediately before a function definition, a C++ *lambda*, or just before a function prototype and applies to that immediately following function or prototype. The **routine** directive with a name may appear anywhere that a function prototype is allowed and applies to the function or the C++ *lambda* in that scope with that name, but must appear before any definition or use of that function.

²³⁴⁰ In Fortran the syntax of the **routine** directive is:

!\$acc routine clause-list
!\$acc routine (name) clause-list

In Fortran, the **routine** directive without a name may appear within the specification part of a subroutine or function definition, or within an interface body for a subroutine or function in an interface block, and applies to the containing subroutine or function. The **routine** directive with a name may appear in the specification part of a subroutine, function or module, and applies to the named subroutine or function.

A C or C++ function or Fortran subprogram compiled with the **routine** directive for an accelerator is called an *accelerator routine*.

²³⁴⁸ If an *accelerator routine* is a C++ *lambda*, the associated function will be compiled for both the ²³⁴⁹ accelerator and the host.

If a *lambda* is called in a compute region and it is not an *accelerator routine*, then the *lambda* is treated as if its name appears in the name list of a **routine** directive with **seq** clause. If *lambda* is defined in an *accelerator routine* that has a **nohost** clause then the *lambda* is treated as if its name appears in the name list of a **routine** directive with a **nohost** clause.

²³⁵⁴ The *clause* is one of the following:

gang
worker
vector
seq
bind(name)
bind(string)
device_type(device-type-list)
nohost

A gang, worker, vector, or seq clause specifies the *level of parallelism* in the routine.

2356 gang clause

The **gang** clause specifies that the procedure contains, may contain, or may call another procedure that contains a loop with a **gang** clause. A call to this procedure must appear in code that is executed in *gang-redundant* mode, and all gangs must execute the call. For instance, a procedure with a **routine gang** directive may not be called from within a loop that has a **gang** clause. Only one of the **gang**, **worker**, **vector** and **seq** clauses may appear for each device type.

2362 worker clause

The **worker** clause specifies that the procedure contains, may contain, or may call another procedure that contains a loop with a **worker** clause, but does not contain nor does it call another procedure that contains a loop with the **gang** clause. A loop in this procedure with an **auto** clause may be selected by the compiler to execute in **worker** or **vector** mode. A call to this procedure must appear in code that is executed in *worker-single* mode, though it may be in *gang-redundant* or *gang-partitioned* mode. For instance, a procedure with a **routine worker** directive may be called from within a loop that has the **gang** clause, but not from within a loop that has the **worker** clause. Only one of the **gang**, **worker**, **vector**, and **seq** clauses may appear for each device type.

2372 vector clause

The **vector** clause specifies that the procedure contains, may contain, or may call another pro-2373 cedure that contains a loop with the **vector** clause, but does not contain nor does it call another 2374 procedure that contains a loop with either a gang or worker clause. A loop in this procedure with 2375 an **auto** clause may be selected by the compiler to execute in **vector** mode, but not **worker** 2376 mode. A call to this procedure must appear in code that is executed in *vector-single* mode, though 2377 it may be in gang-redundant or gang-partitioned mode, and in worker-single or worker-partitioned 2378 mode. For instance, a procedure with a **routine vector** directive may be called from within 2379 a loop that has the **gang** clause or the **worker** clause, but not from within a loop that has the 2380 2381 vector clause. Only one of the gang, worker, vector, and seq clauses may appear for each device type. 2382

2383 seq clause

The **seq** clause specifies that the procedure does not contain nor does it call another procedure that contains a loop with a **gang**, **worker**, or **vector** clause. A loop in this procedure with an **auto** clause will be executed in **seq** mode. A call to this procedure may appear in any mode. Only one of the **gang**, **worker**, **vector** and **seq** clauses may appear for each device type.

2388 bind clause

The **bind** clause specifies the name to use when calling the procedure on a device other than the host. If the name is specified as an identifier, it is called as if that name were specified in the language being compiled. If the name is specified as a string, the string is used for the procedure name unmodified. A **bind** clause on a procedure definition behaves as if it had appeared on a declaration by changing the name used to call the function on a device other than the host; however, the procedure is not compiled for the device with either the original name or the name in the **bind** clause.

If there is both a Fortran bind and an acc **bind** clause for a procedure definition then a call on the host will call the Fortran bound name and a call on another device will call the name in the **bind** clause.

2399 device_type clause

²⁴⁰⁰ The **device_type** clause is described in Section 2.4 Device-Specific Clauses.

nohost clause 2401

The **nohost** tells the compiler not to compile a version of this procedure for the host. All calls 2402 to this procedure must appear within compute regions. If this procedure is called from other pro-2403 cedures, those other procedures must also have a matching **routine** directive with the **nohost** 2404 clause. 2405

Restrictions 2406

2407 2408	• Only the gang, worker, vector, seq and bind clauses may follow a device_type clause.
2409 2410 2411 2412	• At least one of the (gang, worker, vector, or seq) clauses must appear on the construct. If the device_type clause appears on the routine directive, a default level of parallelism clause must appear before the device_type clause, or a level of parallelism clause must appear following each device_type clause on the directive.
2413 2414	• In C and C++, function static variables are not supported in functions to which a routine directive applies.
2415 2416	• In Fortran, variables with the <i>save</i> attribute, either explicitly or implicitly, are not supported in subprograms to which a routine directive applies.
2417	• A bind clause may not bind to a routine name that has a visible bind clause.
2418 2419	• If a function or subroutine has a bind clause on both the declaration and the definition then they both must bind to the same name.

2.15.2. Global Data Access 2420

C or C++ global, file static, or extern variables or array, and Fortran module or common block vari-2421 ables or arrays, that are used in accelerator routines must appear in a declare directive in a **create**, 2422 copyin, device_resident or link clause. If the data appears in a device_resident 2423 clause, the **routine** directive for the procedure must include the **nohost** clause. If the data ap-2424 pears in a link clause, that data must have an active accelerator data lifetime by virtue of appearing 2425 in a data clause for a data construct, compute construct, or enter data directive. 2426

2.16. Asynchronous Behavior 2427

This section describes the **async** clause and the behavior of programs that use asynchronous data 2428 movement and compute constructs, and asynchronous API routines. 2429

2.16.1. async clause 2430

2431 The async clause may appear on a parallel, kernels, or serial construct, or an enter data, exit data, update, or wait directive. In all cases, the async clause is optional. When 2432 there is no **async** clause on a compute or data construct, the local thread will wait until the compute 2433 construct or data operations for the current device are complete before executing any of the code 2434

that follows. When there is no **async** clause on a **wait** directive, the local thread will wait until all operations on the appropriate asynchronous activity queues for the current device are complete. When there is an **async** clause, the parallel, kernels, or serial region or data operations may be processed asynchronously while the local thread continues with the code following the construct or directive.

The **async** clause may have a single *async-argument*, where an *async-argument* is a nonnegative scalar integer expression (*int* for C or C++, *integer* for Fortran), or one of the special values defined below. The behavior with a negative *async-argument*, except the special values defined below, is implementation-defined. The value of the *async-argument* may be used in a **wait** directive, **wait** clause, or various runtime routines to test or wait for completion of the operation.

Two special values for *async-argument* are defined in the C and Fortran header files and the Fortran openacc module. These are negative values, so as not to conflict with a user-specified nonnegative *async-argument*. An **async** clause with the *async-argument* **acc_async_noval** will behave the same as if the **async** clause had no argument. An **async** clause with the *async-argument* **acc_async_sync** will behave the same as if no **async** clause appeared.

The *async-value* of any operation is the value of the *async-argument*, if it appears, or the value 2450 of acc-default-async-var if it is acc_async_noval or if the async clause had no value, or 2451 **acc_async_sync** if no **async** clause appeared. If the current device supports asynchronous 2452 operation with one or more device activity queues, the async-value is used to select the queue on 2453 the current device onto which to enqueue an operation. The properties of the current device and the 2454 implementation will determine how many actual activity queues are supported, and how the async-2455 *value* is mapped onto the actual activity queues. Two asynchronous operations with the same current 2456 device and the same *async-value* will be enqueued onto the same activity queue, and therefore will 2457 be executed on the device in the order they are encountered by the local thread. Two asynchronous 2458 operations with different async-values may be enqueued onto different activity queues, and therefore 2459 may be executed on the device in either order relative to each other. If there are two or more host 2460 threads executing and sharing the same device, two asynchronous operations with the same *async*-2461 *value* will be enqueued on the same activity queue. If the threads are not synchronized with respect 2462 to each other, the operations may be enqueued in either order and therefore may execute on the 2463 device in either order. Asynchronous operations enqueued to difference devices may execute in any 2464 order, regardless of the async-value used for each. 2465

2466 **2.16.2. wait clause**

The wait clause may appear on a parallel, kernels, or serial construct, or an enter 2467 data, exit data, or update directive. In all cases, the wait clause is optional. When there 2468 is no wait clause, the associated compute or update operations may be enqueued or launched or 2469 executed immediately on the device. If there is an argument to the wait clause, it must be a wait-2470 argument (See 2.16.3). The compute, data, or update operation may not be launched or executed 2471 until all operations enqueued up to this point by this thread on the associated asynchronous device 2472 activity queues have completed. One legal implementation is for the local thread to wait for all 2473 the associated asynchronous device activity queues. Another legal implementation is for the local 2474 thread to enqueue the compute, data, or update operation in such a way that the operation will 2475 not start until the operations enqueued on the associated asynchronous device activity queues have 2476 completed. 2477

2478 2.16.3. Wait Directive

2479 Summary The wait directive causes the local thread or a device activity queue on the current
2480 device to wait for completion of asynchronous operations, such as an accelerator parallel, kernels,
2481 or serial region or an update directive.

2482 **Syntax** In C and C++, the syntax of the **wait** directive is:

#pragma acc wait [(wait-argument)] [clause-list] new-line

2483 In Fortran the syntax of the **wait** directive is:

!\$acc wait [(wait-argument)][clause-list]

²⁴⁸⁴ where *clause* is:

async [(int-expr)]
if(condition)

²⁴⁸⁵ The wait argument, if it appears, must be a *wait-argument* where *wait-argument* is:

[devnum : int-expr :] [queues :] int-expr-list

If there is no wait argument and no **async** clause, the local thread will wait until all operations
enqueued by this thread on any activity queue on the current device have completed.

If there are one or more *int-expr* expressions and no **async** clause, the local thread will wait until all operations enqueued by this thread on each of the associated device activity queues have completed. If a **devnum** modifier exists in the *wait-argument* then the device activity queues in the *int-expr* expressions apply to the queues on that device number of the current device type. If no **devnum** modifier exits then the expressions apply to the current device. It is an error to specify a device number that is not between 0 and the number of available devices of the current device type minus 1.

²⁴⁹⁵ The **queues** modifier within a *wait-argument* is optional to improve clarity of the expression list.

If there are two or more threads executing and sharing the same device, a **wait** directive with no async clause will cause the local thread to wait until all of the appropriate asynchronous operations previously enqueued by that thread have completed. To guarantee that operations have been enqueued by other threads requires additional synchronization between those threads. There is no guarantee that all the similar asynchronous operations initiated by other threads will have completed.

If there is an **async** clause, no new operation may be launched or executed on the **async** activity queue on the current device until all operations enqueued up to this point by this thread on the asynchronous activity queues associated with the wait argument have completed. One legal implementation is for the local thread to wait for all the associated asynchronous device activity queues. Another legal implementation is for the thread to enqueue a synchronization operation in such a way that no new operation will start until the operations enqueued on the associated asynchronous device activity queues have completed.

The **if** clause is optional; when there is no **if** clause, the implementation will generate code to perform the wait operation unconditionally. When an **if** clause appears, the implementation will generate code to conditionally perform the wait operation only when the *condition* evaluates to nonzero in C or C++, or **.true.** in Fortran.

A wait directive is functionally equivalent to a call to one of the acc_wait, acc_wait_async,

acc_wait_all or acc_wait_all_async runtime API routines, as described in Sections 3.2.13, 3.2.15, 3.2.17 and 3.2.19.

2515 **Restrictions**

• The *int-expr* that appears in a **devnum** modifier must be a legal device number of the current device type.

2518 2.17. Fortran Optional Arguments

This section refers to the Fortran intrinsic function **PRESENT**. A call to the Fortran intrinsic function **PRESENT (arg)** returns **.true.**, if **arg** is an optional dummy argument and an actual argument for **arg** was present in the argument list of the call site. This should not be confused with the OpenACC **present** data clause.

The appearance of a Fortran optional argument **arg** as a *var* in any of the following clauses has no effect at runtime if **PRESENT (arg)** is **.false.**:

• in data clauses on compute and **data** constructs;

• in data clauses on **enter data** and **exit data** directives;

• in data and **device_resident** clauses on **declare** directives;

- in use_device clauses on host_data directives;
- in self, host, and device clauses on update directives.

The appearance of a Fortran optional argument **arg** in the following situations may result in undefined behavior if **PRESENT (arg)** is **.false**. when the associated construct is executed:

• as a *var* in **private**, **firstprivate**, and **reduction** clauses;

• as a *var* in **cache** directives;

• as part of an expression in any clause or directive.

A call to the Fortran intrinsic function **PRESENT** behaves the same way in a compute construct or an accelerator routine as on the host. The function call **PRESENT (arg)** must return the same value in a compute construct as **PRESENT (arg)** would outside of the compute construct. If a Fortran optional argument **arg** appears as an actual argument in a procedure call in a compute construct or an accelerator routine, and the associated dummy argument **subarg** also has the **optional** attribute, then **PRESENT (subarg)** returns the same value as **PRESENT (subarg)** would when executed on the host.

2542 3. Runtime Library

This chapter describes the OpenACC runtime library routines that are available for use by programmers. Use of these routines may limit portability to systems that do not support the OpenACC API. Conditional compilation using the **_OPENACC** preprocessor variable may preserve portability.

²⁵⁴⁶ This chapter has two sections:

- Runtime library definitions
- Runtime library routines
- ²⁵⁴⁹ There are four categories of runtime routines:
- Device management routines, to get the number of devices, set the current device, and so on.
- Asynchronous queue management, to synchronize until all activities on an async queue are complete, for instance.
- Device test routine, to test whether this statement is executing on the device or not.
- Data and memory management, to manage memory allocation or copy data between memories.

2556 3.1. Runtime Library Definitions

In C and C++, prototypes for the runtime library routines described in this chapter are provided in a header file named **openacc.h**. All the library routines are *extern* functions with "C" linkage. This file defines:

- The prototypes of all routines in the chapter.
- Any datatypes used in those prototypes, including an enumeration type to describe the supported device types.
- The values of acc_async_noval, acc_async_sync, and acc_async_default.
- In Fortran, interface declarations are provided in a Fortran module named openacc. The openacc
 module defines:
- The integer parameter **openacc_version** with a value *yyyymm* where *yyyy* and *mm* are the year and month designations of the version of the Accelerator programming model supported. This value matches the value of the preprocessor variable **_OPENACC**.
- Interfaces for all routines in the chapter.
- Integer parameters to define integer kinds for arguments to and return values for those routines.

• Integer parameters to describe the supported device types.

```
• Integer parameters to define the values of acc_async_noval, acc_async_sync, and
acc_async_default.
```

Many of the routines accept or return a value corresponding to the type of device. In C and C++, the 2575 datatype used for device type values is **acc_device_t**; in Fortran, the corresponding datatype 2576 is **integer** (kind=acc_device_kind). The possible values for device type are implemen-2577 tation specific, and are defined in the C or C++ include file **openacc.h** and the Fortran module 2578 openacc. Four values are always supported: acc_device_none, acc_device_default, 2579 acc_device_host and acc_device_not_host. For other values, look at the appropriate 2580 files included with the implementation, or read the documentation for the implementation. The 2581 value **acc** device default will never be returned by any function; its use as an argument will 2582 tell the runtime library to use the default device type for that implementation. 2583

3.2. Runtime Library Routines

In this section, for the C and C++ prototypes, pointers are typed **h_void*** or **d_void*** to designate a host memory address or device memory address, when these calls are executed on the host, as if the following definitions were included:

```
#define h_void void
#define d_void void
```

²⁵⁸⁸ Except for **acc_on_device**, these routines are only available on the host.

2589 3.2.1. acc_get_num_devices

Summary The **acc_get_num_devices** routine returns the number of devices of the given type available.

2592 Format

```
C or C++:
```

```
int acc_get_num_devices( acc_device_t );
```

Fortran:

```
integer function acc_get_num_devices( devicetype )
    integer(acc_device_kind) :: devicetype
```

Description The acc_get_num_devices routine returns the number of devices of the given type available. The argument tells what kind of device to count.

2595 **Restrictions**

• This routine may not be called within a compute region.

2597 3.2.2. acc_set_device_type

Summary The **acc_set_device_type** routine tells the runtime which type of device to use when executing a compute region and sets the value of *acc-current-device-type-var*. This is useful when the implementation allows the program to be compiled to use more than one type of device.

2601 Format

C or C++:

```
void acc_set_device_type( acc_device_t );
```

Fortran:

```
subroutine acc_set_device_type( devicetype )
integer(acc_device_kind) :: devicetype
```

Description The acc_set_device_type routine tells the runtime which type of device to use among those available and sets the value of *acc-current-device-type-var* for the current thread. A call to acc_set_device_type is functionally equivalent to a set device_type directive with the matching device type argument, as described in Section 2.14.3.

2606 **Restrictions**

• If the device type specified is not available, the behavior is implementation-defined; in particular, the program may abort.

• If some compute regions are compiled to only use one device type, calling this routine with a different device type may produce undefined behavior.

²⁶¹¹ 3.2.3. acc_get_device_type

Summary The **acc_get_device_type** routine returns the value of *acc-current-device-typevar*, which is the device type of the current device. This is useful when the implementation allows the program to be compiled to use more than one type of device.

2615 Format

```
C or C++:
```

```
acc_device_t acc_get_device_type( void );
```

Fortran:

```
function acc_get_device_type()
integer(acc_device_kind) :: acc_get_device_type
```

Description The acc_get_device_type routine returns the value of *acc-current-device-type-var* for the current thread to tell the program what type of device will be used to run the next compute region, if one has been selected. The device type may have been selected by the program with an acc_set_device_type call, with an environment variable, or by the default behavior of the program.

2621 **Restrictions**

• If the device type has not yet been selected, the value **acc_device_none** may be returned.

2623 3.2.4. acc_set_device_num

Summary The **acc_set_device_num** routine tells the runtime which device to use and sets the value of *acc-current-device-num-var*.

2626 Format

```
C or C++:
    void acc_set_device_num( int, acc_device_t );
```

Fortran:

```
subroutine acc_set_device_num( devicenum, devicetype )
integer :: devicenum
integer(acc_device_kind) :: devicetype
```

Description The acc_set_device_num routine tells the runtime which device to use among those available of the given type for compute or data regions in the current thread and sets the value of *acc-current-device-num-var*. If the value of **devicenum** is negative, the runtime will revert to its default behavior, which is implementation-defined. If the value of the second argument is zero, the selected device number will be used for all device types. A call to acc_set_device_num is functionally equivalent to a set device_num directive with the matching device number argument, as described in Section 2.14.3.

2634 **Restrictions**

- If the value of **devicenum** is greater than or equal to the value returned by **acc_get_num_devices** for that device type, the behavior is implementation-defined.
- Calling acc_set_device_num implies a call to acc_set_device_type with that device type argument.

2639 3.2.5. acc_get_device_num

Summary The **acc_get_device_num** routine returns the value of *acc-current-device-numvar* for the current thread.

C or C++:

```
int acc_get_device_num( acc_device_t );
```

Fortran:

```
integer function acc_get_device_num( devicetype )
integer(acc_device_kind) :: devicetype
```

Description The acc_get_device_num routine returns the value of *acc-current-device-numvar* for the current thread.

2645 3.2.6. acc_get_property

²⁶⁴⁶ **Summary** The acc_get_property and acc_get_property_string routines return ²⁶⁴⁷ the value of a *device-property* for the specified device.

2648 Format

C or C++:

Fortran:

Description The acc_get_property and acc_get_property_string routines returns 2649 the value of the specified *property*. **devicenum** and **devicetype** specify the device being 2650 queried. If devicetype has the value acc_device_current, then devicenum is ignored 2651 and the value of the property for the current device is returned. **property** is an enumeration 2652 constant, defined in openacc.h, for C or C++, or an integer parameter, defined in the openacc 2653 module, for Fortran. Integer-valued properties are returned by **acc_get_property**, and string-2654 valued properties are returned by acc_get_property_string. In Fortran, acc_get_property_string 2655 returns the result into the character variable passed as the last argument. 2656

²⁶⁵⁷ The supported values of **property** are given in the following table.

return type	return value
integer	size of device memory in bytes
integer	free device memory in bytes
z_siupport	nonzero if the specified device sup- ports sharing memory with the local thread
string	device name
string	device vendor
string	device driver version
	return type integer integer siupport string string string string

2659 An implementation may support additional properties for some devices.

2660 **Restrictions**

2658

• These routines may not be called within an compute region.

If the value of property is not one of the known values for that query routine, or that property has no value for the specified device, acc_get_property will return 0 and acc_get_property_string will return NULL (in C or C++) or an blank string (in Fortran).

2666 3.2.7. acc_init

Summary The acc_init routine tells the runtime to initialize the runtime for that device type. This can be used to isolate any initialization cost from the computational cost, when collecting performance statistics.

2670 Format

```
C or C++:
    void acc_init( acc_device_t );
```

Fortran:

```
subroutine acc_init( devicetype )
integer(acc_device_kind) :: devicetype
```

Description The acc_init routine also implicitly calls acc_set_device_type. A call to acc_init is functionally equivalent to a init directive with the matching device type argument, as described in Section 2.14.1.

2674 **Restrictions**

• This routine may not be called within a compute region.

• If the device type specified is not available, the behavior is implementation-defined; in particular, the program may abort. • If the routine is called more than once without an intervening **acc_shutdown** call, with a different value for the device type argument, the behavior is implementation-defined.

```
• If some accelerator regions are compiled to only use one device type, calling this routine with
a different device type may produce undefined behavior.
```

2682 3.2.8. acc_shutdown

Summary The **acc_shutdown** routine tells the runtime to shut down any connection to devices of the given device type, and free up any runtime resources. A call to **acc_shutdown** is functionally equivalent to a **shutdown** directive with the matching device type argument, as described in Section 2.14.2.

2687 Format

```
C or C++:
void acc_shutdown( acc_device_t );
```

Fortran:

```
subroutine acc_shutdown( devicetype )
integer(acc_device_kind) :: devicetype
```

Description The **acc_shutdown** routine disconnects the program from any device of the specified device type. Any data that is present in the memory of any such device is immediately deallocated.

2691 Restrictions

• This routine may not be called during execution of a compute region.

- If the program attempts to execute a compute region on a device or to access any data in the memory of a device after a call to **acc_shutdown** for that device type, the behavior is undefined.
- If the program attempts to shut down the **acc_device_host** device type, the behavior is undefined.

2698 3.2.9. acc_async_test

Summary The **acc_async_test** routine tests for completion of all associated asynchronous operations on the current device.

2701 Format

```
C or C++:
    int acc_async_test( int );
```

Fortran:

```
logical function acc_async_test( arg )
integer(acc_handle_kind) :: arg
```

Description The argument must be an *async-argument* as defined in Section 2.16.1 async clause. 2702 If that value did not appear in any **async** clauses, or if it did appear in one or more **async** clauses 2703 and all such asynchronous operations have completed on the current device, the acc_async_test 2704 routine will return with a nonzero value in C and C++, or .true. in Fortran. If some such asyn-2705 chronous operations have not completed, the **acc_async_test** routine will return with a zero 2706 value in C and C++, or .false. in Fortran. If two or more threads share the same accelerator, the 2707 acc_async_test routine will return with a nonzero value or .true. only if all matching asyn-2708 chronous operations initiated by this thread have completed; there is no guarantee that all matching 2709 asynchronous operations initiated by other threads have completed. 2710

2711 3.2.10. acc_async_test_device

2712 **Summary** The acc_async_test_device routine tests for completion of all associated asyn-2713 chronous operations on a device.

2714 Format

```
C or C++:
```

int acc_async_test_device(int, int);

Fortran:

```
logical function acc_async_test_device( arg, device )
integer(acc_handle_kind) :: arg
integer :: device
```

²⁷¹⁵ **Description** The first argument must be an *async-argument* as defined in Section 2.16.1 async clause.
 ²⁷¹⁶ The second argument must be a valid device number of the current device type.

If the *async-argument* did not appear in any **async** clauses, or if it did appear in one or more 2717 **async** clauses and all such asynchronous operations have completed on the specified device, the 2718 2719 acc_async_test_device routine will return with a nonzero value in C and C++, or .true. in Fortran. If some such asynchronous operations have not completed, the acc async test device 2720 routine will return with a zero value in C and C++, or .false. in Fortran. If two or more threads 2721 share the same accelerator, the **acc_async_test_device** routine will return with a nonzero 2722 value or .true. only if all matching asynchronous operations initiated by this thread have com-2723 pleted; there is no guarantee that all matching asynchronous operations initiated by other threads 2724 have completed. 2725

3.2.11. acc_async_test_all

2727 **Summary** The **acc_async_test_all** routine tests for completion of all asynchronous op-2728 erations.

C or C++:

```
int acc_async_test_all( );
```

Fortran:

```
logical function acc_async_test_all( )
```

Description If all outstanding asynchronous operations have completed, the **acc_async_test_all** routine will return with a nonzero value in C and C++, or .true. in Fortran. If some asynchronous operations have not completed, the **acc_async_test_all** routine will return with a zero value in C and C++, or .false. in Fortran. If two or more threads share the same accelerator, the **acc_async_test_all** routine will return with a nonzero value or .true. only if all outstanding asynchronous operations initiated by this thread have completed; there is no guarantee that all asynchronous operations initiated by other threads have completed.

2737 3.2.12. acc_async_test_all_device

2738 **Summary** The acc_async_test_all_device routine tests for completion of all asyn-2739 chronous operations.

2740 Format

```
C or C++:
    int acc_async_test_all_device( int );
```

Fortran:

```
logical function acc_async_test_all_device( device )
integer :: device
```

Description The argument must be a valid device number of the current device type. If all out-2741 standing asynchronous operations have completed on the specified device, the acc_async_test_all_device 2742 routine will return with a nonzero value in C and C++, or .true. in Fortran. If some asynchronous 2743 operations have not completed, the acc_async_test_all_device routine will return with a 2744 zero value in C and C++, or .false. in Fortran. If two or more threads share the same acceler-2745 ator, the acc async test all device routine will return with a nonzero value or .true. 2746 only if all outstanding asynchronous operations initiated by this thread have completed; there is no 2747 guarantee that all asynchronous operations initiated by other threads have completed. 2748

2749 3.2.13. acc_wait

Summary The **acc_wait** routine waits for completion of all associated asynchronous operations on the current device.

```
C or C++:
void acc_wait( int );
```

Fortran:

```
subroutine acc_wait( arg )
integer(acc_handle_kind) :: arg
```

Description The argument must be an *async-argument* as defined in Section 2.16.1 async clause. 2753 If that value appeared in one or more **async** clauses, the **acc_wait** routine will not return until 2754 the latest such asynchronous operation has completed on the current device. If two or more threads 2755 share the same accelerator, the acc_wait routine will return only if all matching asynchronous 2756 operations initiated by this thread have completed; there is no guarantee that all matching asyn-2757 chronous operations initiated by other threads have completed. For compatibility with version 1.0, 2758 this routine may also be spelled acc_async_wait. A call to acc_wait is functionally equiv-2759 alent to a wait directive with a matching wait argument and no async clause, as described in 2760 Section 2.16.3. 2761

2762 3.2.14. acc_wait_device

Summary The **acc_wait_device** routine waits for completion of all associated asynchronous operations on a device.

2765 Format

```
C or C++:
    void acc_wait_device( int, int );
```

Fortran:

```
subroutine acc_wait_device( arg, device )
integer(acc_handle_kind) :: arg
integer :: device
```

2766 Description The first argument must be an *async-argument* as defined in Section 2.16.1 async clause.
 2767 The second argument must be a valid device number of the current device type.

If the *async-argument* appeared in one or more **async** clauses, the **acc_wait** routine will not return until the latest such asynchronous operation has completed on the specified device. If two or more threads share the same accelerator, the **acc_wait** routine will return only if all matching asynchronous operations initiated by this thread have completed; there is no guarantee that all matching asynchronous operations initiated by other threads have completed.

2773 3.2.15. acc_wait_async

Summary The **acc_wait_async** routine enqueues a wait operation on one async queue of the current device for the operations previously enqueued on another async queue.

2776 Format

```
C or C++:
    void acc_wait_async( int, int );
```

Fortran:

```
subroutine acc_wait_async( arg, async )
integer(acc_handle_kind) :: arg, async
```

Description The arguments must be *async-arguments*, as defined in Section 2.16.1 async clause. The routine will enqueue a wait operation on the appropriate device queue associated with the second argument, which will wait for operations enqueued on the device queue associated with the first argument. See Section 2.16 Asynchronous Behavior for more information. A call to **acc_wait_async** is functionally equivalent to a **wait** directive with a matching wait argument and a matching **async** argument, as described in Section 2.16.3.

2783 3.2.16. acc_wait_device_async

Summary The **acc_wait_device_async** routine enqueues a wait operation on one async queue of a device for the operations previously enqueued on another async queue.

2786 Format

```
C or C++:
    void acc_wait_device_async( int, int, int );
```

Fortran:

```
subroutine acc_wait_device_async( arg, async, device )
integer(acc_handle_kind) :: arg, async
integer :: device
```

Description The first two arguments must be *async-arguments*, as defined in Section 2.16.1 async clause. The third argument must be a valid device number of the current device type.

The routine will enqueue a wait operation on the appropriate device queue associated with the second argument, which will wait for operations enqueued on the device queue associated with the first argument.

2792 See Section 2.16 Asynchronous Behavior for more information. A call to acc_wait_device_async

is functionally equivalent to a wait directive with a matching wait argument and a matching async
 argument, as described in Section 2.16.3.

2795 3.2.17. acc_wait_all

2796 **Summary** The **acc_wait_all** routine waits for completion of all asynchronous operations.

2797 Format

```
C or C++:
    void acc_wait_all();
```

Fortran:

```
subroutine acc_wait_all( )
```

Description The acc_wait_all routine will not return until the all asynchronous operations have completed. If two or more threads share the same accelerator, the acc_wait_all routine will return only if all asynchronous operations initiated by this thread have completed; there is no guarantee that all asynchronous operations initiated by other threads have completed. For compatibility with version 1.0, this routine may also be spelled acc_async_wait_all. A call to acc_wait_all is functionally equivalent to a wait directive with no wait argument list and no async argument, as described in Section 2.16.3.

2805 3.2.18. acc_wait_all_device

2806 Summary The acc_wait_all_device routine waits for completion of all asynchronous 2807 operations the specified device.

2808 Format

```
C or C++:
    void acc_wait_all_device( int );
```

Fortran:

```
subroutine acc_wait_all_device( device )
integer :: device
```

Description The argument must be a valid device number of the current device type. The acc_wait_all_device routine will not return until the all asynchronous operations have completed on the specified device. If two or more threads share the same accelerator, the acc_wait_all_device routine will return only if all asynchronous operations initiated by this thread have completed; there is no guarantee that all asynchronous operations initiated by other threads have completed.

2814 3.2.19. acc_wait_all_async

²⁸¹⁵ Summary The acc_wait_all_async routine enqueues wait operations on one async queue
 ²⁸¹⁶ for the operations previously enqueued on all other async queues.

```
C or C++:
```

```
void acc_wait_all_async( int );
```

Fortran:

```
subroutine acc_wait_all_async( async )
integer(acc_handle_kind) :: async
```

Description The argument must be an *async-argument* as defined in Section 2.16.1 async clause. The routine will enqueue a wait operation on the appropriate device queue for each other device queue. See Section 2.16 Asynchronous Behavior for more information. A call to **acc_wait_all_async** is functionally equivalent to a **wait** directive with no wait argument list and a matching **async** argument, as described in Section 2.16.3.

2823 3.2.20. acc_wait_all_device_async

Summary The acc_wait_all_device_async routine enqueues wait operations on one async queue for the operations previously enqueued on all other async queues on the specified device.

```
2827 Format
```

```
C or C++:
    void acc_wait_all_device_async( int, int );
```

Fortran:

```
subroutine acc_wait_all_device_async( async, device )
integer(acc_handle_kind) :: async
integer :: device
```

Description The first argument must be an *async-argument* as defined in Section 2.16.1 async clause.
 The second argument must be a valid device number of the current device type.

The routine will enqueue a wait operation on the appropriate device queue for each other device queue. See Section 2.16 Asynchronous Behavior for more information. A call to **acc_wait_all_async** is functionally equivalent to a **wait** directive with no wait argument list and a matching **async** argument, as described in Section 2.16.3.

2834 3.2.21. acc_get_default_async

2835 Summary The acc_get_default_async routine returns the value of acc-default-async-2836 var for the current thread.

```
C or C++:
```

int acc_get_default_async(void);

Fortran:

```
function acc_get_default_async( )
integer(acc_handle_kind) :: acc_get_default_async
```

Description The acc_get_default_async routine returns the value of *acc-default-asyncvar* for the current thread, which is the asynchronous queue used when an **async** clause appears without an *async-argument* or with the value acc_async_noval.

2841 3.2.22. acc_set_default_async

Summary The acc_set_default_async routine tells the runtime which asynchronous queue to use when an async clause appears with no queue argument.

2844 Format

```
C or C++:
    void acc_set_default_async( int async );
```

Fortran:

subroutine acc_set_default_async(async)
integer(acc_handle_kind) :: async

Description The acc_set_default_async routine tells the runtime to place any directives 2845 with an **async** clause that does not have an *async-argument* or with the special **acc_async_noval** 2846 value into the specified asynchronous activity queue instead of the default asynchronous activity 2847 queue for that device by setting the value of *acc-default-async-var* for the current thread. The spe-2848 cial argument acc_async_default will reset the default asynchronous activity queue to the 2849 initial value, which is implementation-defined. A call to **acc_set_default_async** is func-2850 tionally equivalent to a **set default** async directive with a matching argument in *int-expr*, as 2851 described in Section 2.14.3. 2852

2853 **3.2.23. acc_on_device**

Summary The **acc_on_device** routine tells the program whether it is executing on a particular device.

C or C++:

```
int acc_on_device( acc_device_t );
```

Fortran:

```
logical function acc_on_device( devicetype )
integer(acc_device_kind) :: devicetype
```

Description The **acc_on_device** routine may be used to execute different paths depend-2857 ing on whether the code is running on the host or on some accelerator. If the **acc_on_device** 2858 routine has a compile-time constant argument, it evaluates at compile time to a constant. The ar-2859 gument must be one of the defined accelerator types. If the argument is acc_device_host, 2860 then outside of a compute region or accelerator routine, or in a compute region or accelerator rou-2861 tine that is executed on the host CPU, this routine will evaluate to nonzero for C or C++, and 2862 .true. for Fortran; otherwise, it will evaluate to zero for C or C++, and .false. for Fortran. 2863 If the argument is **acc_device_not_host**, the result is the negation of the result with argu-2864 ment acc_device_host. If the argument is an accelerator device type, then in a compute region 2865 or routine that is executed on a device of that type, this routine will evaluate to nonzero for C or 2866 C++, and .true. for Fortran; otherwise, it will evaluate to zero for C or C++, and .false. for 2867 Fortran. The result with argument acc_device_default is undefined. 2868

2869 3.2.24. acc_malloc

²⁸⁷⁰ **Summary** The **acc_malloc** routine allocates space in the current device memory.

2871 Format

C or C++: d_void* acc_malloc(size_t);

Description The **acc_malloc** routine may be used to allocate space in the current device memory. Pointers assigned from this function may be used in **deviceptr** clauses to tell the compiler that the pointer target is resident on the device. In case of an error, **acc_malloc** returns a NULL pointer.

2876 3.2.25. acc_free

2877 **Summary** The **acc_free** routine frees memory on the current device.

```
2878 Format
```

```
C or C++:
void acc_free( d_void* );
```

Description The acc_free routine will free previously allocated space in the current device memory; the argument should be a pointer value that was returned by a call to acc_malloc. If the argument is a NULL pointer, no operation is performed.

2882 3.2.26. acc_copyin

Summary The acc_copyin routines test to see if the argument is in shared memory or already present in the current device memory; if not, they allocate space in the current device memory to correspond to the specified local memory, and copy the data to that device memory.

```
2886 Format
```

```
C or C++:
    d_void* acc_copyin( h_void*, size_t );
    void acc_copyin_async( h_void*, size_t, int );
```

Fortran:

```
subroutine acc_copyin( a )
subroutine acc_copyin( a, len )
subroutine acc_copyin_async( a, async )
subroutine acc_copyin_async( a, len, async )
type(*), dimension(..) :: a
integer :: len
integer(acc_handle_kind) :: async
```

Description The acc_copyin routines are equivalent to the enter data directive with a copyin clause, as described in Section 2.7.6. In C, the arguments are a pointer to the data and length in bytes; the synchronous function returns a pointer to the allocated device memory, as with acc_malloc. In Fortran, two forms are supported. In the first, the argument is a contiguous array section of intrinsic type. In the second, the first argument is a variable or array element and the second is the length in bytes.

²⁸⁹³ The behavior of the **acc_copyin** routines is:

```
• If the data is in shared memory, no action is taken. The C acc_copyin returns the incoming pointer.
```

```
• If the data is present in the current device memory, a present increment action with the dy-
namic reference counter is performed. The C acc_copyin returns a pointer to the existing
device memory.
```

• Otherwise, a *copyin* action with the dynamic reference counter is performed. The C acc_copyin returns the device address of the newly allocated memory.

This data may be accessed using the **present** data clause. Pointers assigned from the C **acc_copyin** function may be used in **deviceptr** clauses to tell the compiler that the pointer target is resident on the device. The **_async** versions of this function will perform any data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. The function may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

For compatibility with OpenACC 2.0, acc_present_or_copyin and acc_pcopyin are alternate names for acc_copyin.

²⁹¹⁰ 3.2.27. acc_create

Summary The acc_create routines test to see if the argument is in shared memory or already present in the current device memory; if not, they allocate space in the current device memory to correspond to the specified local memory.

2914 Format

```
C or C++:
```

```
d_void* acc_create( h_void*, size_t );
void acc_create_async( h_void*, size_t, int async );
```

Fortran:

```
subroutine acc_create( a )
subroutine acc_create( a, len )
subroutine acc_create_async( a, async )
subroutine acc_create_async( a, len, async )
type(*), dimension(..) :: a
integer :: len
integer(acc_handle_kind) :: async
```

Description The acc_create routines are equivalent to the enter data directive with a create clause, as described in Section 2.7.8. In C, the arguments are a pointer to the data and length in bytes; the synchronous function returns a pointer to the allocated device memory, as with acc_malloc. In Fortran, two forms are supported. In the first, the argument is a contiguous array section of intrinsic type. In the second, the first argument is a variable or array element and the second is the length in bytes.

- ²⁹²¹ The behavior of the **acc_create** routines is:
- If the data is in shared memory, no action is taken. The C acc_create returns the incoming pointer.
- If the data is present in the current device memory, a *present increment* action with the dynamic reference counter is performed. The C **acc_create** returns a pointer to the existing device memory.
- Otherwise, a *create* action with the dynamic reference counter is performed. The C acc_create returns the device address of the newly allocated memory.

This data may be accessed using the **present** data clause. Pointers assigned from the C **acc_copyin** function may be used in **deviceptr** clauses to tell the compiler that the pointer target is resident on the device.

The **_async** versions of these function may perform the data allocation asynchronously on the async queue associated with the value passed in as the **async** argument. The synchronous versions will not return until the data has been allocated.

For compatibility with OpenACC 2.0, acc_present_or_create and acc_pcreate are alternate names for acc_create.

²⁹³⁷ **3.2.28. acc_copyout**

Summary The acc_copyout routines test to see if the argument is in shared memory; if not, the argument must be present in the current device memory, and the routines copy data from device memory to the corresponding local memory, then deallocate that space from the device memory.

```
2941 Format
```

```
C or C++:
    void acc_copyout( h_void*, size_t );
    void acc_copyout_async( h_void*, size_t, int async );
    void acc_copyout_finalize( h_void*, size_t );
    void acc_copyout_finalize_async( h_void*, size_t, int async );
```

Fortran:

```
subroutine acc_copyout( a )
subroutine acc_copyout( a, len )
subroutine acc_copyout_async( a, async )
subroutine acc_copyout_finalize( a )
subroutine acc_copyout_finalize( a, len )
subroutine acc_copyout_finalize_async( a, async )
subroutine acc_copyout_finalize_async( a, len, async )
type(*), dimension(..) :: a
integer :: len
integer(acc_handle_kind) :: async
```

Description The acc_copyout routines are equivalent to the exit data directive with a copyout clause, and the acc_copyout_finalize routines are equivalent to the exit data directive with both copyout and finalize clauses, as described in Section 2.7.7. In C, the arguments are a pointer to the data and length in bytes. In Fortran, two forms are supported. In the first, the argument is a contiguous array section of intrinsic type. In the second, the first argument is a variable or array element and the second is the length in bytes.

²⁹⁴⁸ The behavior of the **acc_copyout** routines is:

• If the data is in shared memory, no action is taken.

• Otherwise, if the data is not present in the current device memory, a runtime error is issued.

Otherwise, a *present decrement* action with the dynamic reference counter is performed (acc_copyout), or the dynamic reference counter is set to zero (acc_copyout_finalize). If both reference counters are then zero, a *copyout* action is performed.

The **__async** versions of these functions will perform any associated data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. The function may return before the data has been transferred or deallocated; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred. Even if the data has not been transferred or deallocated before the function returns, the data will be treated as not present in the current device memory.

²⁹⁶⁰ 3.2.29. acc_delete

Summary The **acc_delete** routines test to see if the argument is in shared memory; if not, the argument must be present in the current device memory, and the routines deallocate that space from the device memory.

2964 Format

```
C or C++:
```

```
void acc_delete( h_void*, size_t );
void acc_delete_async( h_void*, size_t, int async );
void acc_delete_finalize( h_void*, size_t );
void acc_delete_finalize_async( h_void*, size_t, int async );
```

Fortran:

```
subroutine acc_delete( a )
subroutine acc_delete( a, len )
subroutine acc_delete_async( a, async )
subroutine acc_delete_async( a, len, async )
subroutine acc_delete_finalize( a )
subroutine acc_delete_finalize_async( a, async )
subroutine acc_delete_finalize_async( a, len, async )
subroutine acc_delete_finalize_async( a, len, async )
type(*), dimension(..) :: a
integer :: len
integer(acc_handle_kind) :: async
```

2965 Description The acc_delete routines are equivalent to the exit data directive with a 2966 delete clause,

and the **acc_delete_finalize** routines are equivalent to the **exit data** directive with both **delete** clause and **finalize** clauses, as described in Section 2.7.10. The arguments are as for

2969 acc copyout.

²⁹⁷⁰ The behavior of the **acc_delete** routines is:

• If the data is in shared memory, no action is taken.

• Otherwise, if the data is not present in the current device memory, a runtime error is issued.

Otherwise, a *present decrement* action with the dynamic reference counter is performed (acc_delete),
 or the dynamic reference counter is set to zero (acc_delete_finalize). If both reference counters are then zero, a *delete* action is performed.

The **__async** versions of these function may perform the data deallocation asynchronously on the async queue associated with the value passed in as the **async** argument. The synchronous versions will not return until the data has been deallocated. Even if the data has not been deallocated before the function returns, the data will be treated as not present in the current device memory.

2980 3.2.30. acc_update_device

Summary The acc_update_device routines test to see if the argument is in shared memory; if not, the argument must be present in the current device memory, and the routines update the data in device memory from the corresponding local memory.

2984 Format

```
C or C++:
```

```
void acc_update_device( h_void*, size_t );
void acc_update_device_async( h_void*, size_t, int async );
```

Fortran:

```
subroutine acc_update_device( a )
subroutine acc_update_device( a, len )
subroutine acc_update_device_async( a, async )
subroutine acc_update_device_async( a, len, async )
type(*), dimension(..) :: a
integer :: len
integer(acc_handle_kind) :: async
```

Description The acc_update_device routine is equivalent to the update directive with a device clause, as described in Section 2.14.4. In C, the arguments are a pointer to the data and length in bytes. In Fortran, two forms are supported. In the first, the argument is a contiguous array section of intrinsic type. In the second, the first argument is a variable or array element and the second is the length in bytes. For data not in shared memory, the data in the local memory is copied to the corresponding device memory. It is a runtime error to call this routine if the data is not present in the current device memory.

The **_async** versions of this function will perform the data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. The function may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

2996 3.2.31. acc_update_self

Summary The **acc_update_self** routines test to see if the argument is in shared memory; if not, the argument must be present in the current device memory, and the routines update the data in local memory from the corresponding device memory.

```
3000 Format
```

```
C or C++:
    void acc_update_self( h_void*, size_t );
    void acc_update_self_async( h_void*, size_t, int async );
```

Fortran:

```
subroutine acc_update_self( a )
subroutine acc_update_self( a, len )
subroutine acc_update_self_async( a, async )
subroutine acc_update_self_async( a, len, async )
type(*), dimension(..) :: a
integer :: len
integer(acc_handle_kind) :: async
```

Description The acc_update_self routine is equivalent to the update directive with a 3001 self clause, as described in Section 2.14.4. In C, the arguments are a pointer to the data and 3002 length in bytes. In Fortran, two forms are supported. In the first, the argument is a contiguous array 3003 section of intrinsic type. In the second, the first argument is a variable or array element and the 3004 second is the length in bytes. For data not in shared memory, the data in the local memory is copied 3005 to the corresponding device memory. There must be a device copy of the data on the device when 3006 calling this routine, otherwise no action is taken by the routine. It is a runtime error to call this 3007 routine if the data is not present in the current device memory. 3008

The **_async** versions of this function will perform the data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. The function may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

3013 3.2.32. acc_map_data

Summary The **acc_map_data** routine maps previously allocated space in the current device memory to the specified host data.

```
3016 Format
```

C or C++:

```
void acc_map_data( h_void*, d_void*, size_t );
```

Description The acc_map_data routine is similar to an enter data directive with a create 3017 clause, except instead of allocating new device memory to start a data lifetime, the device address 3018 to use for the data lifetime is specified as an argument. The first argument is a host address, fol-3019 lowed by the corresponding device address and the data length in bytes. After this call, when the 3020 host data appears in a data clause, the specified device memory will be used. It is an error to call 3021 acc map data for host data that is already present in the current device memory. It is undefined 3022 to call **acc_map_data** with a device address that is already mapped to host data. The device 3023 address may be the result of a call to **acc_malloc**, or may come from some other device-specific 3024 API routine. After mapping the device memory, the dynamic reference count for the host data is set 3025 to one, but no data movement will occur. Memory mapped by **acc_map_data** may not have the 3026 associated dynamic reference count decremented to zero, except by a call to **acc_unmap_data**. 3027 See Section 2.6.7 Reference Counters. 3028

3029 3.2.33. acc_unmap_data

3030 **Summary** The acc_unmap_data routine unmaps device data from the specified host data.

3031 Format

```
C or C++:
    void acc_unmap_data( h_void* );
```

Description The acc_unmap_data routine is similar to an exit data directive with a 3032 **delete** clause, except the device memory is not deallocated. The argument is pointer to the host 3033 data. A call to this routine ends the data lifetime for the specified host data. The device memory is 3034 not deallocated. It is undefined behavior to call **acc_unmap_data** with a host address unless that 3035 host address was mapped to device memory using acc_map_data. After unmapping memory the 3036 dynamic reference count for the pointer is set to zero, but no data movement will occur. It is an 3037 error to call **acc_unmap_data** if the structured reference count for the pointer is not zero. See 3038 Section 2.6.7 Reference Counters. 3039

3040 3.2.34. acc_deviceptr

Summary The **acc_deviceptr** routine returns the device pointer associated with a specific host address.

3043 Format

```
C or C++:
d_void* acc_deviceptr( h_void* );
```

Description The **acc_deviceptr** routine returns the device pointer associated with a host address. The argument is the address of a host variable or array that has an active lifetime on the current device. If the data is not present in the current device memory, the routine returns a NULL value.

³⁰⁴⁸ 3.2.35. acc_hostptr

Summary The **acc_hostptr** routine returns the host pointer associated with a specific device address.

3051 Format

C or C++: h_void* acc_hostptr(d_void*);

Description The acc_hostptr routine returns the host pointer associated with a device ad dress. The argument is the address of a device variable or array, such as that returned from acc_deviceptr,
 acc_create or acc_copyin. If the device address is NULL, or does not correspond to any host
 address, the routine returns a NULL value.

3056 3.2.36. acc_is_present

Summary The **acc_is_present** routine tests whether a variable or array region is accessible from the current device.

```
3059 Format
```

```
C or C++:
    int acc_is_present( h_void*, size_t );
```

Fortran:

```
logical function acc_is_present( a )
logical function acc_is_present( a, len )
type(*), dimension(..) :: a
integer :: len
```

Description The **acc_is_present** routine tests whether the specified host data is accessible 3060 from the current device. In C, the arguments are a pointer to the data and length in bytes; the 3061 function returns nonzero if the specified data is fully present, and zero otherwise. In Fortran, two 3062 forms are supported. In the first, the argument is a contiguous array section of intrinsic type. In the 3063 second, the first argument is a variable or array element and the second is the length in bytes. The 3064 function returns .true. if the specified data is in shared memory or is fully present, and .false. 3065 otherwise. If the byte length is zero, the function returns nonzero in C or .true. in Fortran if the 3066 given address is in shared memory or is present at all in the current device memory. 3067

3068 3.2.37. acc_memcpy_to_device

Summary The **acc_memcpy_to_device** routine copies data from local memory to device memory.

C or C++:

```
void acc_memcpy_to_device( d_void* dest, h_void* src, size_t bytes );
void acc_memcpy_to_device_async( d_void* dest, h_void* src,
size_t bytes, int async );
```

Description The acc_memcpy_to_device routine copies bytes of data from the local address in src to the device address in dest. The destination address must be an address accessible from the current device, such as an address returned from acc_malloc or acc_deviceptr, or an address in shared memory.

The **_async** version of this function will perform the data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. The function may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

3080 3.2.38. acc_memcpy_from_device

Summary The **acc_memcpy_from_device** routine copies data from device memory to local memory.

3083 Format

```
C or C++:
    void acc_memcpy_from_device( h_void* dest, d_void* src, size_t bytes );
    void acc_memcpy_from_device_async( h_void* dest, d_void* src,
        size_t bytes, int async );
```

Description The acc_memcpy_from_device routine copies bytes data from the device address in src to the local address in dest. The source address must be an address accessible from the current device, such as an addressed returned from acc_malloc or acc_deviceptr, or an address in shared memory.

The **_async** version of this function will perform the data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. The function may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

3092 3.2.39. acc_memcpy_device

Summary The **acc_memcpy_device** routine copies data from one memory location to another memory location on the current device.
3095 Format

C or C++:

```
void acc_memcpy_device( d_void* dest, d_void* src, size_t bytes );
void acc_memcpy_device_async( d_void* dest, d_void* src,
size_t bytes, int async );
```

Description The acc_memcpy_device routine copies bytes data from the device address in src to the device address in dest. Both addresses must be addresses in the current device memory, such as would be returned from acc_malloc or acc_deviceptr. If dest and src overlap, the behavior is undefined.

The **_async** version of this function will perform the data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. The function may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

3104 **3.2.40. acc_attach**

Summary The **acc_attach** routine updates a pointer in device memory to point to the corresponding device copy of the host pointer target.

3107 Format

```
C or C++:
    void acc_attach( h_void** ptr );
    void acc_attach_async( h_void** ptr, int async );
```

Description The acc_attach routines are passed the address of a host pointer. If the data is in shared memory, or if the pointer ***ptr** is in shared memory or is not present in the current device memory, or the address to which the ***ptr** points is not present in the current device memory, no action is taken. Otherwise, these routines perform the *attach* action (Section 2.7.2).

These routines may issue a data transfer from local memory to device memory. The **_async** version of this function will perform the data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. The function may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous version will not return until the data has been completely transferred.

3117 3.2.41. acc_detach

Summary The **acc_detach** routine updates a pointer in device memory to point to the host pointer target.

3120 Format

```
C or C++:
    void acc_detach( h_void** ptr );
    void acc_detach_async( h_void** ptr, int async );
    void acc_detach_finalize( h_void** ptr );
    void acc_detach_finalize_async( h_void** ptr, int async );
```

Description The acc_detach routines are passed the address of a host pointer. If the data is in shared memory, or if the pointer ***ptr** is in shared memory or is not present in the current device memory, if the *attachment counter* for the pointer ***ptr** is zero, no action is taken. Otherwise, these routines perform the *detach* action (Section 2.7.2).

The acc_detach_finalize routines are equivalent to an exit data directive with detach and finalize clauses, as described in Section 2.7.12 detach clause. If the data is in shared memory, or if the pointer *ptr is not present in the current device memory, or if the *attachment counter* for the pointer *ptr is zero, no action is taken. Otherwise, these routines perform the *immediate detach* action (Section 2.7.2).

These routines may issue a data transfer from local memory to device memory. The **_async** versions of these functions will perform the data transfers asynchronously on the async queue associated with the value passed in as the **async** argument. These functions may return before the data has been transferred; see Section 2.16 Asynchronous Behavior for more details. The synchronous versions will not return until the data has been completely transferred.

3135 3.2.42. acc_memcpy_d2d

Summary This acc_memcpy_d2d and acc_memcpy_d2d_async routines copy the contents of an array on one device to an array on the same or a different device without updating the value on the host.

```
3139 Format
```

Fortran:

```
subroutine acc_memcpy_d2d( dst, src, sz, dstdev, srcdev )
subroutine acc_memcpy_d2d_async( dst, src, sz, dstdev, srcdev )
        type(*), dimension(..) :: dst
        type(*), dimension(..) :: src
        integer :: sz
```

integer :: dstdev integer :: srcdev integer :: srcasync

Description The acc_memcpy_d2d and acc_memcpy_d2d_async routines are passed the address of destination and source host pointers as well as integer device numbers for the destination and source devices, which must both be of the current device type. If both arrays are in shared memory, then no action is taken. If either pointer is not in shared memory, then that array must be present on its respective device. If these conditions are met, the contents of the source array on the source device are copied to the destination array on the destination device.

³¹⁴⁶ For **acc_memcpy_d2d_async** the value of *srcasync* is the number of an async queue on the ³¹⁴⁷ source device. This routine will issue the copy operation into the device activity queue for the ³¹⁴⁸ source device and follow the usual asynchronous device queue semantics defined in 2.16.

4. Environment Variables

This chapter describes the environment variables that modify the behavior of accelerator regions. The names of the environment variables must be upper case. The values assigned environment variables are case-insensitive and may have leading and trailing white space. If the values of the environment variables change after the program has started, even if the program itself modifies the values, the behavior is implementation-defined.

3155 4.1. ACC_DEVICE_TYPE

The **ACC_DEVICE_TYPE** environment variable controls the default device type to use when executing parallel, kernels, and serial regions, if the program has been compiled to use more than one different type of device. The allowed values of this environment variable are implementationdefined. See the release notes for currently-supported values of this environment variable.

Example:

setenv ACC_DEVICE_TYPE NVIDIA export ACC_DEVICE_TYPE=NVIDIA

3160 4.2. ACC_DEVICE_NUM

The **ACC_DEVICE_NUM** environment variable controls the default device number to use when executing accelerator regions. The value of this environment variable must be a nonnegative integer between zero and the number of devices of the desired type attached to the host. If the value is greater than or equal to the number of devices attached, the behavior is implementation-defined.

Example:

setenv ACC_DEVICE_NUM 1
export ACC_DEVICE_NUM=1

3165 4.3. ACC_PROFLIB

The **ACC_PROFLIB** environment variable specifies the profiling library. More details about the evaluation at runtime is given in section 5.3.3 Runtime Dynamic Library Loading.

Example:

```
setenv ACC_PROFLIB /path/to/proflib/libaccprof.so
export ACC_PROFLIB=/path/to/proflib/libaccprof.so
```

5. Profiling Interface

This chapter describes the OpenACC interface for tools that can be used for profile and trace data collection. Therefore it provides a set of OpenACC-specific event callbacks that are triggered during the application run. Currently, this interface does not support tools that employ asynchronous sampling. In this chapter, the term *runtime* refers to the OpenACC runtime library. The term *library* refers to the third party routines invoked at specified events by the OpenACC runtime.

There are four steps for interfacing a *library* to the *runtime*. The first is to write the data collection library callback routines. Section 5.1 Events describes the supported runtime events and the order in which callbacks to the callback routines will occur. Section 5.2 Callbacks Signature describes the signature of the callback routines for all events.

The second is to use registration routines to register the data collection callbacks for the appropriate events. The data collection and registration routines are then saved in a static or dynamic library or shared object. The third is to load the *library* at runtime. The *library* may be statically linked to the application or dynamically loaded by the application or by the *runtime*. This is described in Section 5.3 Loading the Library.

The fourth step is to invoke the registration routine to register the desired callbacks with the events. This may be done explicitly by the application, if the library is statically linked with the application, implicitly by including a call to the registration routine in a .init section, or by including an initialization routine in the library if it is dynamically loaded by the *runtime*. This is described in Section 5.4 Registering Event Callbacks.

Subsequently, the *library* may collect information when the callback routines are invoked by the *runtime* and process or store the acquired data.

3190 **5.1. Events**

This section describes the events that are recognized by the runtime. Most events may have a start and end callback routine, that is, a routine that is called just before the runtime code to handle the event starts and another routine that is called just after the event is handled. The event names and routine prototypes are available in the header file **acc_prof.h**, which is delivered with the OpenACC implementation. Event names are prefixed with **acc_ev_**.

The ordering of events must reflect the order in which the OpenACC runtime actually executes them, i.e. if a runtime moves the enqueuing of data transfers or kernel launches outside the originating clauses/constructs, it needs to issue the corresponding launch callbacks when they really occur. A callback for a start event must always precede the matching end callback. The behavior of a tool receiving a callback after the runtime shutdown callback is undefined.

The events that the runtime supports can be registered with a callback and are defined in the enumeration type **acc_event_t**.

```
typedef enum acc_event_t{
   acc_ev_none = 0,
   acc_ev_device_init_start,
   acc_ev_device_init_end,
   acc_ev_device_shutdown_start,
   acc_ev_device_shutdown_end,
   acc_ev_runtime_shutdown,
   acc_ev_create,
   acc_ev_delete,
   acc_ev_alloc,
   acc_ev_free,
   acc_ev_enter_data_start,
   acc_ev_enter_data_end,
   acc_ev_exit_data_start,
   acc_ev_exit_data_end,
   acc_ev_update_start,
   acc_ev_update_end,
   acc_ev_compute_construct_start,
   acc_ev_compute_construct_end,
   acc_ev_enqueue_launch_start,
   acc_ev_enqueue_launch_end,
   acc_ev_enqueue_upload_start,
   acc_ev_enqueue_upload_end,
   acc_ev_enqueue_download_start,
   acc_ev_enqueue_download_end,
   acc_ev_wait_start,
   acc_ev_wait_end,
   acc_ev_last
}acc_event_t;
```

3203 5.1.1. Runtime Initialization and Shutdown

No callbacks can be registered for the runtime initialization. Instead the initialization of the tool is handled as described in Section 5.3 Loading the Library.

3206 The *runtime shutdown* event name is

acc_ev_runtime_shutdown

The **acc_ev_runtime_shutdown** event is triggered before the OpenACC runtime shuts down, either because all devices have been shutdown by calls to the **acc_shutdown** API routine, or at the end of the program.

3210 5.1.2. Device Initialization and Shutdown

3211 The *device initialization* event names are

acc_ev_device_init_start

acc_ev_device_init_end

These events are triggered when a device is being initialized by the OpenACC runtime. This may be when the program starts, or may be later during execution when the program reaches an **acc_init** call or an OpenACC construct. The **acc_ev_device_init_start** is triggered before device initialization starts and **acc_ev_device_init_end** after initialization is complete.

3216 The *device shutdown* event names are

acc_ev_device_shutdown_start acc_ev_device_shutdown_end

These events are triggered when a device is shut down, most likely by a call to the OpenACC acc_shutdown API routine. The acc_ev_device_shutdown_start is triggered before the device shutdown process starts and acc_ev_device_shutdown_end after the device shutdown is complete.

3221 5.1.3. Enter Data and Exit Data

3222 The *enter data* and *exit data* event names are

```
acc_ev_enter_data_start
acc_ev_enter_data_end
acc_ev_exit_data_start
acc_ev_exit_data_end
```

The acc_ev_enter_data_start and acc_ev_enter_data_end events are triggered at enter data directives, entry to data constructs, and entry to implicit data regions such as those generated by compute constructs. The acc_ev_enter_data_start event is triggered before any *data allocation*, *data update*, or *wait* events that are associated with that directive or region entry, and the acc_ev_enter_data_end is triggered after those events.

The acc_ev_exit_data_start and acc_ev_exit_data_end events are triggered at exit data directives, exit from data constructs, and exit from implicit data regions. The

acc_ev_exit_data_start event is triggered before any *data deallocation*, *data update*, or *wait* events associated with that directive or region exit, and the acc_ev_exit_data_end event is triggered after those events.

When the construct that triggers an *enter data* or *exit data* event was generated implicitly by the compiler the **implicit** field in the event structure will be set to **1**. When the construct that triggers these events was specified explicitly by the application code the **implicit** field in the event structure will be set to **0**.

3237 5.1.4. Data Allocation

3238 The *data allocation* event names are

acc_ev_create

acc_ev_delete acc_ev_alloc acc_ev_free

An acc_ev_alloc event is triggered when the OpenACC runtime allocates memory from the de-3239 vice memory pool, and an **acc_ev_free** event is triggered when the runtime frees that memory. 3240 An acc_ev_create event is triggered when the OpenACC runtime associates device memory 3241 with local memory, such as for a data clause (create, copyin, copy, copyout) at entry to 3242 3243 a data construct, compute construct, at an **enter data** directive, or in a call to a data API routine (acc_copyin, acc_create, ...). An acc_ev_create event may be preceded by an 3244 acc_ev_alloc event, if newly allocated memory is used for this device data, or it may not, if 3245 the runtime manages its own memory pool. An acc_ev_delete event is triggered when the 3246 OpenACC runtime disassociates device memory from local memory, such as for a data clause at 3247 exit from a data construct, compute construct, at an **exit data** directive, or in a call to a data API 3248 routine (acc_copyout, acc_delete, ...). An acc_ev_delete event may be followed by 3249 an **acc_ev_free** event, if the disassociated device memory is freed, or it may not, if the runtime 3250 3251 manages its own memory pool.

When the action that generates a *data allocation* event was generated explicitly by the application code the **implicit** field in the event structure will be set to **0**. When the *data allocation* event is triggered because of a variable or array with implicitly-determined data attributes or otherwise implicitly by the compiler the **implicit** field in the event structure will be set to **1**.

3256 5.1.5. Data Construct

The events for entering and leaving *data constructs* are mapped to *enter data* and *exit data* events as described in Section 5.1.3 Enter Data and Exit Data.

3259 5.1.6. Update Directive

3260 The *update directive* event names are

```
acc_ev_update_start
acc_ev_update_end
```

The acc_ev_update_start event will be triggered at an update directive, before any *data update* or *wait* events that are associated with the update directive are carried out, and the corresponding acc_ev_update_end event will be triggered after any of the associated events.

3264 5.1.7. Compute Construct

3265 The *compute construct* event names are

acc_ev_compute_construct_start
acc_ev_compute_construct_end

The **acc_ev_compute_construct_start** event is triggered at entry to a compute construct, before any *launch* events that are associated with entry to the compute construct. The

acc_ev_compute_construct_end event is triggered at the exit of the compute construct, after any *launch* events associated with exit from the compute construct. If there are data clauses on the compute construct, those data clauses may be treated as part of the compute construct, or as part of a data construct containing the compute construct. The callbacks for data clauses must use the same line numbers as for the compute construct events.

3273 5.1.8. Enqueue Kernel Launch

3274 The *launch* event names are

acc_ev_enqueue_launch_start acc_ev_enqueue_launch_end

The **acc_ev_enqueue_launch_start** event is triggered just before an accelerator compu-3275 tation is enqueued for execution on a device, and acc_ev_enqueue_launch_end is trig-3276 gered just after the computation is enqueued. Note that these events are synchronous with the 3277 local thread enqueueing the computation to a device, not with the device executing the compu-3278 tation. The acc ev enqueue launch start event callback routine is invoked just before 3279 the computation is enqueued, not just before the computation starts execution. More importantly, 3280 the **acc_ev_enqueue_launch_end** event callback routine is invoked after the computation is 3281 enqueued, not after the computation finished executing. 3282

Note: Measuring the time between the start and end launch callbacks is often unlikely to be useful, since it will only measure the time to manage the launch queue, not the time to execute the code on the device.

5.1.9. Enqueue Data Update (Upload and Download)

3287 The data update event names are

acc_ev_enqueue_upload_start acc_ev_enqueue_upload_end acc_ev_enqueue_download_start acc_ev_enqueue_download_end

The **_start** events are triggered just before each upload (data copy from local memory to device memory) operation is or download (data copy from device memory to local memory) operation is enqueued for execution on a device. The corresponding **_end** events are triggered just after each upload or download operation is enqueued.

Note: Measuring the time between the start and end update callbacks is often unlikely to be useful, since it will only measure the time to manage the enqueue operation, not the time to perform the actual upload or download.

When the action that generates a *data update* event was generated explicitly by the application code the **implicit** field in the event structure will be set to **0**. When the *data allocation* event is triggered because of a variable or array with implicitly-determined data attributes or otherwise implicitly by the compiler the **implicit** field in the event structure will be set to **1**.

3299 5.1.10. Wait

3300 The *wait* event names are

acc_ev_wait_start
acc_ev_wait_end

An acc_ev_wait_start will be triggered for each relevant queue before the local thread waits for that queue to be empty. A acc_ev_wait_end will be triggered for each relevant queue after the local thread has determined that the queue is empty.

Wait events occur when the local thread and a device synchronize, either due to a **wait** directive or by a *wait* clause on a synchronous data construct, compute construct, or **enter data**, **exit data**, or **update** directive. For *wait* events triggered by an explicit synchronous **wait** directive or *wait* clause, the **implicit** field in the event structure will be **0**. For all other wait events, the **implicit** field in the event structure will be **1**.

The OpenACC runtime need not trigger wait events for queues that have not been used in the 3309 program, and need not trigger *wait* events for queues that have not been used by this thread since 3310 the last *wait* operation. For instance, an **acc wait** directive with no arguments is defined to wait on 3311 all queues. If the program only uses the default (synchronous) queue and the queue associated with 3312 async(1) and async(2) then an acc wait directive may trigger wait events only for those 3313 three queues. If the implementation knows that no activities have been enqueued on the **async (2)** 3314 queue since the last *wait* operation, then the **acc wait** directive may trigger *wait* events only for 3315 the default queue and the **async(1)** queue. 3316

3317 5.2. Callbacks Signature

This section describes the signature of event callbacks. All event callbacks have the same signature. The routine prototypes are available in the header file **acc_prof.h**, which is delivered with the OpenACC implementation.

All callback routines have three arguments. The first argument is a pointer to a struct containing 3321 general information; the same struct type is used for all callback events. The second argument is 3322 a pointer to a struct containing information specific to that callback event; there is one struct type 3323 containing information for data events, another struct type containing information for kernel launch 3324 events, and a third struct type for other events, containing essentially no information. The third 3325 argument is a pointer to a struct containing information about the application programming interface 3326 (API) being used for the specific device. For NVIDIA CUDA devices, this contains CUDA-specific 3327 information; for OpenCL devices, this contains OpenCL-specific information. Other interfaces can 3328 be supported as they are added by implementations. The prototype for a callback routine is: 3329

```
typedef void (*acc_prof_callback)
    (acc_prof_info*, acc_event_info*, acc_api_info*);
```

In the descriptions, the datatype **ssize_t** means a signed 32-bit integer for a 32-bit binary and a 64-bit integer for a 64-bit binary, the datatype **size_t** means an unsigned 32-bit integer for a 32-bit binary and a 64-bit integer for a 64-bit binary, and the datatype **int** means a 32-bit integer for both 32-bit and 64-bit binaries. A null pointer is the pointer with value zero.

5.2.1. First Argument: General Information

³³³⁵ The first argument is a pointer to the **acc_prof_info** struct type:

```
typedef struct acc_prof_info{
    acc_event_t event_type;
    int valid_bytes;
    int version;
    acc_device_t device_type;
    int device_number;
    int thread_id;
    ssize_t async;
    ssize_t async_queue;
    const char* src_file;
    const char* func_name;
    int line_no, end_line_no;
    int func_line_no, func_end_line_no;
}acc_prof_info;
```

³³³⁶ The fields are described below.

thread number.

3353

```
• acc_event_t event_type - The event type that triggered this callback. The datatype
3337
           is the enumeration type acc_event_t, described in the previous section. This allows the
3338
           same callback routine to be used for different events.
3339
        • int valid_bytes - The number of valid bytes in this struct. This allows a library to inter-
3340
           face with newer runtimes that may add new fields to the struct at the end while retaining com-
3341
           patibility with older runtimes. A runtime must fill in the event type and valid bytes
3342
           fields, and must fill in values for all fields with offset less than valid_bytes. The value of
3343
           valid_bytes for a struct is recursively defined as:
3344
           valid_bytes(struct) = offset(lastfield) + valid_bytes(lastfield)
           valid_bytes(type[n]) = (n-1)*sizeof(type) + valid_bytes(type)
           valid_bytes(basictype) = sizeof(basictype)
        • int version - A version number; the value of _OPENACC.
3345
        • acc_device_t device_type - The device type corresponding to this event. The datatype
3346
           is acc_device_t, an enumeration type of all the supported device types, defined in openacc.h.
3347
        • int device_number - The device number. Each device is numbered, typically starting at
3348
           device zero. For applications that use more than one device type, the device numbers may be
3349
           unique across all devices or may be unique only across all devices of the same device type.
3350
        • int thread_id - The host thread ID making the callback. Host threads are given unique
3351
           thread ID numbers typically starting at zero. This is not necessarily the same as the OpenMP
3352
```

3354 3355	• ssize_t async - The value of the async() clause for the directive that triggered this callback.
3356 3357	• ssize_t async_queue - If the runtime uses a limited number of asynchronous queues, this field contains the internal asynchronous queue number used for the event.
3358 3359 3360	• const char* src_file - A pointer to null-terminated string containing the name of or path to the source file, if known, or a null pointer if not. If the library wants to save the source file name, it should allocate memory and copy the string.
3361 3362 3363	• const char* func_name - A pointer to a null-terminated string containing the name of the function in which the event occurred, if known, or a null pointer if not. If the library wants to save the function name, it should allocate memory and copy the string.
3364 3365 3366	• int line_no - The line number of the directive or program construct or the starting line number of the OpenACC construct corresponding to the event. A negative or zero value means the line number is not known.
3367 3368	• int end_line_no - For an OpenACC construct, this contains the line number of the end of the construct. A negative or zero value means the line number is not known.
3369 3370	• int func_line_no - The line number of the first line of the function named in func_name . A negative or zero value means the line number is not known.
3371 3372	• int func_end_line_no - The last line number of the function named in func_name . A negative or zero value means the line number is not known.

3373 5.2.2. Second Argument: Event-Specific Information

³³⁷⁴ The second argument is a pointer to the **acc_event_info** union type.

typedef union acc_event_info{
 acc_event_t event_type;
 acc_data_event_info data_event;
 acc_launch_event_info launch_event;
 acc_other_event_info other_event;
}acc_event_info;

The **event_type** field selects which union member to use. The first five members of each union member are identical. The second through fifth members of each union member (**valid_bytes**, **parent_construct**, **implicit**, and **tool_info**) have the same semantics for all event types:

• **int valid_bytes** - The number of valid bytes in the respective struct. (This field is similar used as discussed in Section 5.2.1 First Argument: General Information.)

• acc_construct_t parent_construct - This field describes the type of construct that caused the event to be emitted. The possible values for this field are defined by the acc_construct_t enum, described at the end of this section.

• int implicit - This field is set to 1 for any implicit event, such as an implicit wait at a synchronous data construct or synchronous enter data, exit data or update directive. This field is set to zero when the event is triggered by an explicit directive or call to a runtime API routine.

• void* tool_info - This field is used to pass tool-specific information from a _start event to the matching _end event. For a _start event callback, this field will be initialized to a null pointer. The value of this field for a _end event will be the value returned by the library in this field from the matching _start event callback, if there was one, or null otherwise. For events that are neither _start or _end events, this field will be null.

3393 Data Events

For a data event, as noted in the event descriptions, the second argument will be a pointer to the acc_data_event_info struct.

```
typedef struct acc_data_event_info{
    acc_event_t event_type;
    int valid_bytes;
    acc_construct_t parent_construct;
    int implicit;
    void* tool_info;
    const char* var_name;
    size_t bytes;
    const void* host_ptr;
    const void* device_ptr;
}acc_data_event_info;
```

³³⁹⁶ The fields specific for a data event are:

• acc_event_t event_type - The event type that triggered this callback. The events that use the acc_data_event_info struct are:

```
acc_ev_enqueue_upload_start
acc_ev_enqueue_upload_end
acc_ev_enqueue_download_start
acc_ev_enqueue_download_end
acc_ev_create
acc_ev_delete
acc_ev_alloc
acc_ev_free
```

- **const char* var_name** A pointer to null-terminated string containing the name of the variable for which this event is triggered, if known, or a null pointer if not. If the library wants to save the variable name, it should allocate memory and copy the string.
- **size_t bytes** The number of bytes for the data event.
- **const void* host_ptr** If available and appropriate for this event, this is a pointer to the host data.
- **const void* device_ptr** If available and appropriate for this event, this is a pointer to the corresponding device data.

3407 Launch Events

For a launch event, as noted in the event descriptions, the second argument will be a pointer to the acc_launch_event_info struct.

```
typedef struct acc_launch_event_info{
    acc_event_t event_type;
    int valid_bytes;
    acc_construct_t parent_construct;
    int implicit;
    void* tool_info;
    const char* kernel_name;
    size_t num_gangs, num_workers, vector_length;
}acc_launch_event_info;
```

³⁴¹⁰ The fields specific for a launch event are:

```
• acc_event_t event_type - The event type that triggered this callback. The events that
use the acc_launch_event_info struct are:
```

acc_ev_enqueue_launch_start
acc_ev_enqueue_launch_end

- const char* kernel_name A pointer to null-terminated string containing the name of
 the kernel being launched, if known, or a null pointer if not. If the library wants to save the
 kernel name, it should allocate memory and copy the string.
- **size_tnum_gangs, num_workers, vector_length** The number of gangs, workers and vector lanes created for this kernel launch.

3418 Other Events

For any event that does not use the acc_data_event_info or acc_launch_event_info struct, the second argument to the callback routine will be a pointer to acc_other_event_info struct.

```
typedef struct acc_other_event_info{
    acc_event_t event_type;
    int valid_bytes;
    acc_construct_t parent_construct;
    int implicit;
    void* tool_info;
}acc_other_event_info;
```

3422 Parent Construct Enumeration

All event structures contain a **parent_construct** member that describes the type of construct that caused the event to be emitted. The purpose of this field is to provide a means to identify the type of construct emitting the event in the cases where an event may be emitted by multiple contruct types, such as is the case with data and wait events. The possible values for the **parent_construct** field are defined in the enumeration type **acc_construct_t**. In the case of combined directives, the outermost construct of the combined construct should be specified as the **parent_construct**. If the event was emitted as the result of the application making a call to the runtime api, the value will be **acc_construct_runtime_api**.

```
typedef enum acc_construct_t{
   acc_construct_parallel = 0,
   acc_construct_kernels = 1,
   acc_construct_loop = 2,
   acc_construct_data = 3,
   acc construct enter data = 4,
   acc construct exit data = 5,
   acc_construct_host_data = 6,
   acc_construct_atomic = 7,
   acc_construct_declare = 8,
   acc_construct_init = 9,
   acc_construct_shutdown = 10,
   acc_construct_set = 11,
   acc_construct_update = 12,
   acc_construct_routine = 13,
   acc_construct_wait = 14,
   acc_construct_runtime_api = 15,
   acc_construct_serial = 16
}acc_construct_t;
```

3431 5.2.3. Third Argument: API-Specific Information

³⁴³² The third argument is a pointer to the **acc_api_info** struct type, shown here.

```
typedef struct acc_api_info{
    acc_device_api device_api;
    int valid_bytes;
    acc_device_t device_type;
    int vendor;
    const void* device_handle;
    const void* context_handle;
    const void* async_handle;
}acc_api_info;
```

3433 The fields are described below:

- **acc_device_api** device_api The API in use for this device. The data type is the enumeration **acc_device_api**, which is described later in this section.
- **int valid_bytes** The number of valid bytes in this struct. See the discussion above in Section 5.2.1 First Argument: General Information.

3438 3439	• acc_device_t device_type - The device type; the datatype is acc_device_t, defined in openacc.h.
3440 3441	• int vendor - An identifier to identify the OpenACC vendor; contact your vendor to determine the value used by that vendor's runtime.
3442 3443	• const void* device_handle - If applicable, this will be a pointer to the API-specific device information.
3444 3445	• const void * context_handle - If applicable, this will be a pointer to the API-specific context information.
3446 3447	• const void* async_handle - If applicable, this will be a pointer to the API-specific async queue information.

According to the value of **device_api** a library can cast the pointers of the fields **device_handle**, **context_handle** and **async_handle** to the respective device API type. The following device APIs are defined in the interface below. Any implementation-defined device API type must have a value greater than **acc_device_api_implementation_defined**.

3452 5.3. Loading the Library

This section describes how a tools library is loaded when the program is run. Four methods are described.

• A tools library may be linked with the program, as any other library is linked, either as a static library or a dynamic library, and the runtime will call a predefined library initialization routine that will register the event callbacks.

- The OpenACC runtime implementation may support a dynamic tools library, such as a shared object for Linux or OS/X, or a DLL for Windows, which is then dynamically loaded at runtime under control of the environment variable **ACC_PROFLIB**.
- Some implementations where the OpenACC runtime is itself implemented as a dynamic library may support adding a tools library using the LD_PRELOAD feature in Linux.
- A tools library may be linked with the program, as in the first option, and the application itself can call a library initialization routine that will register the event callbacks.

Callbacks are registered with the runtime by calling **acc_prof_register** for each event as described in Section 5.4 Registering Event Callbacks. The prototype for **acc_prof_register** is:

extern void acc_prof_register

```
(acc_event_t event_type, acc_prof_callback cb,
acc_register_t info);
```

The first argument to **acc_prof_register** is the event for which a callback is being registered (compare Section 5.1 Events). The second argument is a pointer to the callback routine:

The third argument is usually zero (or **acc_reg**). See Section 5.4.2Disabling and Enabling Callbacks for cases where a nonzero value is used. The argument **acc_register_t** is an enum type:

```
typedef enum acc_register_t{
    acc_reg = 0,
    acc_toggle = 1,
    acc_toggle_per_thread = 2
}acc_register_t;
```

3472 An example of registering callbacks for launch, upload, and download events is:

```
acc_prof_register(acc_ev_enqueue_launch_start, prof_launch, 0);
acc_prof_register(acc_ev_enqueue_upload_start, prof_data, 0);
acc_prof_register(acc_ev_enqueue_download_start, prof_data, 0);
```

As shown in this example, the same routine (**prof_data**) can be registered for multiple events. The routine can use the **event_type** field in the **acc_prof_info** structure to determine for what event it was invoked.

3476 5.3.1. Library Registration

The OpenACC runtime will invoke acc_register_library, passing the addresses of the registration routines acc_prof_register and acc_prof_unregister, in case that routine comes from a dynamic library. In the third argument it passes the address of the lookup routine acc_prof_lookup to obtain the addresses of inquiry functions. No inquiry functions are defined in this profiling interface, but we preserve this argument for future support of sampling-based tools.

Typically, the OpenACC runtime will include a *weak* definition of **acc_register_library**, which does nothing and which will be called when there is no tools library. In this case, the library can save the addresses of these routines and/or make registration calls to register any appropriate callbacks. The prototype for **acc_register_library** is:

```
extern void acc_register_library
  (acc_prof_reg reg, acc_prof_reg unreg,
     acc_prof_lookup_func lookup);
```

³⁴⁸⁷ The first two arguments of this routine are of type:

The third argument passes the address to the lookup function **acc_prof_lookup** to obtain the address of interface functions. It is of type:

```
typedef void (*acc_query_fn)();
typedef acc_query_fn (*acc_prof_lookup_func)
        (const char* acc_query_fn_name);
```

The argument of the lookup function is a string with the name of the inquiry function. There are no inquiry functions defined for this interface.

3492 5.3.2. Statically-Linked Library Initialization

A tools library can be compiled and linked directly into the application. If the library provides an external routine **acc_register_library** as specified in Section 5.3.1Library Registration, the runtime will invoke that routine to initialize the library.

3496 The sequence of events is:

3497 1. The runtime invokes the **acc_register_library** routine from the library.

- 2. The acc_register_library routine calls acc_prof_register for each event to be monitored.
- 3500 3. **acc_prof_register** records the callback routines.
- 4. The program runs, and your callback routines are invoked at the appropriate events.

³⁵⁰² In this mode, only one tool library is supported.

5.3.3. Runtime Dynamic Library Loading

A common case is to build the tools library as a dynamic library (shared object for Linux or OS/X, DLL for Windows). In that case, you can have the OpenACC runtime load the library during initialization. This allows you to enable runtime profiling without rebuilding or even relinking your application. The dynamic library must implement a registration routine **acc_register_library** as specified in Section 5.3.1 Library Registration.

The user may set the environment variable **ACC_PROFLIB** to the path to the library will tell the OpenACC runtime to load your dynamic library at initialization time:

Bash:

or

```
export ACC_PROFLIB=/home/user/lib/myprof.so
./myapp
ACC_PROFLIB=/home/user/lib/myprof.so ./myapp
```

C-shell: setenv ACC_PROFLIB /home/user/lib/myprof.so ./myapp

When the OpenACC runtime initializes, it will read the ACC_PROFLIB environment variable (with 3511 getenv). The runtime will open the dynamic library (using dlopen or LoadLibraryA); if 3512 the library cannot be opened, the runtime may abort, or may continue execution with or with-3513 out an error message. If the library is successfully opened, the runtime will get the address of 3514 the acc_register_library routine (using dlsym or GetProcAddress). If this routine 3515 is resolved in the library, it will be invoked passing in the addresses of the registration routine 3516 acc_prof_register, the deregistration routine acc_prof_unregister, and the lookup 3517 routine acc_prof_lookup. The registration routine in your library, acc_register_library, 3518 should register the callbacks by calling the register argument, and should save the addresses of 3519 the arguments (register, unregister, and lookup) for later use, if needed. 3520

3521 The sequence of events is:

1. Initialization of the OpenACC runtime.

3523 2. OpenACC runtime reads **ACC_PROFLIB**.

3524 3. OpenACC runtime loads the library.

4. OpenACC runtime calls the **acc_register_library** routine in that library.

- 5. Your acc_register_library routine calls acc_prof_register for each event to be monitored.
- 3528 6. **acc_prof_register** records the callback routines.
- ³⁵²⁹ 7. The program runs, and your callback routines are invoked at the appropriate events.

If supported, paths to multiple dynamic libraries may be specified in the ACC_PROFLIB environment variable, separated by semicolons (;). The OpenACC runtime will open these libraries and invoke the acc_register_library routine for each, in the order they appear in ACC_PROFLIB.

3533 5.3.4. Preloading with LD_PRELOAD

The implementation may also support dynamic loading of a tools library using the LD_PRELOAD 3534 feature available in some systems. In such an implementation, you need only specify your tools 3535 library path in the LD **PRELOAD** environment variable before executing your program. The Open-3536 ACC runtime will invoke the **acc_register_library** routine in your tools library at initial-3537 ization time. This requires that the OpenACC runtime include a dynamic library with a default 3538 (empty) implementation of **acc_register_library** that will be invoked in the normal case 3539 where there is no LD_PRELOAD setting. If an implementation only supports static linking, or if the 3540 application is linked without dynamic library support, this feature will not be available. 3541

Bash:

```
export LD_PRELOAD=/home/user/lib/myprof.so
./myapp
or
LD_PRELOAD=/home/user/lib/myprof.so ./myapp
```

C-shell:

setenv LD_PRELOAD /home/user/lib/myprof.so ./myapp

3542 The sequence of events is:

- 1. The operating system loader loads the library specified in LD_PRELOAD.
- 2. The call to **acc_register_library** in the OpenACC runtime is resolved to the routine in the loaded tools library.
- 3546 3. OpenACC runtime calls the **acc_register_library** routine in that library.
- 4. Your acc_register_library routine calls acc_prof_register for each event to be monitored.
- 5. **acc_prof_register** records the callback routines.
- 6. The program runs, and your callback routines are invoked at the appropriate events.

In this mode, only a single tools library is supported, since only one **acc_register_library** initialization routine will get resolved by the dynamic loader.

3553 5.3.5. Application-Controlled Initialization

An alternative to default initialization is to have the application itself call the library initialization routine, which then calls **acc_prof_register** for each appropriate event. The library may be statically linked to the application or your application may dynamically load the library.

3557 The sequence of events is:

- 1. Your application calls the library initialization routine.
- 2. The library initialization routine calls **acc_prof_register** for each event to be monitored.
- 3561 3. acc_prof_register records the callback routines.

4. The program runs, and your callback routines are invoked at the appropriate events.

³⁵⁶³ In this mode, multiple tools libraries can be supported, with each library initialization routine in-³⁵⁶⁴ voked by the application.

5.4. Registering Event Callbacks

This section describes how to register and unregister callbacks, temporarily disabling and enabling callbacks, the behavior of dynamic registration and unregistration, and requirements on an Open-ACC implementation to correctly support the interface.

5.4.1. Event Registration and Unregistration

The library must calls the registration routine **acc_prof_register** to register each callback with the runtime. A simple example:

In this example the **prof_data** routine will be invoked for each data upload and download event, and the **prof_launch** routine will be invoked for each launch event. The **prof_data** routine might start out with:

3575 Multiple Callbacks

³⁵⁷⁶ Multiple callback routines can be registered on the same event:

acc_prof_register(acc_ev_enqueue_upload_start, prof_data, 0); acc_prof_register(acc_ev_enqueue_upload_start, prof_up, 0);

For most events, the callbacks will be invoked in the order in which they are registered. However, *end* events, named **acc_ev_...end**, invoke callbacks in the reverse order. Essentially, each event has an ordered list of callback routines. A new callback routine is appended to the tail of the list for that event. For most events, that list is traversed from the head to the tail, but for *end* events, the list is traversed from the tail to the head.

If a callback is registered, then later unregistered, then later still registered again, the second registration is considered to be a new callback, and the callback routine will then be appended to the tail of the callback list for that event.

3585 Unregistering

A matching call to **acc_prof_unregister** will remove that routine from the list of callback routines for that event.

acc_prof_register(acc_ev_enqueue_upload_start, prof_data, 0);
// prof_data is on the callback list for acc_ev_enqueue_upload_start
...
acc_prof_unregister(acc_ev_enqueue_upload_start, prof_data, 0);
// prof_data is removed from the callback list
// for acc_ev_enqueue_upload_start

Each entry on the callback list must also have a *ref* count. This keeps track of how many times this routine was added to this event's callback list. If a routine is registered *n* times, it must be unregistered *n* times before it is removed from the list. Note that if a routine is registered multiple times for the same event, its *ref* count will be incremented with each registration, but it will only be invoked once for each event instance.

5.4.2. Disabling and Enabling Callbacks

A callback routine may be temporarily disabled on the callback list for an event, then later re-3594 enabled. The behavior is slightly different than unregistering and later re-registering that event. 3595 When a routine is disabled and later re-enabled, the routine's position on the callback list for that 3596 event is preserved. When a routine is unregistered and later re-registered, the routine's position on 3597 the callback list for that event will move to the tail of the list. Also, unregistering a callback must be 3598 done *n* times if the callback routine was registered *n* times. In contrast, disabling, and enabling an 3599 event sets a toggle. Disabling a callback will immediately reset the toggle and disable calls to that 3600 routine for that event, even if it was enabled multiple times. Enabling a callback will immediately 3601 set the toggle and enable calls to that routine for that event, even if it was disabled multiple times. 3602 Registering a new callback initially sets the toggle. 3603

A call to **acc_prof_unregister** with a value of **acc_toggle** as the third argument will disable callbacks to the given routine. A call to **acc_prof_register** with a value of **acc_toggle** as the third argument will enable those callbacks.

A call to either **acc_prof_unregister** or **acc_prof_register** to disable or enable a callback when that callback is not currently registered for that event will be ignored with no error.

All callbacks for an event may be disabled (and re-enabled) by passing **NULL** to the second argument and **acc_toggle** to the third argument of **acc_prof_unregister** (and **acc_prof_register**). This sets a toggle for that event, which is distinct from the toggle for each callback for that event. While the event is disabled, no callbacks for that event will be invoked. Callbacks for that event can be registered, unregistered, enabled, and disabled while that event is disabled, but no callbacks will be invoked for that event until the event itself is enabled. Initially, all events are enabled.

```
acc_prof_unregister(acc_ev_enqueue_upload_start,
       prof_data, acc_toggle);
// prof_data is disabled
. . .
acc_prof_unregister(acc_ev_enqueue_upload_start,
       NULL, acc_toggle);
// acc_ev_enqueue_upload_start callbacks are disabled
. . .
acc_prof_register(acc_ev_enqueue_upload_start,
       prof_data, acc_toggle);
// prof_data is re-enabled, but
// acc_ev_enqueue_upload_start callbacks still disabled
. . .
acc_prof_register(acc_ev_enqueue_upload_start, prof_up, 0);
// prof_up is registered and initially enabled, but
// acc_ev_enqueue_upload_start callbacks still disabled
. . .
acc_prof_register(acc_ev_enqueue_upload_start,
       NULL, acc_toggle);
// acc_ev_enqueue_upload_start callbacks are enabled
```

Finally, all callbacks can be disabled (and enabled) by passing the argument list **(0, NULL**, acc_toggle) to acc_prof_unregister (and acc_prof_register). This sets a global toggle disabling all callbacks, which is distinct from the toggle enabling callbacks for each event and the toggle enabling each callback routine. The behavior of passing zero as the first argument and a non-NULL value as the second argument to acc_prof_unregister or acc_prof_register is not defined, and may be ignored by the runtime without error.

All callbacks can be disabled (or enabled) for just the current thread by passing the argument list (0, NULL, acc_toggle_per_thread) to acc_prof_unregister (and acc_prof_register). This is the only thread-specific interface to acc_prof_register and acc_prof_unregister, all other calls to register, unregister, enable, or disable callbacks affect all threads in the application.

3625 5.5. Advanced Topics

This section describes advanced topics such as dynamic registration and changes of the execution state for callback routines as well as the runtime and tool behavior for multiple host threads.

3628 5.5.1. Dynamic Behavior

Callback routines may be registered or unregistered, enabled or disabled at any point in the execution
of the program. Calls may appear in the library itself, during the processing of an event. The
OpenACC runtime must allow for this case, where the callback list for an event is modified while
that event is being processed.

3633 Dynamic Registration and Unregistration

Calls to **acc_register** and **acc_unregister** may occur at any point in the application. A 3634 callback routine can be registered or unregistered from a callback routine, either the same routine 3635 or another routine, for a different event or the same event for which the callback was invoked. If a 3636 callback routine is registered for an event while that event is being processed, then the new callback 3637 routine will be added to the tail of the list of callback routines for this event. Some events (the 3638 _end) events process the callback routines in reverse order, from the tail to the head. For those 3639 events, adding a new callback routine will not cause the new routine to be invoked for this instance 3640 of the event. The other events process the callback routines in registration order, from the head to 3641 the tail. Adding a new callback routine for such a event will cause the runtime to invoke that newly 3642 registered callback routine for this instance of the event. Both the runtime and the library must 3643 implement and expect this behavior. 3644

If an existing callback routine is unregistered for an event while that event is being processed, that
callback routine is removed from the list of callbacks for this event. For any event, if that callback
routine had not yet been invoked for this instance of the event, it will not be invoked.

Registering and unregistering a callback routine is a global operation and affects all threads, in a multithreaded application. See Section 5.4.1 Multiple Callbacks.

3650 Dynamic Enabling and Disabling

Calls to **acc_register** and **acc_unregister** to enable and disable a specific callback for 3651 an event, enable or disable all callbacks for an event, or enable or disable all callbacks may occur 3652 at any point in the application. A callback routine can be enabled or disabled from a callback 3653 routine, either the same routine or another routine, for a different event or the same event for which 3654 the callback was invoked. If a callback routine is enabled for an event while that event is being 3655 processed, then the new callback routine will be immediately enabled. If it appears on the list of 3656 callback routines closer to the head (for **end** events) or closer to the tail (for other events), that 3657 newly-enabled callback routine will be invoked for this instance of this event, unless it is disabled 3658 or unregistered before that callback is reached. 3659

If a callback routine is disabled for an event while that event is being processed, that callback routine is immediately disabled. For any event, if that callback routine had not yet been invoked for this instance of the event, it will not be invoked, unless it is enabled before that callback routine is reached in the list of callbacks for this event. If all callbacks for an event are disabled while that event is being processed, or all callbacks are disabled for all events while an event is being processed, then when this callback routine returns, no more callbacks will be invoked for this instance of the event.

Registering and unregistering a callback routine is a global operation and affects all threads, in a multithreaded application. See Section 5.4.1 Multiple Callbacks.

5.5.2. OpenACC Events During Event Processing

OpenACC events may occur during event processing. This may be because of OpenACC API routine calls or OpenACC constructs being reached during event processing, or because of multiple host threads executing asynchronously. Both the OpenACC runtime and the tool library must implement the proper behavior.

3673 5.5.3. Multiple Host Threads

Many programs that use OpenACC also use multiple host threads, such as programs using the OpenMP API. The appearance of multiple host threads affects both the OpenACC runtime and the tools library.

3677 Runtime Support for Multiple Threads

The OpenACC runtime must be thread-safe, and the OpenACC runtime implementation of this tools interface must also be thread-safe. All threads use the same set of callbacks for all events, so registering a callback from one thread will cause all threads to execute that callback. This means that managing the callback lists for each event must be protected from multiple simultaneous updates. This includes adding a callback to the tail of the callback list for an event, removing a callback from the list for an event, and incrementing or decrementing the *ref* count for a callback routine for an event.

In addition, one thread may register, unregister, enable, or disable a callback for an event while another thread is processing the callback list for that event asynchronously. The exact behavior may be dependent on the implementation, but some behaviors are expected and others are disallowed. In the following examples, there are three callbacks, A, B, and C, registered for event E in that order, where callbacks A and B are enabled and callback C is temporarily disabled. Thread T1 is dynamically modifying the callbacks for event E while thread T2 is processing an instance of event E.

- Suppose thread T1 unregisters or disables callback A for event E. Thread T2 may or may not invoke callback A for this event instance, but it must invoke callback B; if it invokes callback A, that must precede the invocation of callback B.
- Suppose thread T1 unregisters or disables callback B for event E. Thread T2 may or may not invoke callback B for this event instance, but it must invoke callback A; if it invokes callback B, that must follow the invocation of callback A.
- Suppose thread T1 unregisters or disables callback A and then unregisters or disables callback
 B for event E. Thread T2 may or may not invoke callback A and may or may not invoke
 callback B for this event instance, but if it invokes both callbacks, it must invoke callback A
 before it invokes callback B.
- Suppose thread T1 unregisters or disables callback B and then unregisters or disables callback
 A for event E. Thread T2 may or may not invoke callback A and may or may not invoke
 callback B for this event instance, but if it invokes callback B, it must have invoked callback
 A for this event instance.
- Suppose thread T1 is registering a new callback D for event E. Thread T2 may or may not

invoke callback D for this event instance, but it must invoke both callbacks A and B. If it invokes callback D, that must follow the invocations of A and B.

• Suppose thread T1 is enabling callback C for event E. Thread T2 may or may not invoke callback C for this event instance, but it must invoke both callbacks A and B. If it invokes callback C, that must follow the invocations of A and B.

The **acc_prof_info** struct has a **thread_id** field, which the runtime must set to a unique value for each host thread, though it need not be the same as the OpenMP threadnum value.

3714 Library Support for Multiple Threads

The tool library must also be thread-safe. The callback routine will be invoked in the context of the thread that reaches the event. The library may receive a callback from a thread T2 while it's still processing a callback, from the same event type or from a different event type, from another thread T1. The **acc_prof_info** struct has a **thread_id** field, which the runtime must set to a unique value for each host thread.

If the tool library uses dynamic callback registration and unregistration, or callback disabling and 3720 enabling, recall that unregistering or disabling an event callback from one thread will unregister or 3721 disable that callback for all threads, and registering or enabling an event callback from any thread 3722 will register or enable it for all threads. If two or more threads register the same callback for the 3723 same event, the behavior is the same as if one thread registered that callback multiple times; see 3724 Section 5.4.1 Multiple Callbacks. The **acc_unregister** routine must be called as many times 3725 as **acc register** for that callback/event pair in order to totally unregister it. If two threads 3726 register two different callback routines for the same event, unless the order of the registration calls 3727 is guaranteed by some sychronization method, the order in which the runtime sees the registration 3728 may differ for multiple runs, meaning the order in which the callbacks occur will differ as well. 3729

3730 6. Glossary

Clear and consistent terminology is important in describing any programming model. We define here the terms you must understand in order to make effective use of this document and the associated programming model. In particular, some terms used in this specification conflict with their usage in the base language specifications. When there is potential confusion, the term will appear here.

Accelerator – a device attached to a CPU and to which the CPU can offload data and compute kernels to perform compute-intensive calculations.

Accelerator routine – a C or C++ function or Fortran subprogram compiled for the accelerator with the routine directive.

Accelerator thread – a thread of execution that executes on the accelerator; a single vector lane of a single worker of a single gang.

Aggregate datatype – an array or composite datatype, or any non-scalar datatype. In Fortran, aggregate datatypes include arrays and derived types. In C, aggregate datatypes include arrays, targets
of pointers, structs, and unions. In C++, aggregate datatypes include arrays, targets of pointers,
classes, structs, and unions.

3746 Aggregate variables – an array or composite variable, or a variable of any non-scalar datatype.

Async-argument – an *async-argument* is a nonnegative scalar integer expression (*int* for C or C++, *integer* for Fortran), or one of the special values acc_async_noval or acc_async_sync.

Barrier – a type of synchronization where all parallel execution units or threads must reach the barrier before any execution unit or thread is allowed to proceed beyond the barrier; modeled after the starting barrier on a horse race track.

Compute intensity – for a given loop, region, or program unit, the ratio of the number of arithmetic
 operations performed on computed data divided by the number of memory transfers required to
 move that data between two levels of a memory hierarchy.

3755 **Construct** – a directive and the associated statement, loop, or structured block, if any.

Composite datatype – a derived type in Fortran, or a **struct** or **union** type in C, or a **class**, **struct**, or **union** type in C++. (This is different from the use of the term *composite data type* in the C and C++ languages.)

3759 Composite variable – a variable of composite datatype. In Fortran, a composite variable must not
 3760 have allocatable or pointer attributes.

- 3761 **Compute construct** a *parallel construct*, *kernels construct*, or *serial construct*.
- 3762 **Compute region** a *parallel region*, *kernels region*, or *serial region*.

³⁷⁶³ **CUDA** – the CUDA environment from NVIDIA is a C-like programming environment used to ³⁷⁶⁴ explicitly control and program an NVIDIA GPU. ³⁷⁶⁵ **Current device** – the device represented by the *acc-current-device-type-var* and *acc-current-device-*³⁷⁶⁶ *num-var* ICVs

3767 Current device type – the device type represented by the acc-current-device-type-var ICV

Data lifetime – the lifetime of a data object in device memory, which may begin at the entry to a data region, or at an **enter data** directive, or at a data API call such as **acc_copyin** or **acc_create**, and which may end at the exit from a data region, or at an **exit data** directive, or at a data API call such as **acc_delete**, **acc_copyout**, or **acc_shutdown**, or at the end of the program execution.

Data region – a *region* defined by a **data** construct, or an implicit data region for a function or subroutine containing OpenACC directives. Data constructs typically allocate device memory and copy data from host to device memory upon entry, and copy data from device to local memory and deallocate device memory upon exit. Data regions may contain other data regions and compute regions.

3778 **Device** – a general reference to an accelerator or a multicore CPU.

- 3779 Default asynchronous queue the asynchronous activity queue represented in the *acc-default-* 3780 *async-var* ICV
- **Device memory** memory attached to a device, logically and physically separate from the host memory.
- **Device thread** a thread of execution that executes on any device.
- **Directive** in C or C++, a **#pragma**, or in Fortran, a specially formatted comment statement, that is interpreted by a compiler to augment information about or specify the behavior of the program.
- **Discrete memory** memory accessible from the local thread that is not accessible from the current device, or memory accessible from the current device that is not accessible from the local thread.

DMA – Direct Memory Access, a method to move data between physically separate memories; this is typically performed by a DMA engine, separate from the host CPU, that can access the host physical memory as well as an IO device or other physical memory.

- 3791 **GPU** a Graphics Processing Unit; one type of accelerator.
- 3792 **GPGPU** General Purpose computation on Graphics Processing Units.
- **Host** the main CPU that in this context may have one or more attached accelerators. The host CPU controls the program regions and data loaded into and executed on one or more devices.
- 3795 **Host thread** a thread of execution that executes on the host.
- **Implicit data region** the data region that is implicitly defined for a Fortran subprogram or C function. A call to a subprogram or function enters the implicit data region, and a return from the subprogram or function exits the implicit data region.
- **Kernel** a nested loop executed in parallel by the accelerator. Typically the loops are divided into a parallel domain, and the body of the loop becomes the body of the kernel.

Kernels region – a *region* defined by a kernels construct. A kernels region is a structured block
which is compiled for the accelerator. The code in the kernels region will be divided by the compiler
into a sequence of kernels; typically each loop nest will become a single kernel. A kernels region
may require space in device memory to be allocated and data to be copied from local memory to

device memory upon region entry, and data to be copied from device memory to local memory and space in device memory to be deallocated upon exit.

Level of parallelism – The possible levels of parallelism in OpenACC are gang, worker, vector, and sequential. One or more of gang, worker, and vector parallelism may appear on a loop construct. Sequential execution corresponds to no parallelism. The **gang**, **worker**, **vector**, and **seq** clauses specify the level of parallelism for a loop.

- **Local device** the device where the *local thread* executes.
- 3812 Local memory the memory associated with the *local thread*.
- **Local thread** the host thread or the accelerator thread that executes an OpenACC directive or
 construct.
- 3815 **Loop trip count** the number of times a particular loop executes.
- ³⁸¹⁶ **MIMD** a method of parallel execution (Multiple Instruction, Multiple Data) where different exe-³⁸¹⁷ cution units or threads execute different instruction streams asynchronously with each other.
- **OpenCL** short for Open Compute Language, a developing, portable standard C-like programming environment that enables low-level general-purpose programming on GPUs and other accelerators.
- **Orphaned loop construct** a **loop** construct that is not lexically contained in any compute construct, that is, that has no parent compute construct.
- Parallel region a *region* defined by a parallel construct. A parallel region is a structured block
 which is compiled for the accelerator. A parallel region typically contains one or more work-sharing
 loops. A parallel region may require space in device memory to be allocated and data to be copied
 from local memory to device memory upon region entry, and data to be copied from device memory
 to local memory and space in device memory to be deallocated upon exit.
- Parent compute construct for a loop construct, the parallel, kernels, or serial con struct that lexically contains the loop construct and is the innermost compute construct that con tains that loop construct, if any.
- Present data data for which the sum of the structured and dynamic reference counters is greater
 than zero.
- **Private data** with respect to an iterative loop, data which is used only during a particular loop iteration. With respect to a more general region of code, data which is used within the region but is not initialized prior to the region and is re-initialized prior to any use after the region.
- ³⁸³⁵ **Procedure** in C or C++, a function in the program; in Fortran, a subroutine or function.
- **Region** all the code encountered during an instance of execution of a construct. A region includes any code in called routines, and may be thought of as the dynamic extent of a construct. This may be a *parallel region*, *kernels region*, *serial region*, *data region* or *implicit data region*.
- 3839 Scalar a variable of scalar datatype. In Fortran, scalars must not have allocatable or pointer
 3840 attributes.
- **Scalar datatype** an intrinsic or built-in datatype that is not an array or aggregate datatype. In Fortran, scalar datatypes are integer, real, double precision, complex, or logical. In C, scalar datatypes are char (signed or unsigned), int (signed or unsigned, with optional short, long or long long attribute), enum, float, double, long double, _Complex (with optional float or long attribute), or any pointer datatype. In C++, scalar datatypes are char (signed or unsigned), wchar_t, int (signed or

³⁸⁴⁶ unsigned, with optional short, long or long long attribute), enum, bool, float, double, long double, ³⁸⁴⁷ or any pointer datatype. Not all implementations or targets will support all of these datatypes.

Serial region – a *region* defined by a **serial** construct. A serial region is a structured block which is compiled for the accelerator. A serial region contains code that is executed by one vector lane of one worker in one gang. A serial region may require space in device memory to be allocated and data to be copied from local memory to device memory upon region entry, and data to be copied from device memory to local memory and space in device memory to be deallocated upon exit.

- 3853 Shared memory memory that is accessible from both the local thread and the current device.
- SIMD A method of parallel execution (single-instruction, multiple-data) where the same instruc tion is applied to multiple data elements simultaneously.
- 3856 **SIMD operation** a *vector operation* implemented with SIMD instructions.

Structured block – in C or C++, an executable statement, possibly compound, with a single entry
at the top and a single exit at the bottom. In Fortran, a block of executable statements with a single
entry at the top and a single exit at the bottom.

- Thread On a host CPU, a thread is defined by a program counter and stack location; several host
 threads may comprise a process and share host memory. On an accelerator, a thread is any one
 vector lane of one worker of one gang.
- *var* the name of a variable (scalar, array, or composite variable), or a subarray specification, or an
 array element, or a composite variable member, or the name of a Fortran common block between
 slashes.
- Vector operation a single operation or sequence of operations applied uniformly to each element
 of an array.
- **Visible device copy** a copy of a variable, array, or subarray allocated in device memory that is visible to the program unit being compiled.

A. Recommendations for Implementors

This section gives recommendations for standard names and extensions to use for implementations for specific targets and target platforms, to promote portability across such implementations, and recommended options that programmers find useful. While this appendix is not part of the Open-ACC specification, implementations that provide the functionality specified herein are strongly recommended to use the names in this section. The first subsection describes devices, such as NVIDIA GPUs. The second subsection describes additional API routines for target platforms, such as CUDA and OpenCL. The third subsection lists several recommended options for implementations.

3878 A.1. Target Devices

3879 A.1.1. NVIDIA GPU Targets

³⁸⁸⁰ This section gives recommendations for implementations that target NVIDIA GPU devices.

3881 Accelerator Device Type

These implementations should use the name **acc_device_nvidia** for the **acc_device_t** type or return values from OpenACC Runtime API routines.

3884 ACC_DEVICE_TYPE

An implementation should use the case-insensitive name **nvidia** for the environment variable **ACC_DEVICE_TYPE**.

3887 device_type clause argument

An implementation should use the case-insensitive name **nvidia** as the argument to the **device_type** clause.

3890 A.1.2. AMD GPU Targets

³⁸⁹¹ This section gives recommendations for implementations that target AMD GPUs.

3892 Accelerator Device Type

These implementations should use the name **acc_device_radeon** for the **acc_device_t** type or return values from OpenACC Runtime API routines.

3895 ACC_DEVICE_TYPE

These implementations should use the case-insensitive name **radeon** for the environment variable **ACC_DEVICE_TYPE**.

3898 device_type clause argument

An implementation should use the case-insensitive name **radeon** as the argument to the **device_type** clause.

3901 A.1.3. Multicore Host CPU Target

³⁹⁰² This section gives recommendations for implementations that target the multicore host CPU.

3903 Accelerator Device Type

These implementations should use the name **acc_device_host** for the **acc_device_t** type or return values from OpenACC Runtime API routines.

3906 ACC_DEVICE_TYPE

These implementations should use the case-insensitive name **host** for the environment variable **ACC_DEVICE_TYPE**.

device_type clause argument

An implementation should use the case-insensitive name **host** as the argument to the **device_type** clause.

3912 A.2. API Routines for Target Platforms

These runtime routines allow access to the interface between the OpenACC runtime API and the underlying target platform. An implementation may not implement all these routines, but if it provides this functionality, it should use these function names.

3916 A.2.1. NVIDIA CUDA Platform

³⁹¹⁷ This section gives runtime API routines for implementations that target the NVIDIA CUDA Run-³⁹¹⁸ time or Driver API.

3919 acc_get_current_cuda_device

Summary The acc_get_current_cuda_device routine returns the NVIDIA CUDA device handle for the current device.

3922 Format

C or C++: void* acc_get_current_cuda_device ();

3923 acc_get_current_cuda_context

Summary The acc_get_current_cuda_context routine returns the NVIDIA CUDA context handle in use for the current device.

3926 Format

```
C or C++:
    void* acc_get_current_cuda_context ();
```

3927 acc_get_cuda_stream

Summary The acc_get_cuda_stream routine returns the NVIDIA CUDA stream handle in
use for the current device for the asynchronous activity queue associated with the async argument.
This argument must be an *async-argument* as defined in Section 2.16.1 async clause.

3931 Format

```
C or C++:
    void* acc_get_cuda_stream ( int async );
```

3932 acc_set_cuda_stream

Summary The acc_set_cuda_stream routine sets the NVIDIA CUDA stream handle the current device for the asynchronous activity queue associated with the **async** argument. This argument must be an *async-argument* as defined in Section 2.16.1 async clause.

3936 Format

C or C++:

void acc_set_cuda_stream (int async, void* stream);

3937 A.2.2. OpenCL Target Platform

This section gives runtime API routines for implementations that target the OpenCL API on any device.

3940 acc_get_current_opencl_device

Summary The acc_get_current_opencl_device routine returns the OpenCL device handle for the current device.

3943 Format

C or C++:

void* acc_get_current_opencl_device ();

3944 acc_get_current_opencl_context

Summary The acc_get_current_opencl_context routine returns the OpenCL context handle in use for the current device.

3947 Format

C or C++:
 void* acc_get_current_opencl_context ();

3948 acc_get_opencl_queue

Summary The acc_get_opencl_queue routine returns the OpenCL command queue handle in use for the current device for the asynchronous activity queue associated with the **async** argument. This argument must be an *async-argument* as defined in Section 2.16.1 async clause.

3952 Format

C or C++:

cl_command_queue acc_get_opencl_queue (int async);
3953 acc_set_opencl_queue

Summary The acc_set_opencl_queue routine returns the OpenCL command queue handle in use for the current device for the asynchronous activity queue associated with the **async** argument. This argument must be an *async-argument* as defined in Section 2.16.1 async clause.

3957 Format

```
C or C++:
```

```
void acc_set_opencl_queue ( int async, cl_command_queue cmdqueue );
```

A.3. Recommended Options

The following options are recommended for implementations; for instance, these may be implemented as command-line options to a compiler or settings in an IDE.

3961 A.3.1. C Pointer in Present clause

³⁹⁶² This revision of OpenACC clarifies the construct:

```
void test(int n){
float* p;
...
#pragma acc data present(p)
{
    // code here...
}
```

This example tests whether the pointer \mathbf{p} itself is present in the current device memory. Implementations before this revision commonly implemented this by testing whether the pointer target $\mathbf{p}[0]$ was present in the current device memory, and this appears in many programs assuming such. Until such programs are modified to comply with this revision, an option to implement **present** (**p**) as **present** (**p**[0]) for C pointers may be helpful to users.

3968 A.3.2. Autoscoping

If an implementation implements autoscoping to automatically determine variables that are private to a compute region or to a loop, or to recognize reductions in a compute region or a loop, an option to print a message telling what variables were affected by the analysis would be helpful to users. An option to disable the autoscoping analysis would be helpful to promote program portability across implementations.

Index

_**OPENACC**, 16–20, 24, 121 3974 acc-current-device-num-var, 24 3975 acc-current-device-type-var, 24 3976 acc-default-async-var, 24, 81 3977 acc_async_noval, 16, 81 3978 acc_async_sync, 16, 81 3979 acc_device_host, 142 3980 **ACC_DEVICE_NUM**, 25, 113 3981 acc_device_nvidia, 141 3982 acc_device_radeon, 142 3983 ACC_DEVICE_TYPE, 25, 113, 141, 142 3984 ACC PROFLIB, 113 3985 action 3986 attach, 41, 45 3987 copyin, 44 3988 3989 copyout, 44 create, 44 3990 delete, 45 3991 detach, 41, 45 3992 immediate, 46 3993 present decrement, 44 3994 present increment, 43 3995 AMD GPU target, 141 3996 **async** clause, 40, 76, 80 3997 async queue, 11 3998 async-argument, 81 3999 asynchronous execution, 11, 80 4000 atomic construct, 16, 63 4001 attach action, 41, 45 4002 attach clause, 50 4003 attachment counter, 41 4004 auto clause, 16, 55 4005 autoscoping, 145 4006 barrier synchronization, 11, 28, 29, 31, 137 4007 bind clause, 79 4008 cache directive, 61 4009 capture clause, 67 4010 collapse clause, 53 4011

common block, 41, 68, 70, 80 4012 compute construct, 137 4013 compute region, 137 4014 construct, 137 4015 atomic, 63 4016 4017 compute, 137 data, 37, 41 4018 host_data, 51 4019 kernels, 28, 41 4020 kernels loop, 62 4021 parallel, 27, 41 4022 parallel loop, 62 4023 serial, 30, 41 4024 serial loop, 62 4025 copy clause, 47 4026 copyin action, 44 4027 copyin clause, 47 4028 copyout action, 44 4029 copyout clause, 48 4030 create action, 44 4031 create clause, 49, 69 4032 CUDA, 11, 12, 137, 141, 143 4033 data attribute 4034 explicitly determined, 35 4035 implicitly determined, 35 4036 predetermined, 35 4037 data clause, 41 4038 data construct, 37, 41 4039 data lifetime, 138 4040 data region, 36, 138 4041 implicit, 36 4042 declare directive, 16, 67 4043 default clause, 34 4044 default (none) clause, 16, 28, 29, 31 4045 default(present), 28, 29, 31 4046 delete action, 45 4047 delete clause, 50 4048 detach action, 41, 45 4049 immediate, 46 4050

detach clause, 51 4051 device clause, 75 4052 device_resident clause, 69 4053 device_type clause, 25 4054 device_type clause, 16, 41, 141, 142 4055 deviceptr clause, 41, 46 4056 direct memory access, 11, 138 4057 DMA, 11, 138 4058 enter data directive, 38, 41 4059 environment variable 4060 OPENACC, 24 4061 **ACC_DEVICE_NUM**, 25, 113 4062 **ACC_DEVICE_TYPE**, 25, 113, 141, 142 4063 ACC PROFLIB, 113 4064 exit data directive, 38, 41 4065 explicitly determined data attribute, 35 4066 firstprivate clause, 28, 31, 33 4067 gang, 27, 31 4068 gang clause, 54, 78 4069 gang parallelism, 10 4070 gang-arg, 53 4071 gang-partitioned mode, 10 4072 gang-redundant mode, 10, 27, 31 4073 GP mode, 10 4074 GR mode, 10 4075 host. 142 4076 **host** clause, 16, 75 4077 host_data construct, 51 4078 ICV, 24 4079 if clause, 38, 39, 71, 73, 74, 76, 83 4080 immediate detach action, 46 4081 implicit data region, 36 4082 implicitly determined data attribute, 35 4083 independent clause, 56 4084 init directive. 71 4085 internal control variable, 24 4086 kernels construct, 28, 41 4087 kernels loop construct, 62 4088 level of parallelism, 10, 139 4089 link clause, 16, 41, 70 4090 local device, 11 4091 local memory, 11 4092 local thread, 11 4093

```
loop construct, 52
4094
         orphaned, 53
4095
     no\ create clause, 49
4096
     nohost clause, 80
4097
     num_gangs clause, 32
4098
     num_workers clause, 32
4099
     nvidia. 141
4100
     NVIDIA GPU target, 141
4101
     OpenCL, 11, 12, 139, 141, 144
4102
     orphaned loop construct, 53
4103
     parallel construct, 27, 41
4104
     parallel loop construct, 62
4105
     parallelism
4106
          level, 10, 139
4107
     parent compute construct, 53
4108
     predetermined data attribute, 35
4109
     present clause, 41, 46
4110
     present decrement action, 44
4111
     present increment action, 43
4112
     private clause, 33, 57
4113
     radeon, 142
4114
     read clause, 67
4115
     reduction clause, 33, 57
4116
     reference counter, 40
4117
     region
4118
          compute, 137
4119
          data, 36, 138
4120
          implicit data, 36
4121
     routine directive, 16, 77
4122
     self clause, 16, 75
4123
     sentinel, 23
4124
     seq clause, 55, 79
4125
     serial construct, 30, 41
4126
     serial loop construct, 62
4127
     shutdown directive, 72
4128
     size-expr, 53
4129
     thread, 140
4130
     tile clause, 16, 56
4131
     update clause, 67
4132
     update directive, 74
4133
4134
     use_device clause, 52
     vector clause, 55, 79
4135
```

vector lane, 27 4136 vector parallelism, 10 4137 vector-partitioned mode, 10 4138 vector-single mode, 10 4139 vector_length clause, 33 4140 visible device copy, 140 4141 VP mode, 10 4142 VS mode, 10 4143 wait clause, 40, 76, 81 4144 wait directive, 82 4145 worker, 27, 31 4146 worker clause, 54, 78 4147 worker parallelism, 10 4148 worker-partitioned mode, 10 4149 worker-single mode, 10 4150 WP mode, 10 4151 WS mode, 10 4152